

COURSE INFORMATION

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1.	Name of Course													Paral	lel Pro	cessi	ng					
2 .	Course Code													TSN3151								
3 .	ype of Course e.g. : Core, major, elective etc.)													Elective								
4 .	Synopsis														This course teaches the students how to write parallel computer programmes using various diffent techniques.							
5 .	Version (State the date of theSenate's approval - previous and the current approval date)														Current: January 2018 Previous: June 2016							
	Name(s) of Academic Staff											lan Chai, lan Tan, Soo Wooi King Trimester 2 (Delta Level)										
8.	Semester and Year Offered Credit Value												3									
	Pre-Requisite Objective of the course in the programme: The objective of the subject is to provide the knowledge of parallel processing concepts, parallel environn												TCP1101 Programming Fundamentals ironments and architectures, parallel algorithms and parallel programming.									
11 .	Justification for including t This course will enable studer and requirements of the curre	nts to	acquii	re the	requir	ed kno	wledg			for pa	rallel p	roces	sing	that er	nable t	hem t	o prov	ide relevant soluti	ions which will me	et the unique challenges		
12 .	Course Learning Outcomes	s (CLC	D)											l			omai	n		Level		
	CLO1: Describe differen	t types	s of pa	arallelis	sm, th	eir prir	ciples	and structures					Cognitive					2				
	CLO2: Comprehend the	princi	ples, t	echnic	ues, a	and pra	actice	s relevant to the design and											_			
	implementation of	implementation of parallel computing systems											Cognitive Cognitive					3				
13 .		apping of the Course Learning Outcomes to the Programme Learning Outcomes, To									Teac	hing										
	Course Learning Programme Learning Outcomes (CLO) (Must tally with CLOs in						ng Outcomes (PLO)						Teaching Methods				ethods	Assessment Method				
	item 12)	P L O	P L O	P L O	P L O	P L O	P L O	P L O	P L O	P L O	L 0 1	L 0 1	L 0 1									
		1	2	3	4	5	6	7	8	9	0	1	2									
	CLO1 CLO2	✓ ✓	✓	✓											re/Pra				Mid-Term Test/Fi Mid-Term Test/Fi			
	CLO3	✓	✓	✓											re/Pra				Assignment			
	CLO4 Total	3	3	2										Indicate the relevancy between the CLO and PLO by ticking "✓" the appropriate relev (This description must be read together with standards 2.1.2, 2.2.1, and 2.2.2 in Area pages 16 & 18 of COPPA 2.0)								
14 .	Transferable Skills: Ability to write parallel program	ms, bo	oth for	comp	utation	n and t	o prod	cess d	lata.	1			<u> </u>	, °				,				
15 .	Distribution of Student Lea	rning	Time	(SLT))																	
	Course Content Outline											Teaching and Learning Activities				Guided	Independent					
							**CLO						Guided Learning				Learning	Learning	Total SLT			
													1	(F2F) *L *T *P *O		(NF2F)*	(NF2F)*					
														-	-	F	0					
	Introduction to Parallel Computing Motivations for parallelism, scope of parallel computing, parallel paradigms, parallel programming environments, job- and task-level parallelism, physical organization of parallel platforms and the relevant communication methods.						1,2					4		4			8	16				
	Analytical Modeling of Parallel Programs Basics of message passing programming, performance metrics for parallel systems (execution time, overhead, speedup, efficiency, cost, etc), analytical evaluation of communication operations and parallel programs.						1,2						2		2			4	8			
	Message Passing Paradigms Message Passing Interface (MPI), introduction on Parallel Virtual Machine (PVM), sample programs.						1,2,3					2		4		4	6	16				
	Partitioning and divide-	Parallel Algorithm Design Partitioning and divide-and-conquer strategies, pipelined computations, embarrassingly parallel computations and sample applications						1,2						6		8		4	14	32		
	Synchronization 5 Barriers, synchronized computations, local synchronization, data-parallelism and sample applications						1,2				2		2			4	8					

Parallel Processing on Shared Memory Shared memory multiprocessors and chip-level multiprocessor (CMP or multi-core), concurrent process creation (heavyweight process and threads), shared data access, shared memory synchronization (lock, barrier, semaphores, deadlock), cache coherence protocols.	1,2	2		2			4	8					
Shared Memory Programming Creating and Maintaining shared memory parallelization with threads and OpenMP.	1,2,3	4		6		4	10	24					
						l.	Total SLT	112					
SUMMATIVE ASSESSMENT 1. Continuous Assessment Percentage % Total SLT													
1. Continuous Assessment Mid-Term Test				Per	30%		Total SLT 8						
Assignment				30%		8 18							
Assignment					30 /6		10						
		Total	SLT f	or Co	ntinu	ous Assessment		26					
2. Final Assessment				Por	centag	% or	Total SLT						
				1 610		Je 70	F2F	ILT					
Final Exam					40%		2	20					
	<u>Tota</u>	SLT fo	r Fina	al Ass	essm	ent (F2F + NF2F)		22					
Grand Total					100%			160					
**Indicate the CLO based on the CLO's numbering in Item 12													
*L= Lecture, *T= Tutorial, *P= Practical, *O= Others, F2F*= Fa		Face											
	,												
Identify Special Requirement to Deliver the Course (e.g., software, nursery, computer lab, simulation room): Computer Lab													
Conjuder Lau Main References:													
Robert Cook, (2011), An Introduction to Parallel Programming with OpenMP, PThreads and MPI, Cook's													
dditional References:													
1. Harry F. Jordan & Gita Alaghband, (2003), Fundamentals of Parallel Processing", Prentice Hall.													
2. Peter Pacheco, (2011), Introduction to Parallel Computing (2nd Edition)", Morgan Kaufmann. 3. Books Barry Wilkinson and William Allen, (2004), Parallel Programming: Techniques and Applications Using Networked Workstations and Parallel Computers (2nd Edition), Prentice													
 Books Barry Wilkinson and William Allen, (2004), Parallel Prog 	ramming: Techniques and Applic	ations U	sıng N	letwor	ked W	orkstations and Pa	arallel Computers	(2nd Edition), Prentice					

Note:

Cells shaded light grey contain formulas / fixed values. Edit these formulas only if needed.