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EE213M
Digital circuits

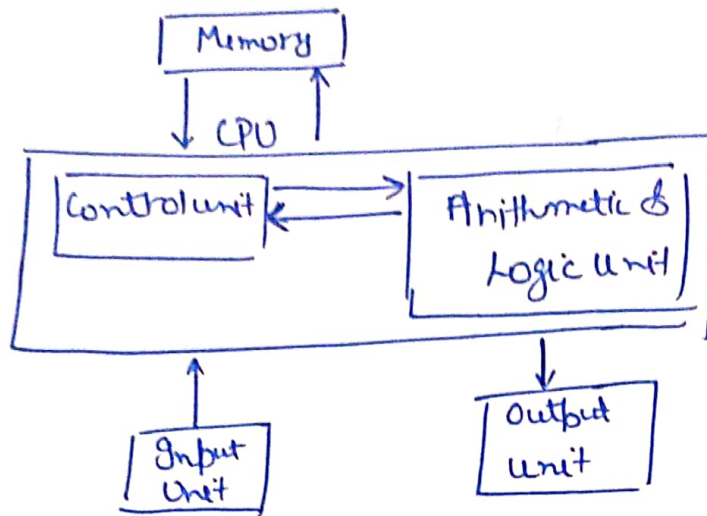
Notes Assignment - 1

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Background and number systems

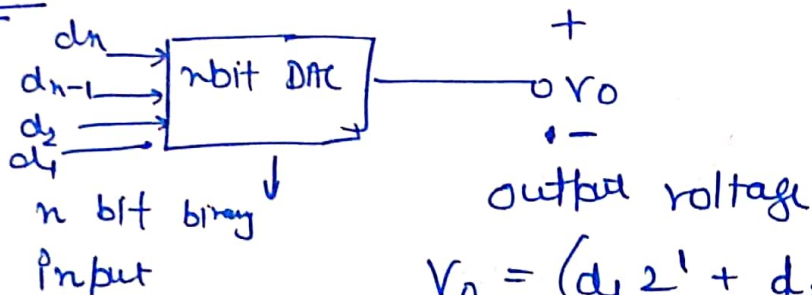


Block Diagram of a Generic Digital Computer:-



Digital systems :- Most real life systems are analog. They are converted to digital for storing/processing using ADCs (Analog to digital converters) & reconverted back to analog using DACs (Digital to analog converters) for further use.

Eg DAC :-



$$V_o = (d_1 2^1 + d_2 2^2 + \dots + d_n 2^n) V_{ref}$$

Smallest possible voltage change by DAC = $(2^{-n} V_{ref})$

Either Analog input devices (photodiode / tachometer / thermistors) could be used in embedded systems or digital input devices could be used. Analog would require an ADC.

Embedded System

egs. Microwave oven, digital camera

Smaller computer / micro computers which are more prevalent, less powerful & only make parts of products (enclosed within them) are called embedded systems

Pre-defined software functions specific to the product called embedded software

we do not interact with these systems or limited interaction with it

→ output devices can be (LED displays, relays, stepper motor etc.)

Number systems →

Binary → Base 2 → (Polynomial in power of 10)

Octal → Base 8

Hexa-decimal → Base 16

Decimal → Base 10

Conversion :-

$$(312.4)_5 = 3 \times 5^2 + 1 \times 5^1 + 2 \times 5^0 + 4 \times 5^{-1} = (82.8)_{10}$$

$$(11010)_2 = 1 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 1 \times 2^1 + 0 \times 2^0 = (26)_{10}$$

$$\rightarrow 2^{10} = \text{Kilo}$$

$$2^{20} = \text{Mega}$$

$$2^{30} = \text{Giga}$$

$$2^{40} = \text{Tera}$$

$$\rightarrow (625)_{10} = (?)_2$$

\rightarrow Subtract highest power of 2 from table until it becomes 0.

$$625 - 512 = 113 = N_1$$

$$512 = 2^9$$

$$113 - 64 = 49 = N_2$$

$$64 = 2^6$$

$$49 - 32 = 17 = N_3$$

$$32 = 2^5$$

$$17 - 16 = 1 = N_4$$

$$16 = 2^4$$

$$1 - 1 = 0 = N_5$$

$$1 = 2^0$$

$$\therefore (625)_{10} = (1001110001)_2$$

Decimal to any other Radix :-

Eg. 53 to binary

$$2 \overline{) 53}$$

$$2 \overline{) 26}$$

$$2 \overline{) 13}$$

$$2 \overline{) 6}$$

$$2 \overline{) 3}$$

$$2 \overline{) 1}$$

$$0$$

$$\text{rem} = 1 = a_0$$

$$\text{rem} = 0 = a_1$$

$$\text{rem} = 1 = a_2$$

$$\text{rem} = 0 = a_3$$

$$\text{rem} = 1 = a_4$$

$$\text{rem} = 1 = a_5$$

$$\therefore 53_{10} = 110101_2$$

Eg. 0.7 to binary

$$0.7$$

$$\begin{array}{r} 2 \\ \hline (1).4 \end{array}$$

$$\begin{array}{r} 2 \\ \hline (0).8 \end{array}$$

$$\begin{array}{r} 2 \\ \hline (1).6 \end{array}$$

$$\begin{array}{r} 2 \\ \hline (1).2 \end{array}$$

$$\begin{array}{r} 2 \\ \hline (1).1 \end{array}$$

repeat

$$\therefore 0.7_{10} = (0.1011001100110...)_2$$

Lecture - 2

N bits can represent values from 0 to $(2^n - 1)$

Method → Decimal to Binary (Power of 2s method)

Eg. $(625)_{10} = (???)_2$

512	256	128	64	32	16	8	4	2	1
1	0	0	1	1	1	0	0	0	1

$$625 - 512 = 113$$

$$113 - 64 = 49$$

$$49 - 32 = 17$$

$$17 - 16 = 1$$

$$1 - 1 = 0$$

$$(625)_{10} = (1001110001)_2$$

fill one's for powers ^{we} used 0 for others

Octal system → Radix = $8 = 2^3$ 3 bit binary = 1 octal no.

Binary to Octal conversion :- Integer → LSB to MSB, 3 bits group.
Fraction → MSB to LSB, 3 bits groups

Eg. $1.(1111011001)_2 = \frac{1}{1} \frac{111}{7} \frac{011}{3} \frac{001}{1} = (1731)_8$

Octal to Binary → Each digit is represented by its 3 digit binary value.

Hexadecimal system → Radix = $16 = 2^4$

↓
 0 1 - - - 9 A B C D E F → Every 4 bits binary no. → 1 hexadecimal.

Binary to Hexadecimal \rightarrow

$$\begin{array}{ccccccc} (0010 & 1100 & 0110 & 1011 & 1111 & 0000 & 0110)_2 \\ (2 & C & 6 & B & F & 0 & 6)_{16} \end{array}$$

Octal to Hex :-

1. Use binary as intermediate

$$\text{eg. } (3541)_8 = ??_{16}$$

$$\rightarrow \begin{array}{cccc} 011 & 101 & 100 & 001 \end{array}$$

$$0111 \ 0110 \ 0001 = \boxed{761}_{16}$$

Hex to Octal

$$\text{eg: } 1FOA_{16} = ??_{10}$$

$$0 \ 001 \ 1111 \ 0000 \ 1010 \ 1010 \ 1000$$

$$\begin{array}{ccccccc} 0001 & 111 & 100 & 001 & 010 & 101 & 010 & 000 \\ 1 & 7 & 4 & 1 & 2 & 5 & 2 & 0 \end{array}$$

$$= (17412.52)_8$$

Main use of octal/hexadecimal \rightarrow to represent binary nos compactly

No. of bits	Term
1 bit	Bit
4	Nibble
8	Byte
16/32/64	words

Ranges

1. Unsigned Nos \rightarrow 16 bit unsigned integer range \rightarrow
 n bits $\rightarrow 0$ to $(2^n - 1)$

$$\therefore 16 \rightarrow 0 \text{ to } 65,535$$

16 bit unsigned fractions

$$\text{Max. fraction value} = 0.11111111 \dots$$

$$\therefore S_n = \left(\frac{1}{2} + \frac{1}{4} + \frac{1}{8} + \dots + \frac{1}{2^{n-1}} \right) + \frac{1}{2^n}$$

$$2S_n = \left(1 + \frac{2}{4} + \frac{2}{8} + \dots \right) + 1 + \left(\frac{1}{2} + \frac{1}{8} + \dots \right)$$

$$= 1 + \left(S_n + \frac{1}{2^n} \right)$$

$$S_n = 1 - \frac{1}{2^n} = \frac{2^n - 1}{2^n} \rightarrow \text{Fraction values range upper limit.}$$

$$\therefore 16 \rightarrow 0.0 \text{ to } 65,535 / 65,536$$

$$16 \rightarrow 0.0 \text{ to } 0.999847412$$

Arithmetic operations :-

* Binary addition :-

Carries	00000
Augend	01100
Addend	+10001
Sum	<u>11101</u>

01100
10110
10110
<u>101101</u>

* Binary Subtraction :-

Borrow	00000
	10110
Minuend	-10010
Subtrahend	<u>00100</u>
Difference	<u>00100</u>

00110
10110
-10011
<u>00011</u>

Eg. Subtrahend > Minuend \rightarrow reverse no. & add +ve sign.

$$\begin{array}{r} \text{Eg. } 10011 \\ - 11110 \\ \hline -01011 \end{array} \Rightarrow \begin{array}{r} 10110 \\ 11110 \\ - 10011 \\ \hline \xleftarrow{+ve} 01011 \end{array}$$

Binary Multiplication:-

$$\begin{array}{r} \text{Multiplicand } (1011) \\ \times \text{ Multiplier } (101) \\ \hline 1011 \\ +0000 \\ +1011 \\ \hline 110111 \end{array}$$

\rightarrow For base b arithmetic operations, convert to decimal, do the operation & convert all sum & carry to corresponding base b .

Hexadecimal addition:-

$$(59F)_{16} + (E46)_{16}$$

$$\begin{array}{r} \text{Carry} = 1 \quad \quad \quad \text{Carry} = 1 \\ 1 \leftarrow \begin{array}{r} 5 \\ 9 \\ 14 \end{array} \quad \begin{array}{r} 9 \\ 4 \\ 6 \end{array} \quad \begin{array}{r} 15 \\ 6 \end{array} \\ \hline 19 = 16 + 3 \quad 14 \quad 21 = 16 + 5 \\ \text{Multiplier of 16} \end{array}$$

$$\therefore (13E5)_{16}$$

Octal Multiplication :-

$$(762)_8 \times (45)_8 = (45772)_8$$

$$\begin{array}{r} 762 \\ \times 45 \\ \hline 3710x \\ 43772 \\ \hline \end{array}$$

$$11 \rightarrow 8+3$$

$$13$$

octal
5x2Decimal \rightarrow
 $10 = 8+2$ multiple plus 8
from
octal

$$5 \times 6 + 1 = 31 = 24 + 7$$

$$5 \times 7 + 3 = 38 = 32 + 6$$

$$4 \times 2 = 8$$

$$4 \times 6 + 1 = 25 = 24 + 1$$

$$4 \times 7 + 3 = 31 = 24 + 7$$

(12)

Binary Long division :-

Eg: Divisor

11001

quotient
10110

1000100110

dividend

11001

100101

11001

11001

11001

000000 Remainder

To verify \rightarrow convert to decimal divide convert back.

Lecture 83.

$$x^2 + 21x + 104 = 0$$

Solns are 5 & 8. What is base?

$$(x-5)(x-8) = x^2 - (5+8)x + 5 \times 8$$

$$13 = 5+8 = 2 \times 6^1 + 1 \times 6^0 = 6^2 - 6$$

$$5 \times 8 = 40 = (104)_6$$

$$\Rightarrow 4 \times 6^0 + 0 \times 6^1 + 1 \times 6^2$$

Range for signed nos :- Out of n bits, 1 is reserved for sign, $n-1$ for storing nos.

$$\therefore \text{signed nos. range} = \underline{-(2^{n-1}-1) \text{ to } (2^{n-1}-1)}$$

3 ways to represent -ve nos :-

1. sign is method
 2. 1's complement
 3. 2's complement
- Range $\rightarrow -(2^{n-1}-1)$ to $+(2^{n-1}-1)$
 Zero can be represented as -0 & +0
 Range is $\rightarrow -2^{n-1}$ to $(2^{n-1}-1)$
 only (+0)

Left most bit \rightarrow 0 then +ve value
 1 then -ve value

Overflow :- when result falls outside the specified range

1. Sign & Magnitude representation :-

Range : $-(2^{n-1}-1)$ to $+(2^{n-1}-1)$

Eg : $n=4$

Positive
 0000 (+0)
 0001 (+1)
 0010 (+2)
 ⋮

Negative
 1000 (-0)
 1001 (-1)
 1010 (-2)
 ⋮

Draw back: \rightarrow $(+)$ & $(-)$ representation is un-necessary & thus this method is not the best

1's complement \rightarrow

Negative no, denoted by 1's complement of N , denoted by N^0

$$N^0 = (2^n - 1) - N$$

1's complement of N is obtained by complement N bit - by bit

Eg. $n = 4$

Positive	Negative
0000	1111
0001	1110
0010	1101
0011	1100
0100	1011
0101	1010
0110	1001
0111	1000

$$(16-1) - 0 = 15 = 1111$$

$$(16-2) - 1 = 14 = 1110$$

Advantage :-

1. Subtraction can be done using addition \rightarrow

$$A - B = A + B^0$$

\rightarrow if no carry, -ve no. (in 1's complement form)
 \rightarrow if carry, +ve no. This is called end-around carry

\hookrightarrow add carry back to $A+B^0$.

Eg. $A = 2$; $B = 6$

$$\begin{array}{r}
 A+B \quad 0010 \\
 + \quad 1001 \\
 \hline
 1011 \\
 \hline
 100 = \boxed{-4}
 \end{array}$$

2's Complement →

→ positive integer $+N$ by 0 followed by magnitude N .
 Negative integer $-N$ represented by 2's complement i.e. N^* .

$$N^* = 2^n - N$$

i.e. 1's Complement + 1

<u>+ve</u>	<u>-ve</u>
0000 (+0)	—
0001 (+1)	1111 (-1)
0010 (+2)	1110 (-2)
0011 (+3)	1101 (-3)
1	1

$$\begin{array}{r}
 0010 (+2) \\
 \downarrow \\
 1101 \\
 + 1 \\
 \hline
 1110 (-2)
 \end{array}$$

Note : $N^* = (2^n - N)$ directly on Binary nos?

eg $n=7 \rightarrow$ largest integer $(2^7 - 1) = 1111111$
 ↪ For 2^7 , 8 bits necessary.

∴ To avoid this, 2^n is rewritten as $\rightarrow N^* = (2^n - 1 - N) + 1$

eg. $N = 0101100 \quad 2^n - 1 = 1111111$
 $\quad \quad \quad \quad \quad - 0101100$

$$\begin{array}{r}
 2^n - 1 - N = 1010011 \\
 + 0000001 \\
 \hline
 N^* = 1010100
 \end{array}$$

Easier way :- Leaving all zeros from right of the first one, complement all other digits of N bit by bit eg. $N = 0101100$

$$N^* = 1010100$$

NOTE: (a) Weighted no. representation of 2's complement, but with MSB having weight -2^{n-1}

Eg:- 1101 \longrightarrow -ve no. (-3) in 2's complement
 $-1 \times 2^3 + 1 \times 2^2 + 0 \times 2^1 + 1 \times 2^0 = -8 + 5 = -3$

(b) Shift left by k positions with 0 padding, multiplies the number by $2^k \rightarrow$

Eg:-
 $00010011 = +19$
 \downarrow shift left by 1 position $\downarrow \times 2^1$
 $00100110 = +38$
 $\downarrow \times 2^2$
 $01001100 = +76$

(c) Shift ~~no~~ right by k positions with zero padding divides no. by 2^k (one padding for -ve nos)

Eg:-
 $00110100 = +52$
 $00011010 = +26$
 $00001101 = +13$
 $00000110 = +6$
 $\downarrow \frac{52}{2^1}$
 $\downarrow \frac{52}{2^2}$
 $\downarrow \frac{52}{2^3}$

(d) Sign bit copied as many times required in beginning to extend size of no \rightarrow AKA sign extension

Eg:-
 8 bit \longrightarrow N = 00110000 = +48
 32 bit \longrightarrow N = 00000000 00000000 00000000 00110000
 as 1 instead of 0, 1 is copied 2^n times.

Subtraction using 2's Complement :-A. No overflow →1. $C = A - B$ obtain $A+B^*$

2. If carry is 1, ignore it → result is +ve no

3. Else result is -ve no in 2's complement from inc.

$A = 3 \quad B = 5$

$\therefore A+B^*$

$$\begin{array}{r}
 0011 \\
 1011 \\
 \hline
 1110 = -8 + 4 + 2 + 0 = \textcircled{-2}
 \end{array}$$

$A = 5, B = 3$

$A+B^* = 0101$

$$\begin{array}{r}
 0101 \\
 +1101 \\
 \hline
 10010
 \end{array}$$

ignore +②

B. Overflow →

↓ In 4 bit representation, range is +7 to -8

can occur when → 1. sign of 2 no.s is same
 2. sign of sum is diffⁿ from
 3. sign of either of no.s

eg. $A = -6, B = -3$

$$\begin{array}{r}
 1010 \\
 1101 \\
 \hline
 \end{array}$$

$A+B = 1011 = \boxed{+ve \text{ No.}} \quad \text{wrong ans}$

eg

$$\begin{array}{r}
 +5 \\
 +6 \\
 \hline
 +11
 \end{array}$$

$$\begin{array}{r}
 0101 \\
 +0110 \\
 \hline
 1011 = \textcircled{-5}
 \end{array}$$

$$\begin{array}{r}
 +4 \quad 0111 \\
 +7 \quad 0111 \\
 \hline
 +14 \quad 1110 = \textcircled{-2}
 \end{array}$$