Time		1 lts
clock=1		
i[31:0]=4	(O )(X(X)	0.000000000000000000000000000000000000
read=0	]	
read_data[1:0]=XXX	XXX	]\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
read_done=0		
read_en=0		
reset_n=1		
sram_clock=1		
	(0	<u>X1</u>
sram_read_data[31:0]=2576980377	2576980377	
sram_write=0		
sram_write_addr[12:0]=0	0	\(\sqrt{1}\) \(\sqrt{2}\) \(\sqrt{3}\) \(\sqrt{0}\)
sram_write_data[31:0]=0	0	X3840206052 X0
write=1		· · · · · · · · · · · · · · · · · · ·
write_data[1:0]=3	(XXX )(XX	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
write_done=0		П
write_en=1		
ri[31:0]=0	0	
wi[31:0]=3	(0 XX)	<b>√√√√√√√√√√√√√√√√√√√√√√√√√√√</b>