						***					***		***					
Time clk						100 sec					200 sec		300 sec				400	sec
clk	Ш					$\prod \coprod \prod$	\prod	\prod		\prod								
reset																		
reset_all																		
address[2:0]	000		001	(010	(011	100	101	(110	111	000			(001 (010	011 (100	X101 X110 X	111		
load					$\overline{\Box}$		$\overline{\Box}$											
d_in[7:0]	00	AA	AB	AC	AD	AE	AF	/B0	B1		(CC							
	XX	00 AA	+ A B)+ (AC	+ (AD	+ AE	(+ A F	+ (B0	+ B1	00)+ <u>)</u> + <u>)</u>	+ \/+ \/+ \/+ \/+	\(\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ 	+ \ CC	(00	
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