Time														
		10 sec	20 sec	30 sec	output_AA	40 sec	50 sec	60 sec	output_CC	70 sec	reset_sem_clk 80 sec	90 sec	100 sec	110 sec	120 sec	130 sec	140 sec

clk=1

d_in[7:0]=AA   00   AA   CC

load=1

reset=0

q_out[7:0]=AA xx   00   AA   CC   00