

#### Assignment presentation

Group: L05

Ho Chi Minh City University of Technology

2022.12.12

Task 1 Requirement elicitation

Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1

- 1 Task 1 Requirement elicitation
- 2 Task 2 System modelling
- 3 Task 3 Architecture design
- 4 Task 4 Implementation Sprint 1
- 5 Task 5 Implementation Sprint 1

### Primary diagram

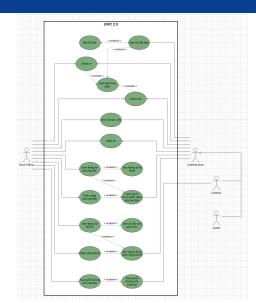
#### Task 1 Requirement elicitation

Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1





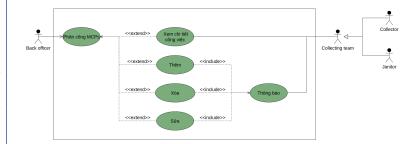
# Task assignment module

#### Task 1 Requirement elicitation

Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1





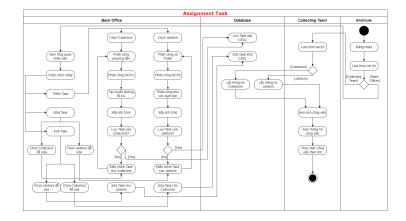
## Activity diagram

Task 1 Requirement elicitation

## Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1





## Sequence diagram

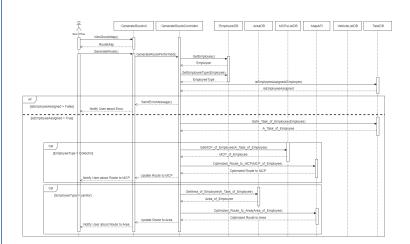
Task 1 Requirement elicitation

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#### Class diagram

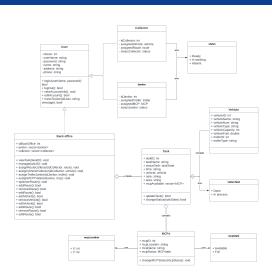
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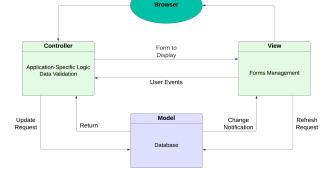
# System modules

Task 1 Requirement elicitation

Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1





# Component modules

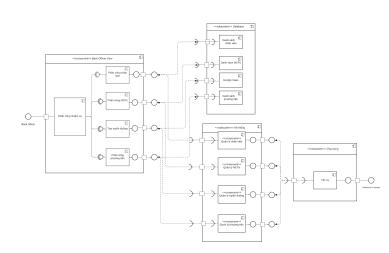
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#### MVP1

Task 1 Requirement elicitation

Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1



#### MVP2

Task 1 Requirement elicitation

Task 2 System modelling

Task 3 Architecture design

Task 4 Implementation -Sprint 1

