USAF ACADEMY

DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

ECE 281 GRADED REVIEW #1 SPRING 2016

Name:	Section:
Academic Security	This examination is not released from academic security until 1630 on 16
	February 2016 . Until this time, you may not discuss the examination contents
	or the course material with anyone other than your instructor.
Integrity	Your honor is extremely important. This academic security policy is designed to
	help you succeed in meeting academic requirements while practicing the
	honorable behavior our country rightfully demands of its military. Do not
	compromise your integrity by violating academic security or by taking unfair
	advantage of your classmates.
Authorized Resources	Calculator
Instructions	Show all work for full credit
	 Box or circle your final answer.
	 For all numerical answers, use engineering notation and include units.
	 Completely label all your diagrams, drawings, graphs, etc. for full credit.
	You have 53 minutes to complete this exam.

Problem	Value	Earned
1	20	
2	20	
3	18	
4	16	
5	6	
6	20	
Total	100	

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Spring 2016 Page 2 of 7

Problem 1

Number Conversions

a. Using 2s complement representation, the range of numbers you can represent with eight bits is:

-2 to 2 N-1-1

-128 to 127

b. Convert 1111012 to hexadecimal. Show your work.

a. 75

b. 3D

c. E1

d. 61

e. none of these

c. Convert 101₁₀ to octal. Show your work.

12 5

a. 131

b. 145

c. 65

d. 61

e. none of these

6+24+27+2'=67+16+4+2

d. Convert 1010110_2 to decimal. Show your work

a. 108

b. 29

c. 56

d. 86

e. none of these

27

e. Convert 27₁₆ to binary. Show your work.

a. 011011

b. 101010

c. 100110

d. 010111

e. none of these

o

f. Convert -25_{10} to 6-bit 2s complement binary. Show your work.

a. 100111

b. 111001

c. 011001

d. 010101

e. none of these

25-7016

2'5 10011

g. Convert -25₁₀ to 6-bit signed magnitude. Show your work.

a. 110101

b. 011001

c. 111101

d. 100111

e. none of these

257011001 -257111001

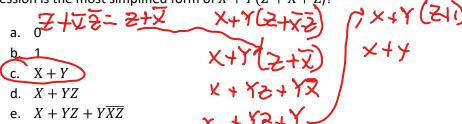
Spring 2016

Page 3 of 7

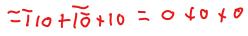
Problem 2 (20 points)

Boolean Equations and Algebra

a. Which expression is the most simplified form of $X + Y(Z + \overline{X} + \overline{Z})$?



b. Given that: $F(A, B, C) = \overline{A}BC + \overline{B}\overline{C} + BC$, what does F(1, 1, 0) equal?



(i. 0)

ii. 1

iii. C

iv. $\overline{\mathsf{C}}$

v. none of these

c. Answer the following questions for this truth table.

						$\mathcal{O} \mathcal{O}$	Ol	Įι	10
Χ	Υ	Z	L		A			•	
	0				U	0	0	ı	1
	0				1	1	0	1	Δ
	1					l	U	l	U
	1								
	0								
1	0	1	0						
1	1								
1	1	1	1						

i. Write the Boolean equation for this truth table in SOP form.

1= xxx + xxx

ii. Write the SOP in simplified form.

d. Darth Vader will enjoy his picnic on sunny days that have no ants. He will also enjoy his picnic any day he sees a humming bird, as well as days where there are ladybugs but no ants. Write a Boolean equation for his enjoyment (E) in terms of sun (S), ants (A), hummingbirds (H), and ladybugs (L).

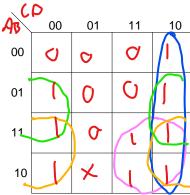
E=SA+H+LA

Problem 3

K Maps & Combinational Logic Implementation

a. Simplify the following equation using a Karnaugh map:

Α	В	С	D	Υ
0	0	0	0	0
0	0	0	1	0
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1 0
0	1	0	1	0
0	1	1	0	1
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1 1 1
1	1	0	0	1
1	1	0	1	Χ
1	1	1	0	1
1	1	1	1	1

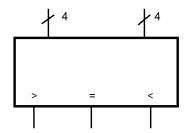


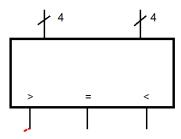
Simplified Boolean Equation: $Y = \frac{CD+BD+AD+AC}{}$

Spring 2016 Page 5 of 7

Circuit Design

a. Given two four-bit magnitude comparators, add some combinational logic gates and create an
eight-bit magnitude comparator. Ensure the inputs (and associated bits) are clearly labeled.
The outputs are shown on the left side of the page. (Hint: How would a person compare two
numbers?)





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See design problem email

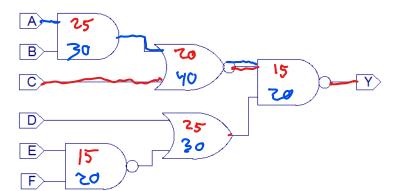
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Problem 5

Timing Analysis

Use the information in the below schematic and associated table to answer the next two questions.



Gate	t _{pd}	t _{cd}
AND	30	25
OR	30	25
NAND	20	15
NOR	40	20
XOR	60	40

- a. Draw the short path in the above schematic.
- b. Given the chart of gate delays in ps, calculate the length of the critical path. Show your work.

Spring 2016 Page 7 of 7