

# CERTIFICATE OF COMPLETION

THIS IS TO CERTIFY THAT

**GR Varshitha**

**Reg No: 22BEC7004 of Vellore Institute of Technology (VIT)**

has successfully completed the Industry Certification Program titled

**Certificate in VLSI Chip Design and simulation for Industrial Applications**

offered by the **CollegeConnect Program of L&T EduTech** during the period **22-May-2025 to 07-July-2025**

The participant has been awarded the grade: **A**

A handwritten signature in blue ink, appearing to read 'Febin', written over a horizontal line.

**M.F. Febin**

Head - L&T EduTech

**Ref No: LTET/VIT/JUL25/04-0166**