

MNNIT

ALLAHABAD

Call for Papers

VCAS 2018





International Conference on VLSI, Communication and Signal Processing

November 29 to December 1, 2018

Conference Website: www.mnnit.ac.in/vcas2018/

Organized by: Electronic & Communication Engineering Department MNNIT Allahabad

The Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology (MNNIT) ALLAHABAD, proudly announces the hosting of *First International Conference on VLSI, Communication and Signal Processing (VCAS 2018).* The conference aims to provide a platform for academicians, individuals working in the industry, as well as students to showcase their research innovations and get recognition. VCAS 2018 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

TRACKS

VLSI

Analog, Digital & Mixed Signal Design, Testing & Testable Circuit Low- Power Design Packaging & Interconnect 2D material based Devices MEMS & NEMS design and Technology Devices Modeling and Process simulation Thin Film, Ultra High Speed Transistors, HEMT/HBT Reconfigurable Architecture Design Power Devices and Reliability Artificial Intelligence (Process & Device) Internet of Things (Process & Device) Emerging Technology.

COMMUNICATION

Communication Theory and Applications Optical Networks and Systems Microwave Communications Algorithms and Techniques for 5G and beyond Wireless and Ad Hoc Networks IOT and Sensor Networks Antenna Design for Communication Systems and Networks. Vehicular Communications and Networks Wireless Modeling, Algorithms, and Simulation Cognitive Radio and Spectrum Management Adaptive Communication Systems and Networks Intelligent Protocols and Designing for Communication Systems and Networks.

SIGNAL PROCESSING

Signal Processing: Theory, Methods, and Applications Signal Processing Systems and Architectures Signal Processing Algorithms Image, Video, and Multidimensional Signal processing Multimedia Signal Processing Biomedical Signal Processing and Bioinformatics Signal Processing for Communication and Networking Signal Processing for Machine Learning and Compression Signal Processing for Smart systems Sparse Signal Processing.

Registration Fee

Category	Indian Delegates	Foreign Delegates
Student	INR 5,000/-	USD 250
Faculty	INR 8,000/-	USD 400
Industry	INR. 10,000/-	USD 500

IMPORTANT DATES

Submission dateJuly 15, 2018Acceptance notificationSeptember 15, 2018Registration startsSeptember 16, 2018Deadline for final paperNovember 1, 2018Conference dateNovember 29 to December 1, 2018

Paper Submissions: Prospective authors are encouraged to submit research papers containing the original contributions which have not been published or submitted elsewhere. Springer guidelines can be obtained of conference website for manuscript preparation.

Easy Chair link for paper submission: -

https://easychair.org/conferences/?conf=vcas18

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	Signal	
,	Processing	Dr. Rajib Kumar Jha, IIT Patna

Publications:

Select papers from the conference will be published by Springer as a proceedings book volume. Springer will conduct quality checks on the accepted papers and only papers that pass these checks will be published.

CONTACTS INFORMATION

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