

## Schedule - VCAS 2018

Date	8:30AM to 9:30AM	9:30AM to 11:00 AM		11:00 AM to 11:30 AM	11:30AM to 01:30 PM		01:30PM to 02:30PM	02:30 to 04:00PM	04:00 PM to 04:30 PM	04:30 PM to 06:00PM
29/Nov	Registration	Inauguration (9:30 AM to 10:00 AM)  <b>Venue:</b> Institute Seminar Hall	Keynote lecture by Prof. S.C. Dutta Roy, IIT Delhi (10:00 AM to 11:00 AM)  <b>Venue:</b> Institute Seminar Hall	High Tea	Invited lecture by Dr. D.C. Pande, Dr. Raja Ramanna chair, LRDE, DRDO, Bengaluru (11:30 AM to 12:30 PM) <b>Venue:</b> Institute Seminar Hall	Invited lecture by Dr. Debashish Dutta, Scientist G. and group coordinator, MEITY (Retd.) (12:30 PM to 01:30 PM) <b>Venue:</b> Institute Seminar Hall	Lunch Break	VLSI – S1 (Paper IDs: 4,8,31,36,58) <b>Venue:</b> LH3	Tea Break	VLSI – S2 (Paper IDs: 5,65,67,93,95)  <b>Venue:</b> LH3
								Comm. – S1 (Paper IDs: 3,7,13,15,17,18) <b>Venue:</b> LH1		Comm – S2 (Paper IDs: 24,28,29,33, 44,46) <b>Venue:</b> LH1
								Signal Proc – S1 (Paper IDs: 14,20, 22,26,30,32 ) <b>Venue:</b> LH4		Signal Proc – S2 (Paper IDs: 35,37,38,39,40) <b>Venue:</b> LH4
								<b>Workshop on Advanced DSP Processors</b> <b>Venue:</b> Computer Architecture Lab		
30/Nov	Registration + Quick Snacks	Invited lecture by Prof. Shekhar Varma, IIIT Allahabad (09:30 AM to 10:30 AM)  <b>Venue:</b> Institute Seminar Hall	Tea Break	VLSI – S3 (Paper IDs: 25,27,70,71,78,118)  <b>Venue:</b> LH3		Lunch Break	VLSI – S4 (Paper IDs: 96,117,126,130,131) <b>Venue:</b> LH3	Tea Break	VLSI – S5 (Paper IDs: 106,107,90,63,109) <b>Venue:</b> LH3	
				Comm – S3 (Paper IDs: 49,50,51,56,61,76 ) <b>Venue:</b> LH1			Comm – S4 (Paper IDs: 77, 80,81,84,85,86) <b>Venue:</b> LH1		Comm – S5 (Paper IDs: 21, 87,92,99 ,101,103) <b>Venue:</b> LH1	
				Signal Proc – S3 (Paper IDs: 41,43,45,47,53,55) <b>Venue:</b> LH4			Signal Proc – S4 (Paper IDs: 64,79,88,91,94) <b>Venue:</b> LH4		Signal Proc – S5 (Paper IDs: 98,104, 110, 114,116) <b>Venue:</b> LH4	
				<b>Workshop on Cadence Tool</b> <b>Venue:</b> VLSI Lab			<b>Workshop on Cadence Tool</b> <b>Venue:</b> VLSI Lab			
01/Dec	Registration + Quick Snacks	VLSI – S6 (Paper IDs: 42,62,83,133,138) <b>Venue:</b> LH3	Tea Break	Valedictory  <b>Venue:</b> Institute Seminar Hall	Lunch Break					
		Comm – S6 (Paper IDs: 12,127,129,136) <b>Venue:</b> LH1								
		Signal Proc – S6 (Paper IDs: 120,121,124,128) <b>Venue:</b> LH4								
		Sangam Visit								