VCAS —International Conference on VLSI, Communication And Signal Processing

VCAS

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VCAS 2018

International Conference on VLSI, Communication and Signal Processing

Organised by ECED MNNIT Allahabad
November 29 to December 1, 2018

Submit paper   Brochure

About

The Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology, proudly announces the hosting of First International Conference on VLSI, Communication and Signal Processing (VCAS 2018).

The conference aims to provide a platform for academicians, individuals working in the industry, as well as students to showcase their research innovations and get recognition. VCAS 2018 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

In a quest to regard all classes of research-oriented audience from across the globe, the three-day conference (VCAS 2018) is proposed to be held from November 29, 2018 to December 1, 2018. The scope of the conference embraces all the latest research trends in Electronics and Communication Engineering.

Publication

Select papers from the conference will be published by Springer as a proceedings book volume. Springer will

Call for papers

International Conference on VLSI, Communication and Signal Processing (VCAS) is the premier forum for the presentation of new advances and research results in the fields of VLSI, Communication and Signal Processing. The conference will bring together leading researchers, engineers and scientists in the domain of interest from around the world. Topics of interest for submission include, but are not limited to:

Tracks

VLSI

Analog, Digital & Mixed Signal Design
Testing & Testable Circuit
Low- Power Design
Packaging & Interconnect
2D material based Devices
MEMS & NEMS design and Technology
Devices Modeling and Process simulation
Thin Film, Ultra High Speed Transistors, HEMT/HBT
Reconfigurable Architecture Design
Power Devices and Reliability
Artificial Intelligence (Process & Device)
Internet of Things (Process & Device)
Emerging Technology

Communication

Communication Theory and Applications
Optical Networks and Systems
Microwave Communications
Algorithms and Techniques for 5G and beyond
Wireless and Ad Hoc Networks
IoT and Sensor Networks
Antenna Design for Communication Systems and Networks
Vehicular Communications and Networks
Wireless Modeling, Algorithms, and Simulation
Cognitive Radio and Spectrum Management
Adaptive Communication Systems and Networks
Intelligent Protocols and Designing for Communication Systems and Networks

Signal Processing

Signal Processing: Theory, Methods, and Applications
Signal Processing Systems and Architectures
Signal Processing Algorithms
Image, Video, and Multidimensional Signal processing
Multimedia Signal Processing
Biomedical Signal Processing and Bioinformatics
Signal Processing for Communication and Networking
Signal Processing for Machine Learning and IoT
Gnomic Signal Processing
Coding and Compression
Signal Processing for Smart systems
Sparse Signal Processing

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Schedule

Submission date

July 15, 2018

Acceptance notification

September 15, 2018

Registration starts

September 16, 2018

Deadline for final paper

November 1, 2018

Conference date

November 29 to December 1, 2018

Registration

Fee details

Category

Indian Delegates

Foreign Delegates

Industry

INR 10,000

USD 500

Faculty

INR 8,000

USD 400

Student

INR 5,000

USD 250

Payment Details

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VCAS 2018

Account Number

7184 0030 1000 317

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