



# VCAS 2018

International Conference on  
VLSI, Communication and  
Signal Processing

Organised by ECED MNNIT Allahabad

November 29 to December 1, 2018

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# About

The Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology, proudly announces the hosting of First International Conference on VLSI, Communication and Signal Processing (VCAS 2018). The conference aims to provide a platform for academicians, individuals working in the industry, as well as students to showcase their research innovations and get recognition. VCAS 2018 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

In a quest to regard all classes of research-oriented audience from across the globe, the three-day conference (VCAS 2018) is proposed to be held from November 29, 2018 to December 1, 2018. The scope of the conference embraces all the latest research trends in Electronics and Communication Engineering.

# Publication

Select papers from the conference will be published by Springer as a proceedings book volume. Springer will conduct quality checks on the accepted papers and only papers that pass these checks will be published.



# Call for papers

and Signal Processing. The conference will bring together leading researchers, engineers and scientists in the domain of interest from around the world. Topics of interest for submission include, but are not limited to:

## Tracks

Signal Design	and Applications	Methods, and
Testing & Testable Circuit	Optical Networks and	Applications
Low- Power Design	Systems	Signal Processing Systems
Packaging & Interconnect	Microwave	and Architectures
2D material based Devices	Communications	Signal Processing
MEMS & NEMS design	Algorithms and	Algorithms
and Technology	Techniques for 5G and	Image, Video, and
Devices Modeling and	beyond	Multidimensional Signal
Process simulation	Wireless and Ad Hoc	processing
Thin Film, Ultra High	Networks	Multimedia Signal
Speed Transistors,	IoT and Sensor Networks	Processing
HEMT/HBT	Antenna Design for	Biomedical Signal
Reconfigurable	Communication Systems	Processing and
Architecture Design	and Networks	Bioinformatics
Power Devices and	Vehicular	Signal Processing for
Reliability	Communications and	Communication and
Artificial Intelligence	Networks	Networking
(Process & Device)	Wireless Modeling,	Signal Processing for
Internet of Things	Algorithms, and	Machine Learning and IoT
(Process & Device)	Simulation	Genomic Signal Processing
Emerging Technology	Cognitive Radio and	Coding and Compression
	Spectrum Management	Signal Processing for
	Adaptive Communication	Smart systems
	Systems and Networks	Sparse Signal Processing
	Intelligent Protocols and	
	Designing for	
	Communication Systems	
	and Networks	

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# Schedule

Submission date	July 15, 2018
Acceptance notification	September 15, 2018
Registration starts	September 16, 2018
Deadline for final paper	November 1, 2018
Conference date	November 29 to December 1, 2018

# Registration

## Fee details

Category	Indian	Foreign
	Delegates	Delegates
Industry	INR 10,000	USD 500
Faculty	INR 8,000	USD 400
Student	INR 5,000	USD 250

## Payment Details

Account name	VCAS 2018
Account Number	7184 0030 1000 317
IFSC	VIJB0007184
Bank & branch	Vijya bank, MNNIT Allahabad

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