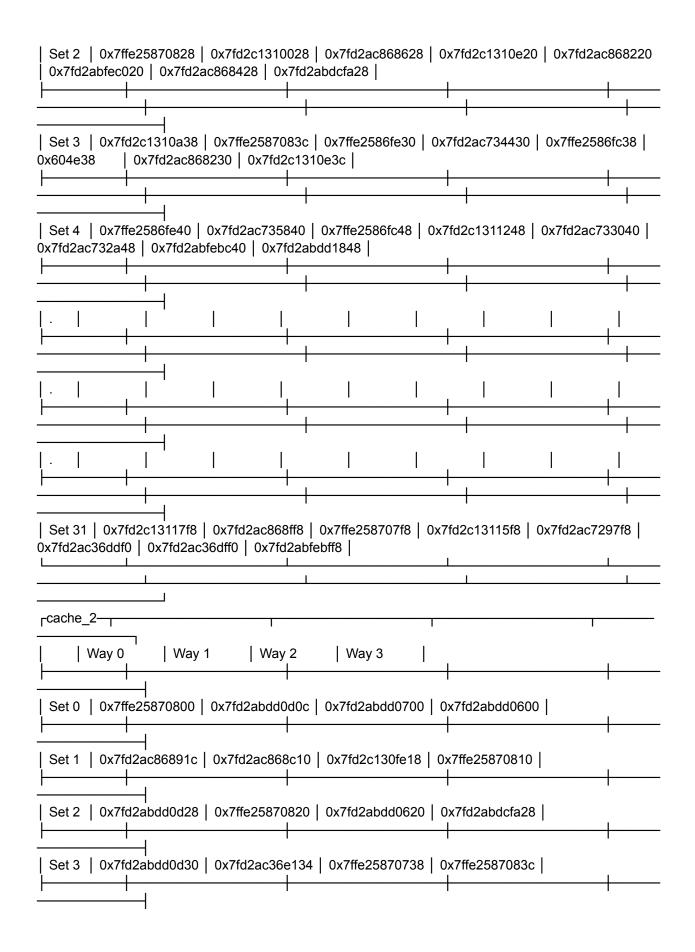
Convolution Operation:

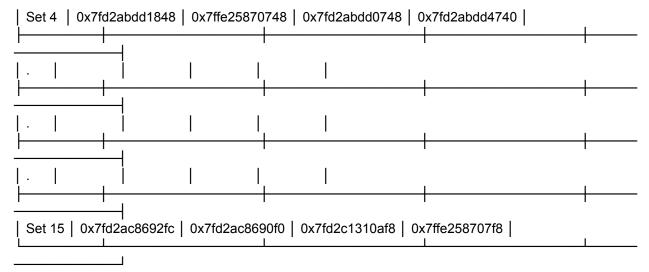
Varying Block Size:

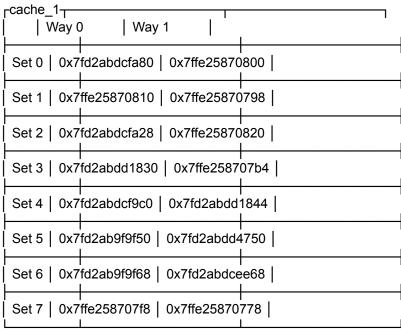
Matrix Size 20x20:

Block Size 16

Number of instructions: 186697 Total cycles taken: 1629177 cache_3 Number of accesses: 1571 Number of hits: 1009 Number of misses: 562 cache_2 Number of accesses: 16765 Number of hits: 15063 Number of misses: 1702 cache 1 Number of accesses: 186697 Number of hits: 167123 Number of misses: 19574 AMATs: {'cache_1': 7.549544455680323, 'cache_2': 62.46961792388624, 'cache 3': 457.7339274347549, 'mem': 1000} rcache_3-r Way 2 Way 3 Way 4 Way 5 Way 0 Way 1 Way 6 Way 7 Set 0 | 0x7ffe25870808 | 0x7fd2ac868e08 | 0x7fd2ac869008 | 0x7fd2c1311600 | 0x7fd2ac868600 0x7fd2ac36de00 | 0x7fd2ac868c00 | 0x7fd2abdd0600 | Set 1 | 0x7ffe25870818 | 0x7fd2ac733a18 | 0x7fd2c130fe18 | 0x7fd2ac868618 | 0x7fd2c1310e18 | 0x7fd2c131161c | 0x605210 0x7fd2ac868c10







real 1m48.760s user 0m17.443s sys 0m2.055s

Block Size 32

Number of instructions: 186697 Total cycles taken: 1430181

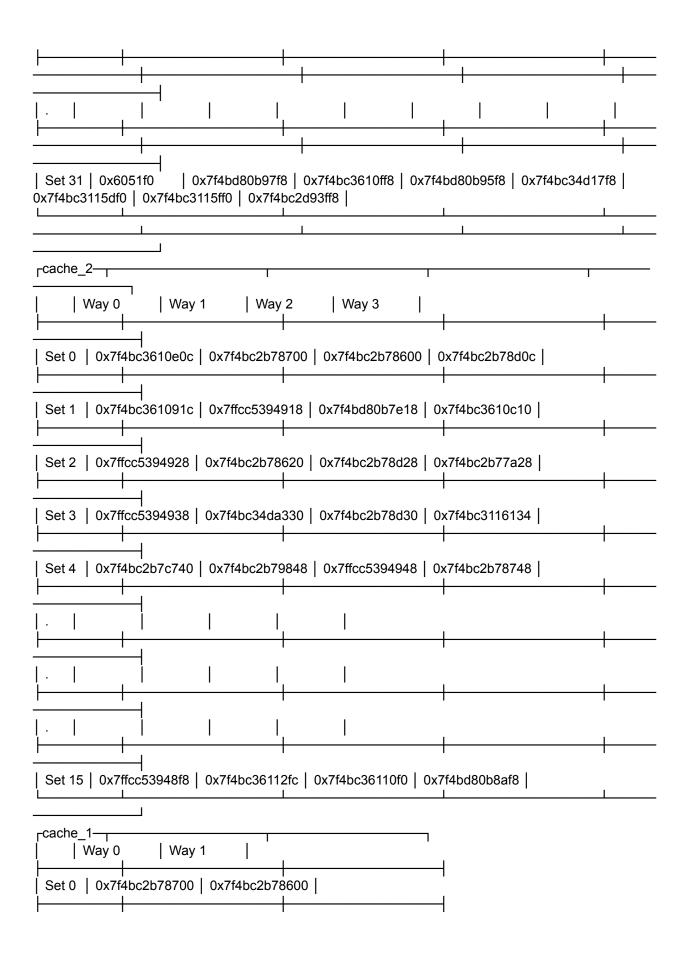
cache_3

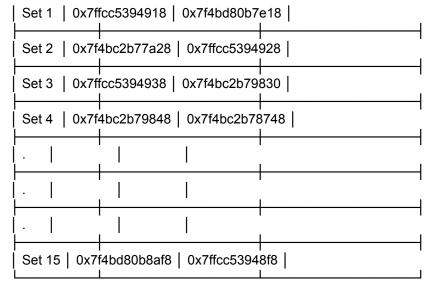
Number of accesses: 1556 Number of hits: 985 Number of misses: 571

cache_2

Number of accesses: 8298

Number of hits: 6607 Number of misses: 1691 cache 1 Number of accesses: 186697 Number of hits: 177747 Number of misses: 8950 AMATs: {'cache_1': 6.328875285083403, 'cache 2': 111.16033844683979, 'cache_3': 466.9665809768637, 'mem': 1000} rcache 3-r Way 0 Way 1 Way 2 Way 3 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7f4bd80b9808 | 0x7f4bc3610e08 | 0x7f4bc3611008 | 0x7f4bd80b9600 | 0x7f4bc3610600 0x7f4bc3115e00 | 0x7f4bc3610c00 | 0x7f4bc2b78600 | Set 1 | 0x7f4bd80b7e18 | 0x7ffcc5393e10 | 0x7f4bc3610618 | 0x7f4bd80b8e18 | 0x7f4bd80b961c | 0x7ffcc5394818 | 0x7f4bc3610c10 | Set 2 | 0x7f4bc361042e | 0x7f4bc3610628 | 0x7f4bd80b8e20 | 0x7f4bc3610220 | 0x7ffcc5394828 0x7f4bc2d94020 | 0x7f4bc2b78620 | 0x7f4bc2b77a28 | Set 3 | 0x7f4bd80b8a38 | 0x7ffcc5394230 | 0x7f4bc34dc430 | 0x7ffcc5393e38 | 0x604e38 0x7f4bd80b8e3c | 0x7f4bc3610230 | 0x7ffcc5394838 | Set 4 | 0x7ffcc5394848 | 0x7f4bc34dd840 | 0x7ffcc5393e48 | 0x7f4bd80b9248 | 0x7f4bc34db040 | 0x7f4bc34daa48 | 0x7f4bc2d93c40 | 0x7f4bc2b79848 |





real 1m42.603s user 0m16.449s sys 0m2.019s

Block Size 64

Number of instructions: 186697 Total cycles taken: 1249197

cache_3

Number of accesses: 1015 Number of hits: 428 Number of misses: 587

cache_2

Number of accesses: 2745 Number of hits: 1643 Number of misses: 1102

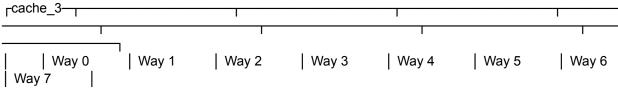
cache_1

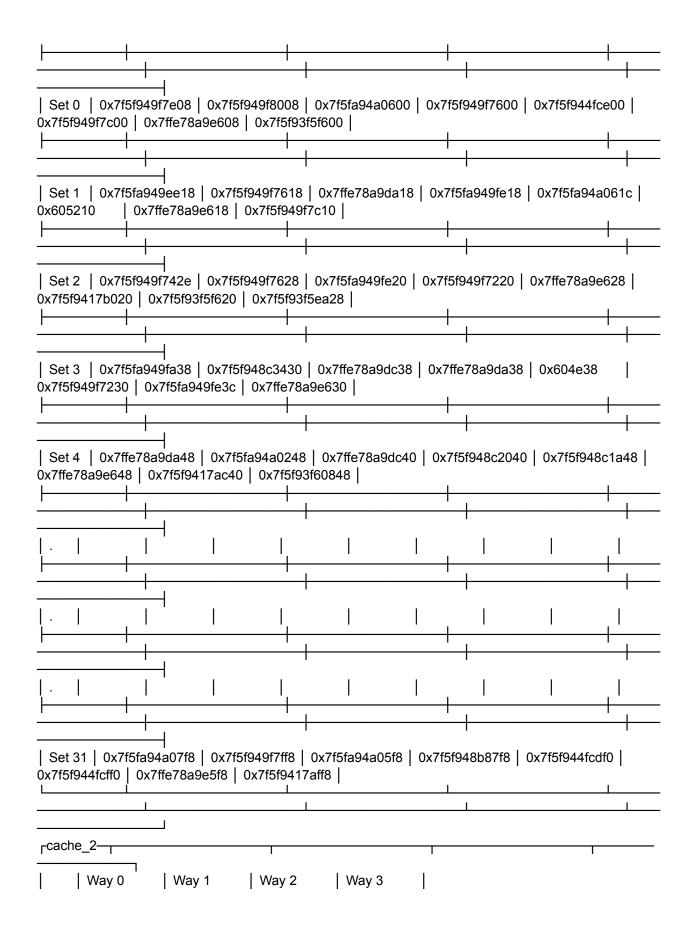
Number of accesses: 186697 Number of hits: 183495 Number of misses: 3202

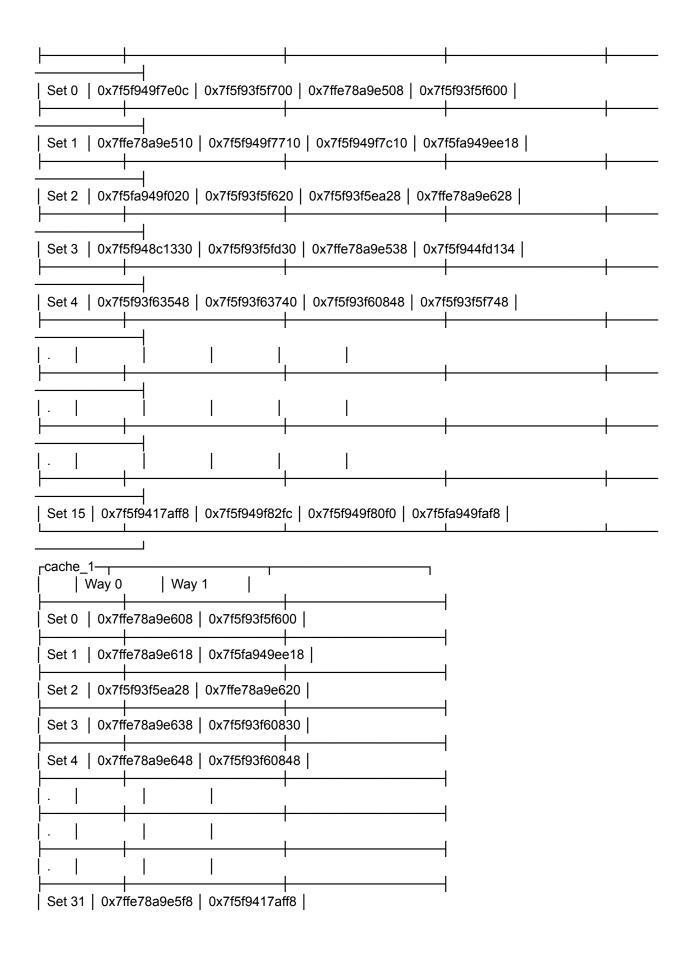
AMATs:

{'cache_1': 5.94488459640029, 'cache_2': 288.3185011709602, 'cache_3': 678.3251231527094,

'mem': 1000}







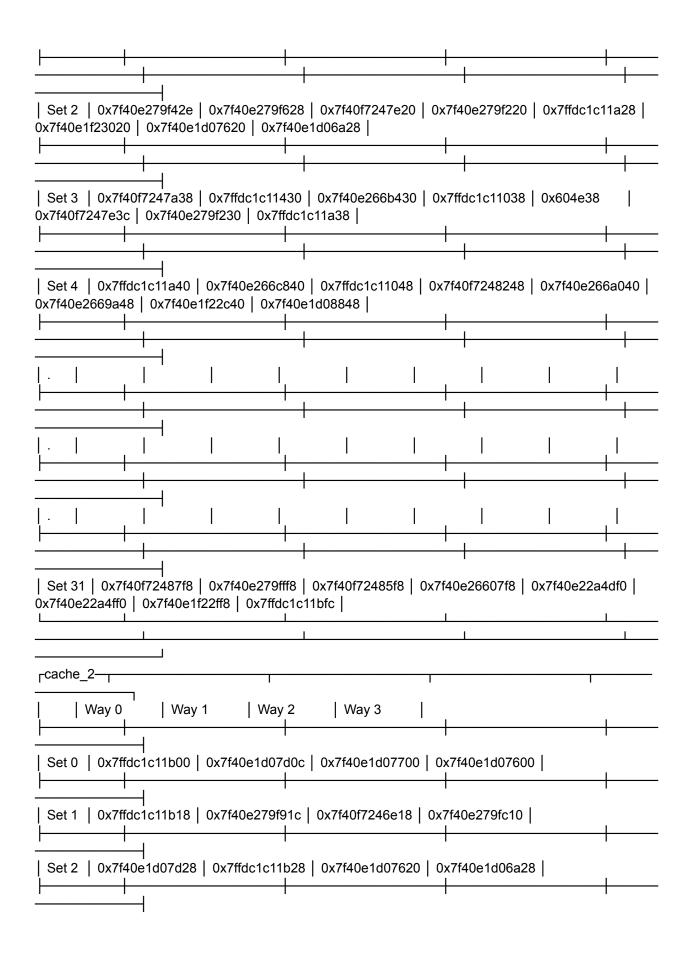
1m38.976s real 0m16.035s user 0m1.946s

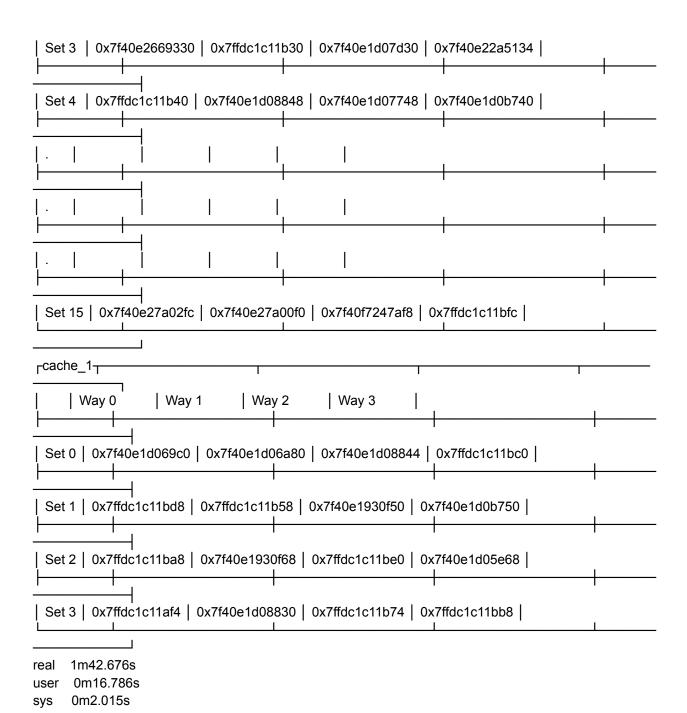
Matrix Size 32x32:

sys

Block Size 16

```
Number of instructions: 186697
Total cycles taken: 1473353
cache 3
    Number of accesses: 1493
    Number of hits: 933
    Number of misses: 560
cache_2
    Number of accesses: 11460
    Number of hits: 9843
    Number of misses: 1617
cache 1
    Number of accesses: 186697
    Number of hits: 174231
    Number of misses: 12466
AMATs:
{'cache_1': 6.54429182524576,
'cache 2': 83.03406472789247,
'cache_3': 475.0837240455459,
'mem': 1000}
rcache 3-r
     Way 0
                    Way 1
                                Way 2
                                             Way 3
                                                          Way 4
                                                                       Way 5
                                                                                    Way 6
 Way 7
Set 0 | 0x7f40f7248808 | 0x7f40e279fe08 | 0x7f40e27a0008 | 0x7f40f7248600 | 0x7f40e279f600 |
0x7f40e22a4e00 | 0x7f40e279fc00 | 0x7f40e1d07600 |
 Set 1 | 0x7f40e266aa18 | 0x7ffdc1c11010 | 0x7f40f7246e18 | 0x7f40e279f618 | 0x7f40f7247e18 |
0x7f40f724861c | 0x605210
                             0x7f40e279fc10
```





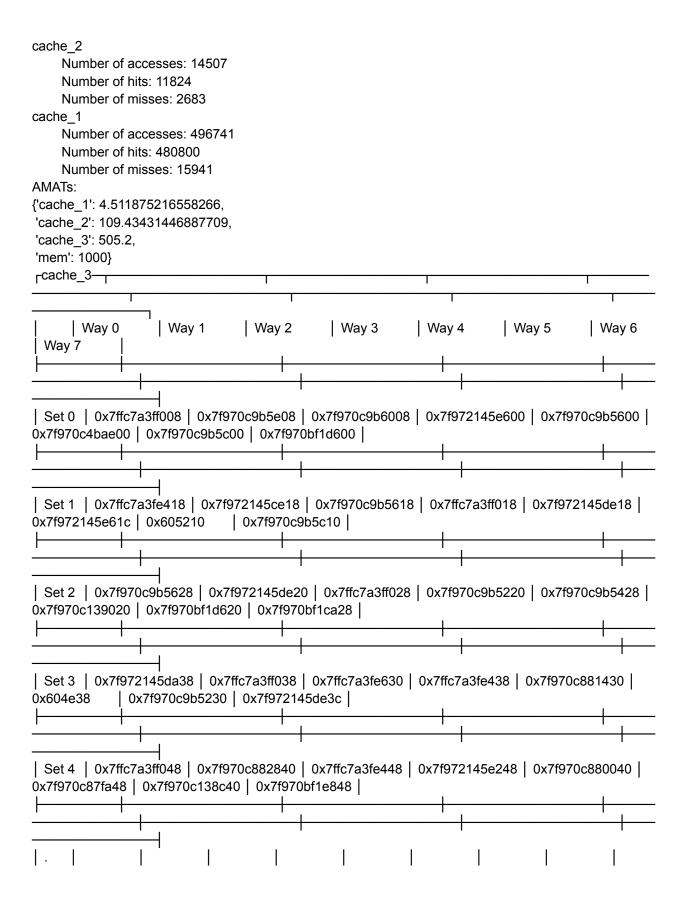
Block Size 32

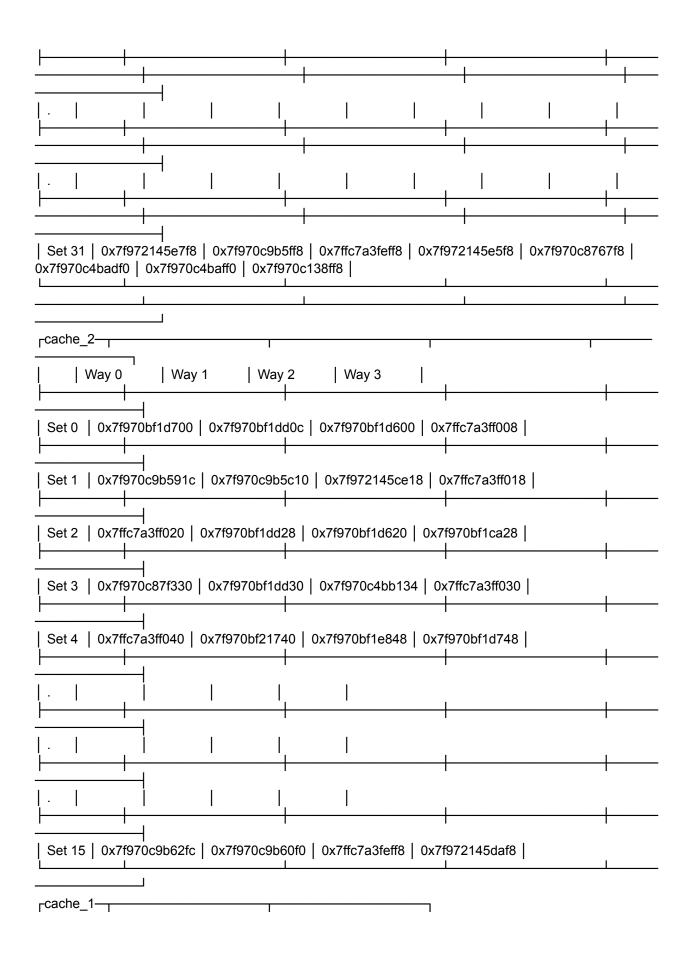
2.00K 0.10 01

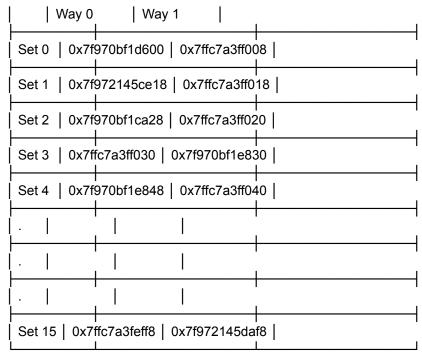
Number of instructions: 496741 Total cycles taken: 2790121

cache_3

Number of accesses: 2500 Number of hits: 1487 Number of misses: 1013







real 4m45.368s user 0m45.527s sys 0m5.569s

Block Size 64

Number of instructions: 496741 Total cycles taken: 2504085

cache_3

Number of accesses: 1978 Number of hits: 923

Number of misses: 1055

cache_2

Number of accesses: 4181 Number of hits: 2046 Number of misses: 2135

cache_1

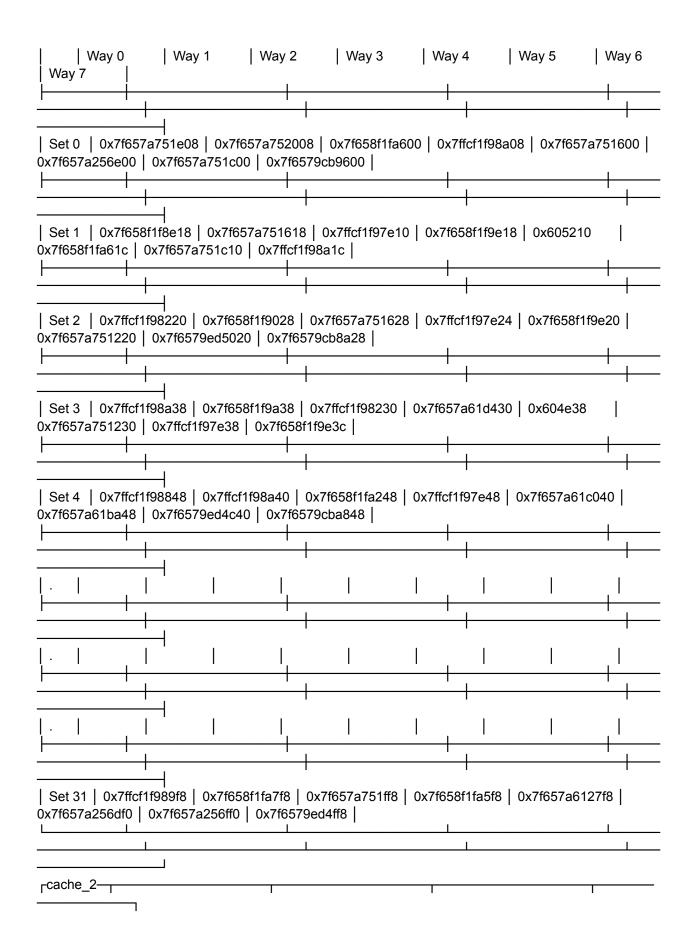
Number of accesses: 496741 Number of hits: 491917 Number of misses: 4824

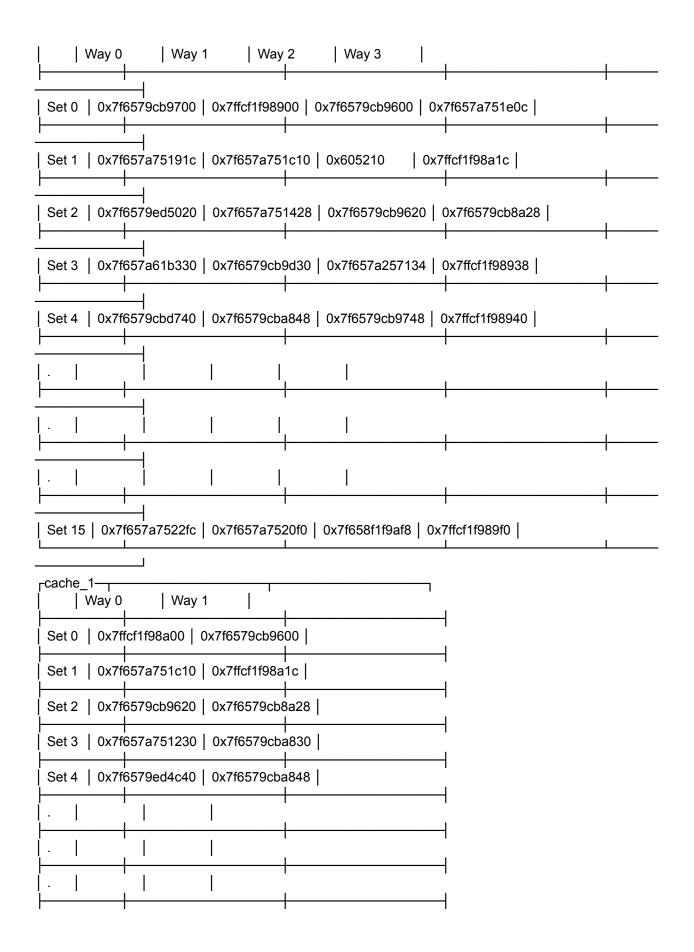
AMATs:

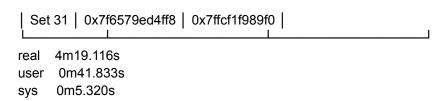
{'cache_1': 4.296254385939403, 'cache_2': 339.4246890393709, 'cache_3': 633.3670374115268,

'mem': 1000}

rcache_3







Varying Associativity:

Matrix Size 20x20:

Associativity: 2

Number of instructions: 186697

Total cycles taken: 1629177

cache 3

Number of accesses: 1571 Number of hits: 1009 Number of misses: 562

cache_2

Number of accesses: 16765 Number of hits: 15063 Number of misses: 1702

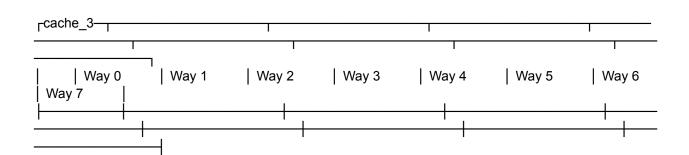
cache_1

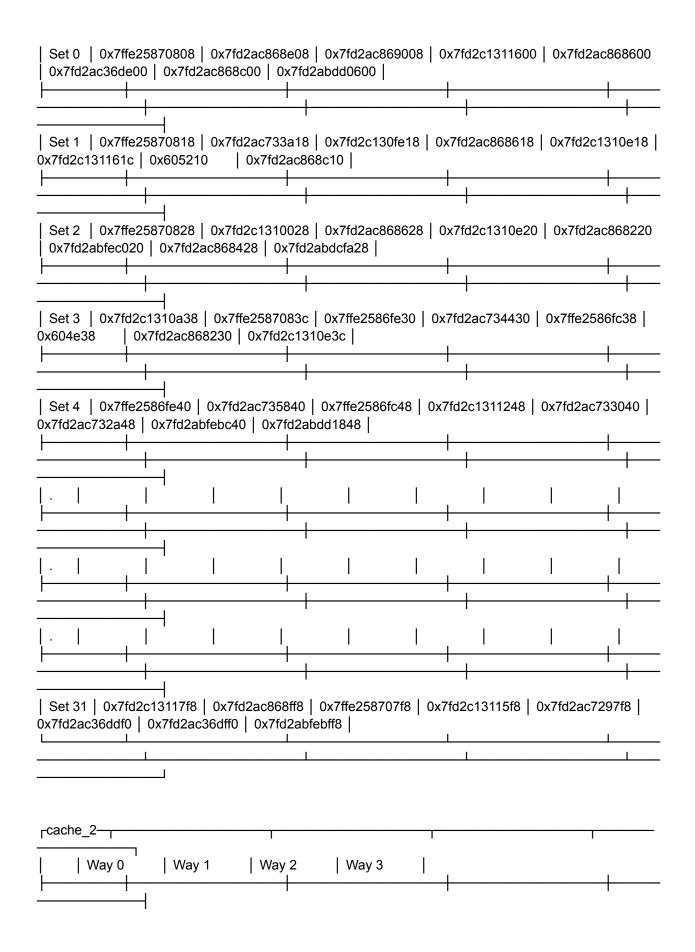
Number of accesses: 186697 Number of hits: 167123 Number of misses: 19574

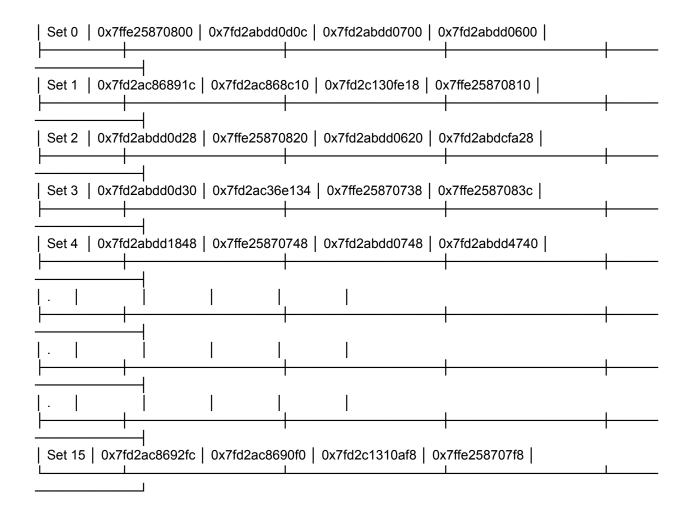
AMATs:

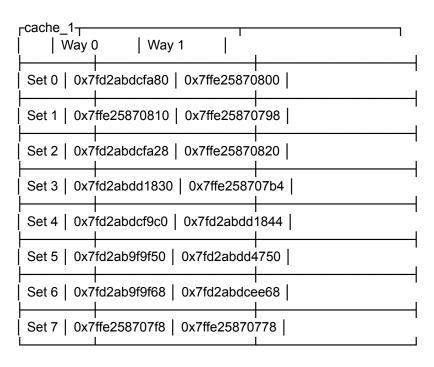
{'cache_1': 7.549544455680323, 'cache_2': 62.46961792388624, 'cache 3': 457.7339274347549,

'mem': 1000}



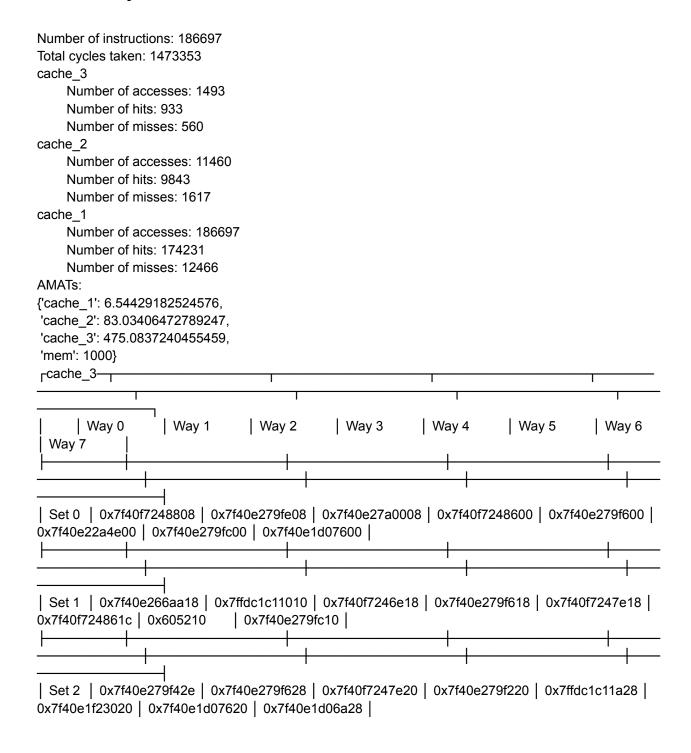


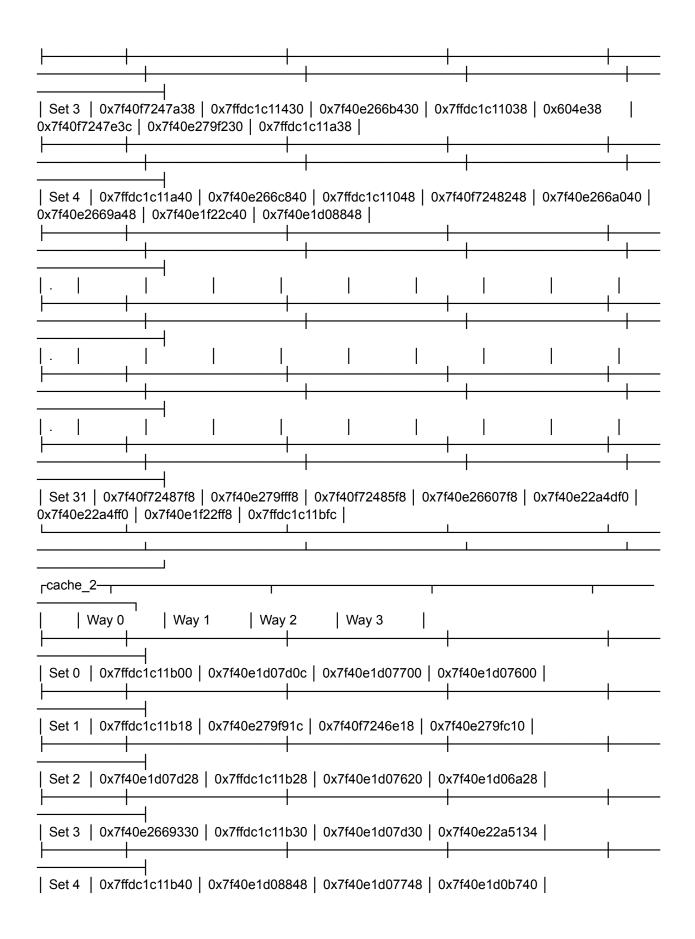


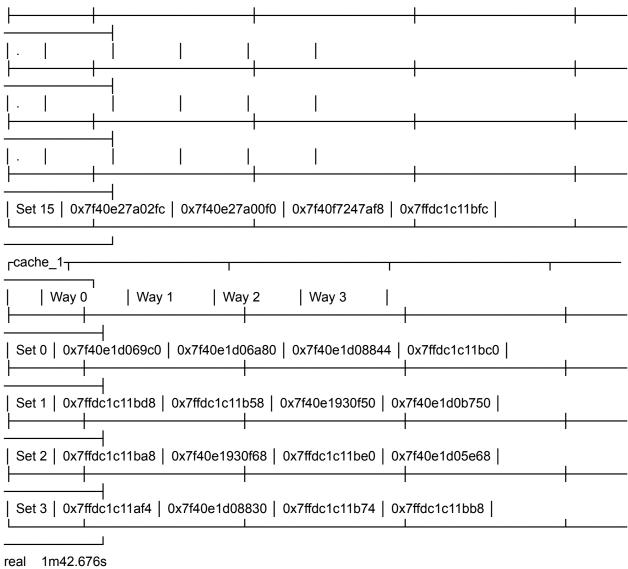


real 1m48.760s user 0m17.443s sys 0m2.055s

Associativity: 4







real 1m42.676s user 0m16.786s sys 0m2.015s

Associativity: 8

Number of instructions: 186697 Total cycles taken: 1491545

cache_3

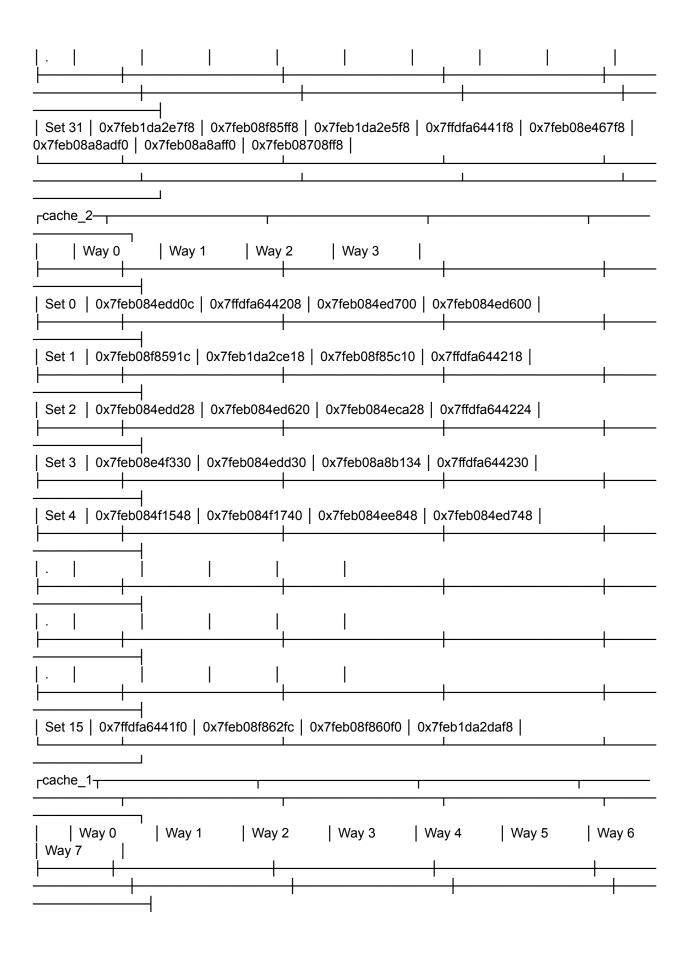
Number of accesses: 1540 Number of hits: 975 Number of misses: 565

cache_2

Number of accesses: 12021 Number of hits: 10357 Number of misses: 1664

cache_1

Number of accesses: 186697 Number of hits: 173695 Number of misses: 13002 AMATs: {'cache_1': 6.615117545996271, 'cache 2': 80.62802649475971, 'cache 3': 466.8831168831169, 'mem': 1000} rcache 3-r Way 0 Way 1 Way 2 Way 3 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7feb08f85e08 | 0x7feb08f86008 | 0x7feb1da2e600 | 0x7ffdfa644200 | 0x7feb08f85600 | 0x7feb08a8ae00 | 0x7feb08f85c00 | 0x7feb084ed600 | Set 1 | 0x7ffdfa643618 | 0x7feb1da2ce18 | 0x7feb08f85618 | 0x7feb1da2de18 | 0x7feb1da2e61c | 0x7ffdfa644218 | 0x605210 0x7feb08f85c10 Set 2 | 0x7feb08f85628 | 0x7feb1da2de20 | 0x7ffdfa644228 | 0x7feb08f85220 | 0x7feb08f85428 | 0x7feb08709020 | 0x7feb084ed620 | 0x7feb084eca28 | Set 3 | 0x7feb1da2da38 | 0x7ffdfa643838 | 0x7feb08e51430 | 0x7ffdfa643638 | 0x604e38 0x7ffdfa644238 | 0x7feb08f85230 | 0x7feb1da2de3c | Set 4 | 0x7ffdfa643840 | 0x7feb08e52840 | 0x7feb1da2e248 | 0x7feb08e50040 | 0x7feb08e4fa48 | 0x7ffdfa644248 | 0x7feb08708c40 | 0x7feb084ee848 |

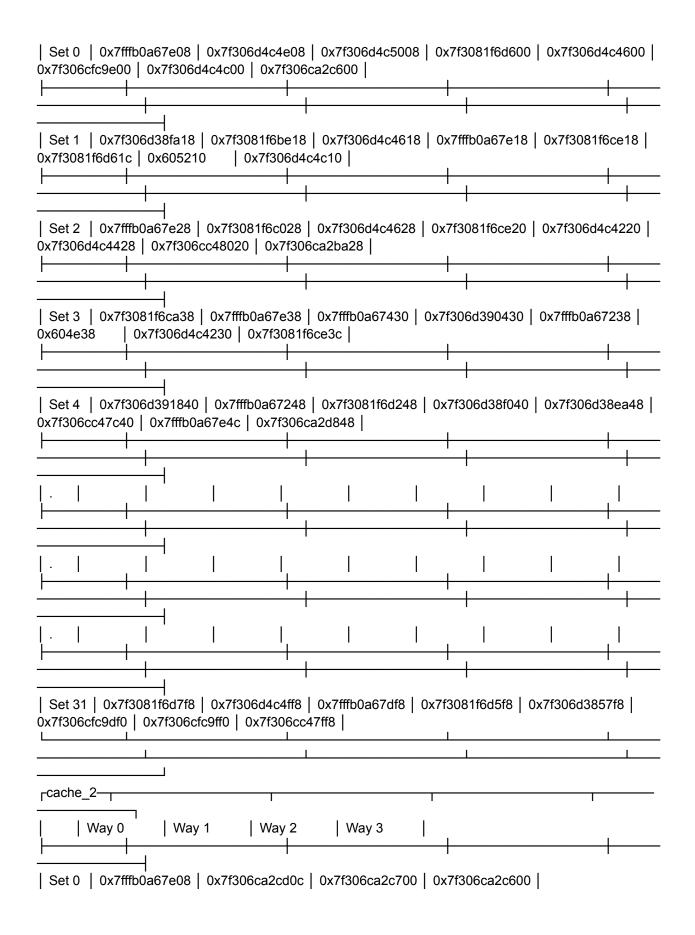


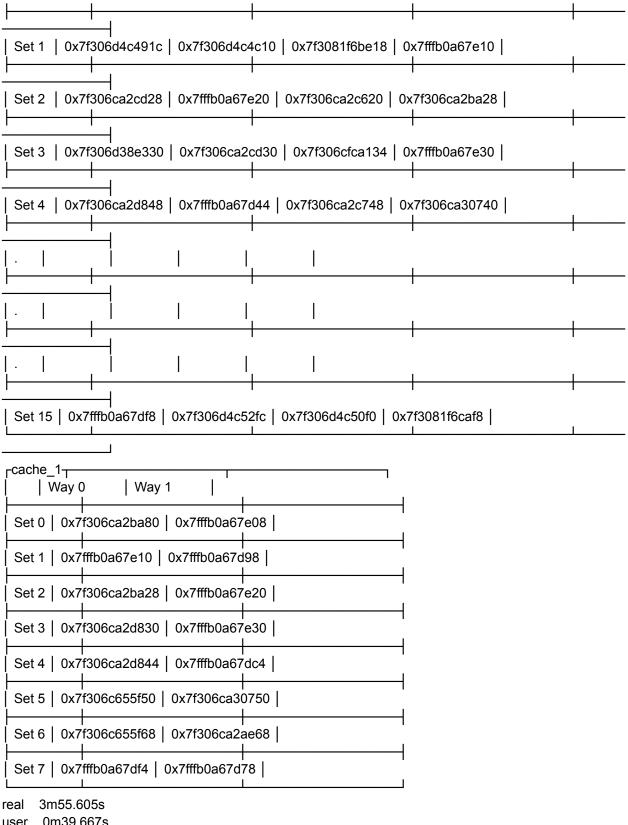


Matrix Size 32x32:

Associativity: 2

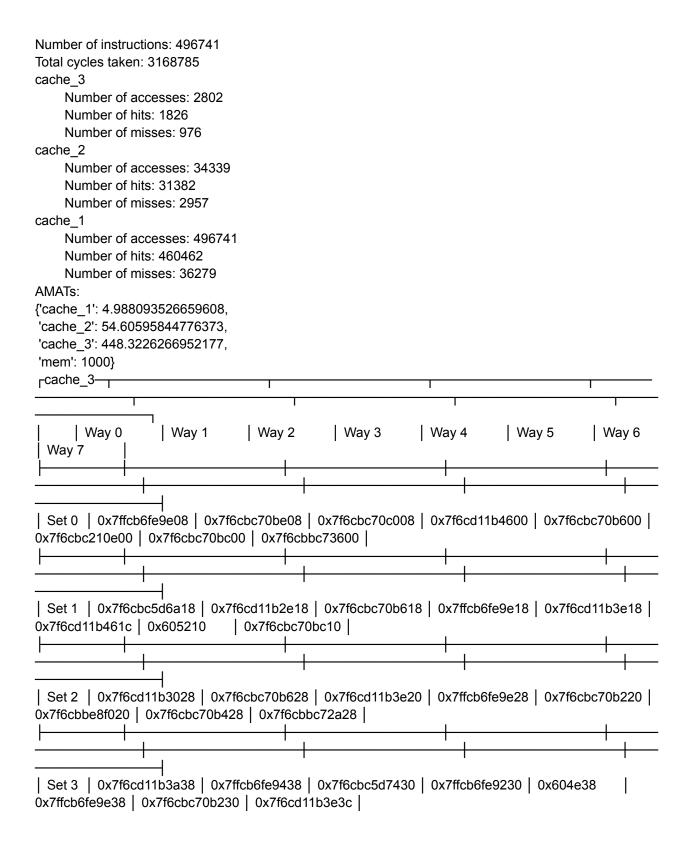
Number of instructions: 496741 Total cycles taken: 3632997 cache 3 Number of accesses: 3087 Number of hits: 2097 Number of misses: 990 cache_2 Number of accesses: 50683 Number of hits: 47417 Number of misses: 3266 cache_1 Number of accesses: 496741 Number of hits: 439318 Number of misses: 57423 AMATs: {'cache_1': 5.983468649986084, 'cache 2': 43.10978528921751, 'cache_3': 420.6997084548105, 'mem': 1000} rcache_3-r Way 0 Way 1 Way 2 Way 3 Way 4 Way 5 Way 6 Way 7

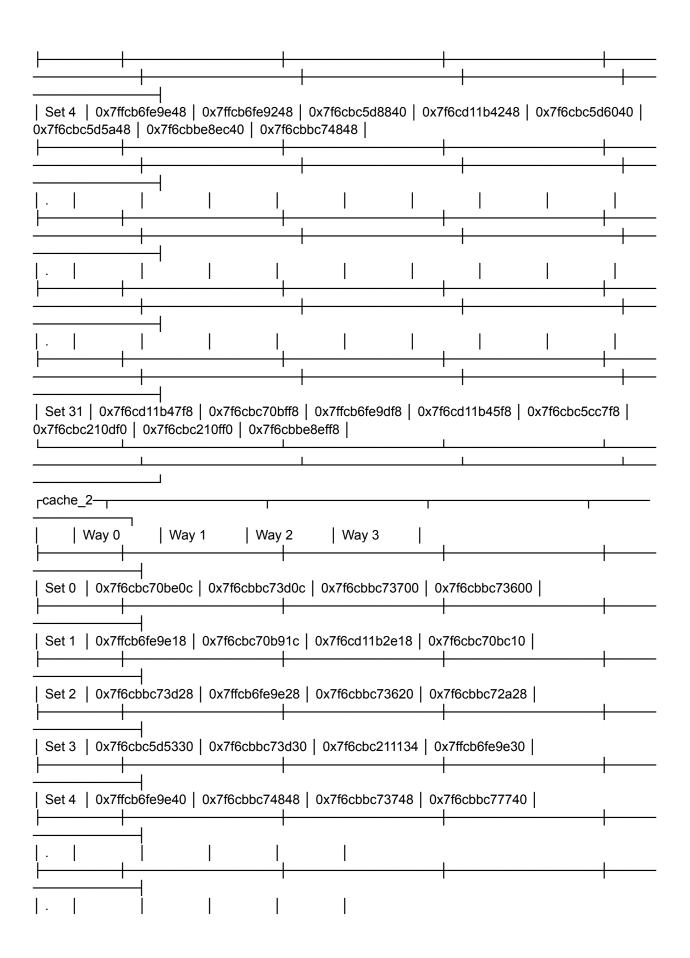


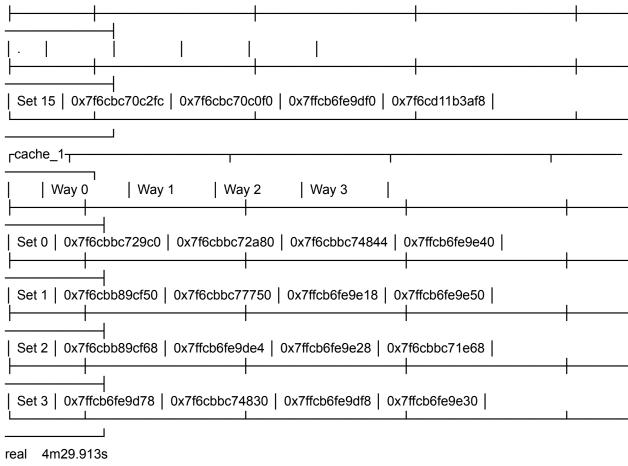


user 0m39.667s 0m4.893s sys

Associativity: 4







real 4m29.913s user 0m44.151s sys 0m5.153s Associativity: 8

Number of instructions: 496741 Total cycles taken: 2976081

cache_3

Number of accesses: 2566 Number of hits: 1605 Number of misses: 961

cache_2

Number of accesses: 25855 Number of hits: 23101 Number of misses: 2754

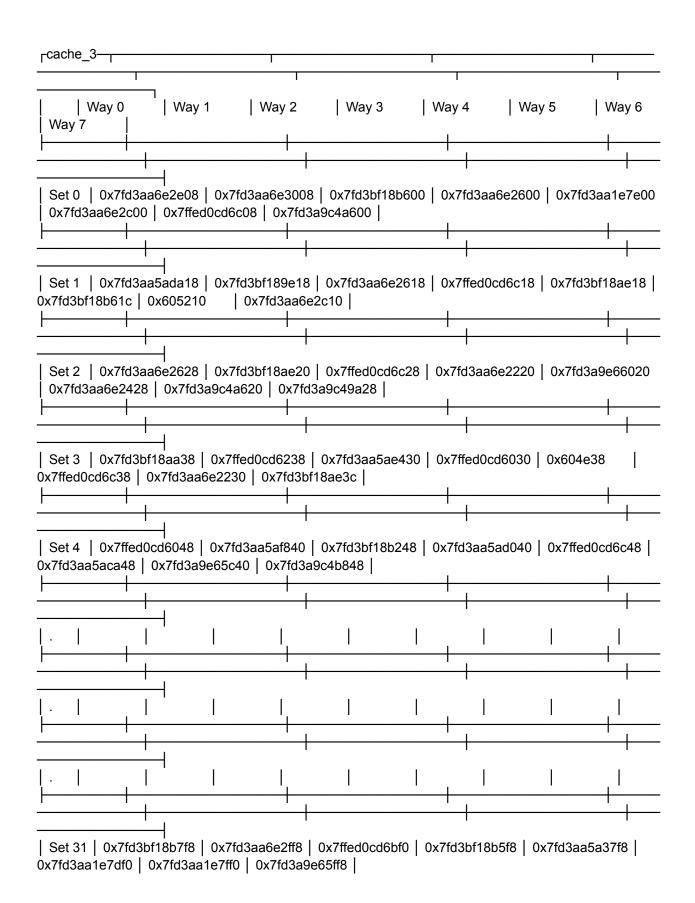
cache_1

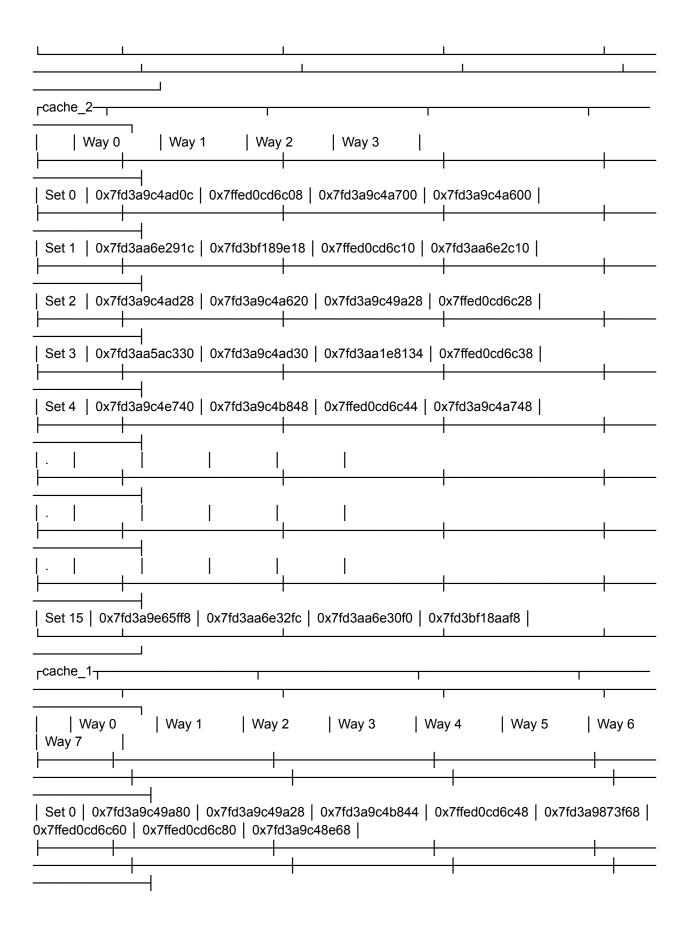
Number of accesses: 496741 Number of hits: 468906 Number of misses: 27835

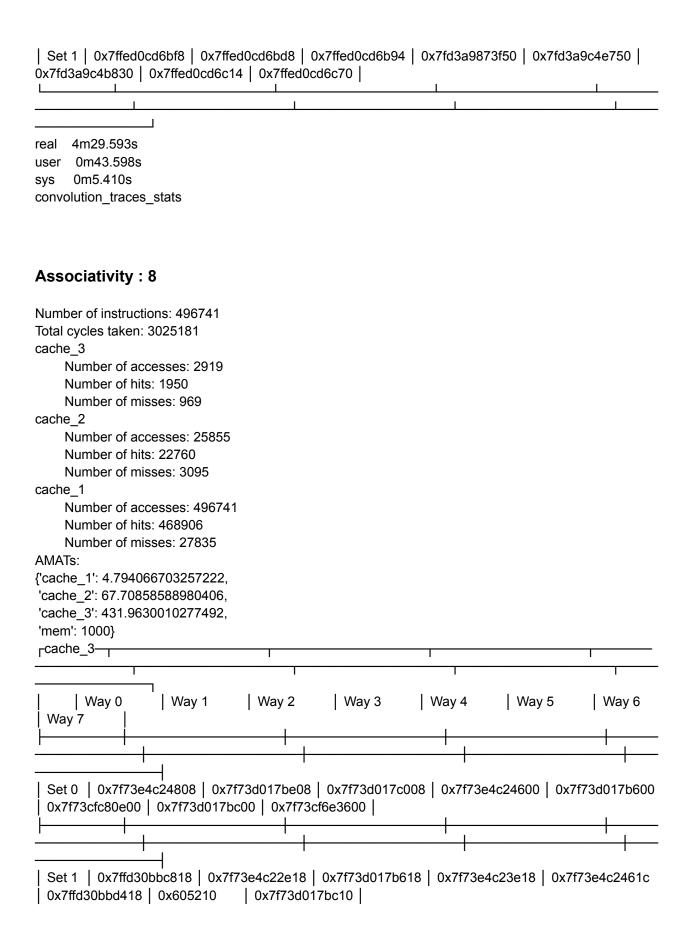
AMATs:

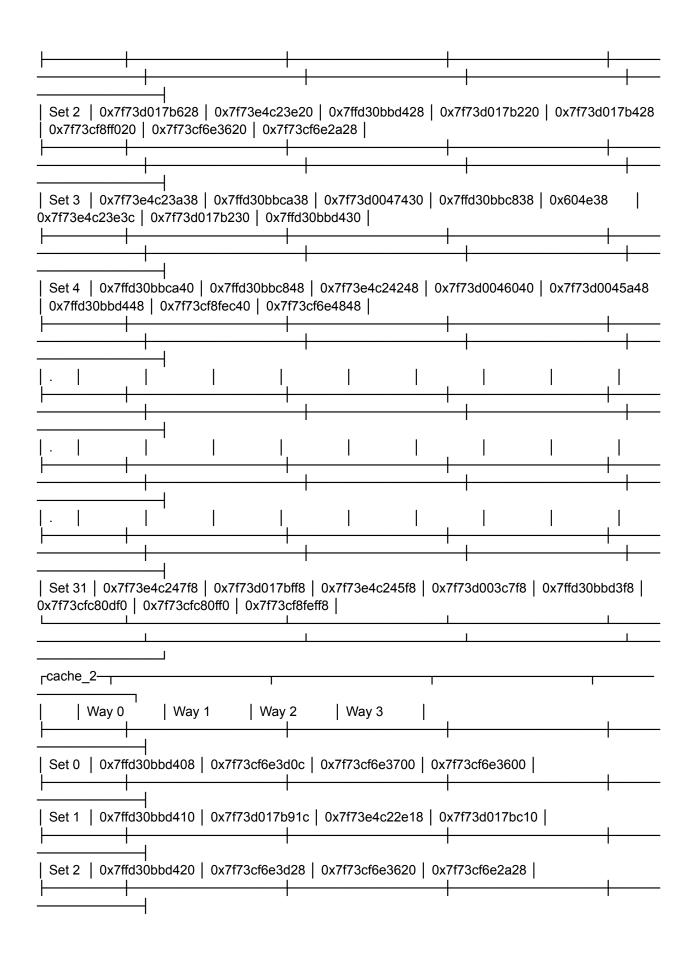
{'cache_1': 4.728794330461738, 'cache_2': 66.54374077628503, 'cache_3': 474.5128604832424,

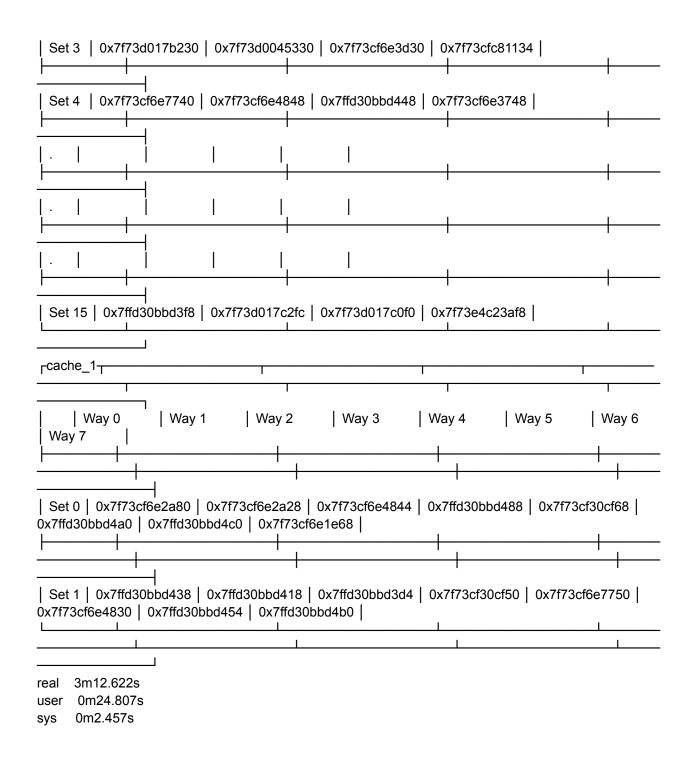
'mem': 1000}











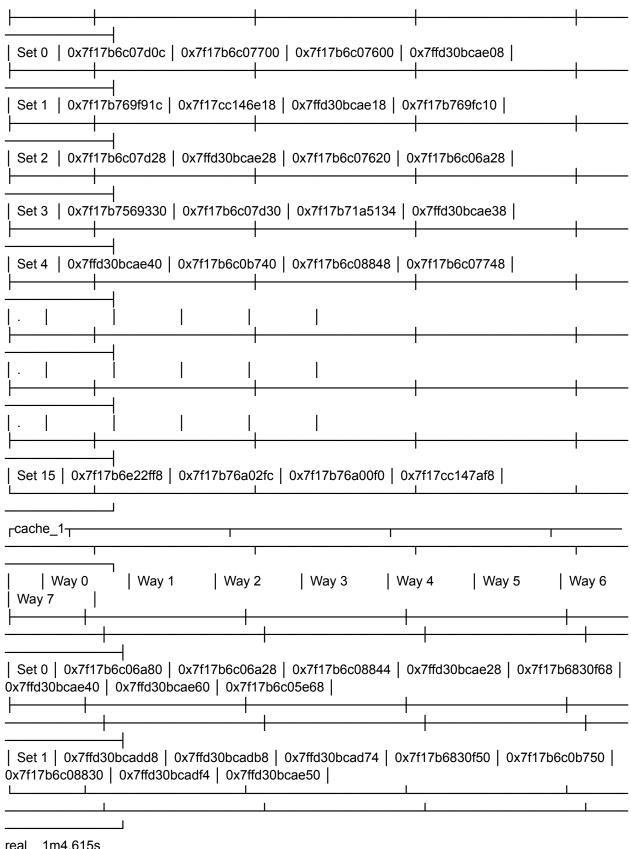
Varying Number of Caches:

Matrix Size 20x20:

Using 1 Cache:

Number of instructions: 186697

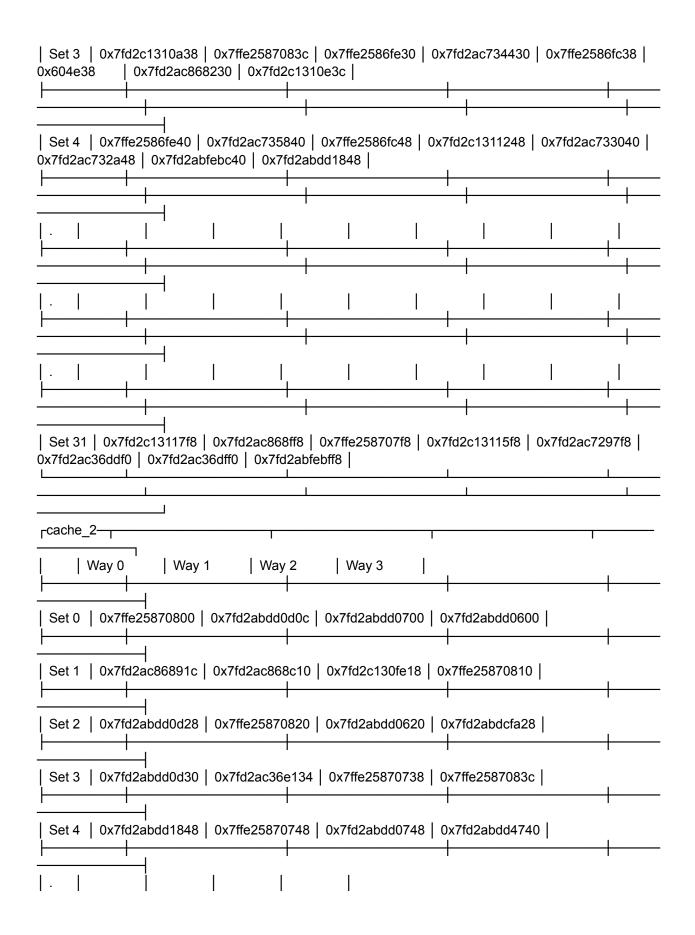
Total cycles taken: 13239697 cache_1 Number of accesses: 186697 Number of hits: 174609 Number of misses: 12088 AMATs: {'cache 1': 65.74662153114404, 'mem': 1000} rcache_1_ Way 0 Way 1 Way 3 Way 4 Way 5 Way 6 Way 2 Way 7 Set 0 | 0x7f4be19bda80 | 0x7f4be19bda28 | 0x7f4be19bf844 | 0x7ffe27032728 | 0x7f4be15e7f68 | 0x7ffe27032740 | 0x7ffe27032760 | 0x7f4be19bce68 | Set 1 | 0x7ffe270326d8 | 0x7ffe270326b8 | 0x7ffe27032674 | 0x7f4be15e7f50 | 0x7f4be19c2750 | 0x7f4be19bf830 | 0x7ffe270326f4 | 0x7ffe27032750 | 1m2.214s real 0m10.121s user 0m1.215s sys **Using 2 Cache:** Number of instructions: 186697 Total cycles taken: 2553545 cache 2 Number of accesses: 11105 Number of hits: 9613 Number of misses: 1492 cache 1 Number of accesses: 186697 Number of hits: 174609 Number of misses: 12088 AMATs: {'cache 1': 10.73490671212252, 'cache 2': 150.35389464205312, 'mem': 1000} Way 0 Way 1 Way 2 Way 3

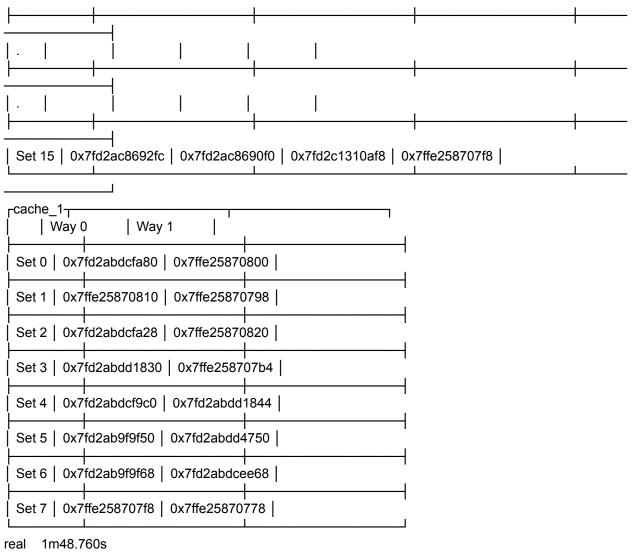


real 1m4.615s user 0m10.142s

Using 3 Cache:

```
Number of instructions: 186697
Total cycles taken: 1629177
cache 3
    Number of accesses: 1571
    Number of hits: 1009
    Number of misses: 562
cache_2
    Number of accesses: 16765
    Number of hits: 15063
    Number of misses: 1702
cache 1
    Number of accesses: 186697
    Number of hits: 167123
    Number of misses: 19574
AMATs:
{'cache 1': 7.549544455680323,
'cache 2': 62.46961792388624,
'cache_3': 457.7339274347549,
'mem': 1000}
rcache_3-r
      Way 0
                   Way 1
                               Way 2
                                            Way 3
                                                          Way 4
                                                                      Way 5
                                                                                    Way 6
 Way 7
 Set 0 | 0x7ffe25870808 | 0x7fd2ac868e08 | 0x7fd2ac869008 | 0x7fd2c1311600 | 0x7fd2ac868600
 0x7fd2ac36de00 | 0x7fd2ac868c00 | 0x7fd2abdd0600 |
 Set 1 | 0x7ffe25870818 | 0x7fd2ac733a18 | 0x7fd2c130fe18 | 0x7fd2ac868618 | 0x7fd2c1310e18 |
0x7fd2c131161c | 0x605210 | 0x7fd2ac868c10 |
 Set 2 | 0x7ffe25870828 | 0x7fd2c1310028 | 0x7fd2ac868628 | 0x7fd2c1310e20 | 0x7fd2ac868220
 0x7fd2abfec020 | 0x7fd2ac868428 | 0x7fd2abdcfa28 |
```





real 1m48.760s user 0m17.443s sys 0m2.055s

Matrix Size 32x32:

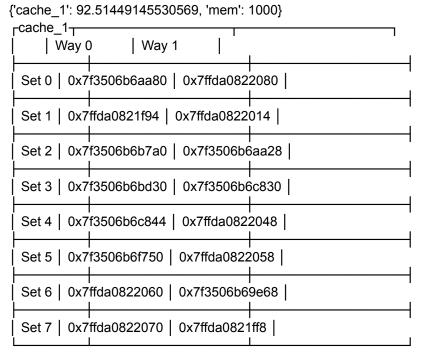
Using 1 Cache:

Number of instructions: 496741 Total cycles taken: 53426741

cache_1

Number of accesses: 496741 Number of hits: 451282 Number of misses: 45459

AMATs:



real 1m37.067s user 0m25.125s sys 0m2.881s

/home/grads/vmn5118/CachePerformanceOnMatMul

Using 2 Cache:

Number of instructions: 496741 Total cycles taken: 5419621

cache_2

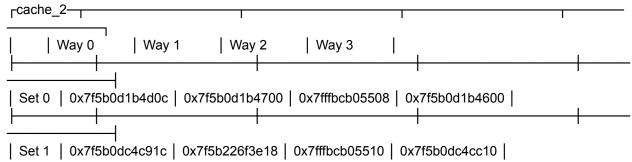
Number of accesses: 38782 Number of hits: 35931 Number of misses: 2851

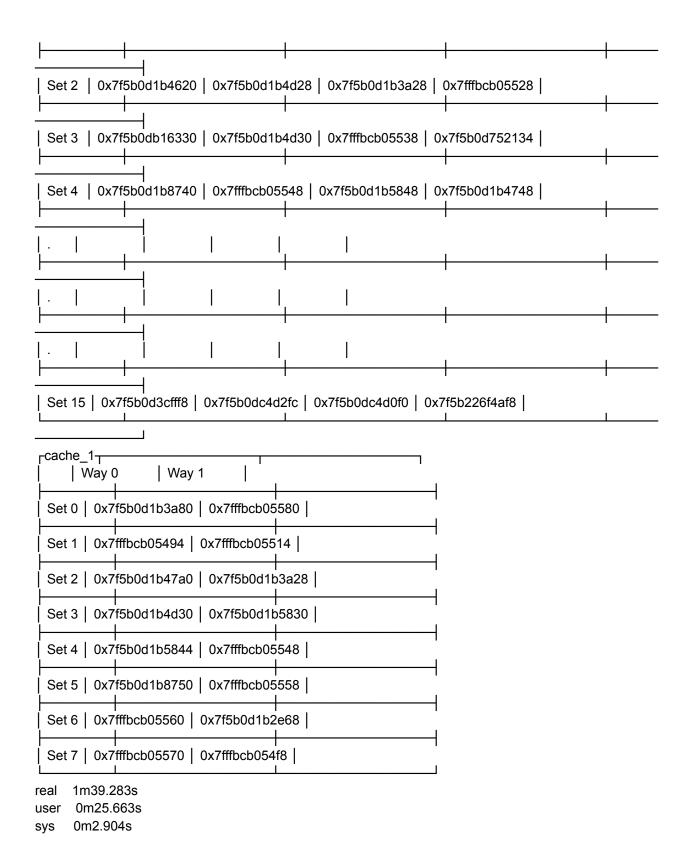
cache 1

Number of accesses: 496741 Number of hits: 451282 Number of misses: 45459

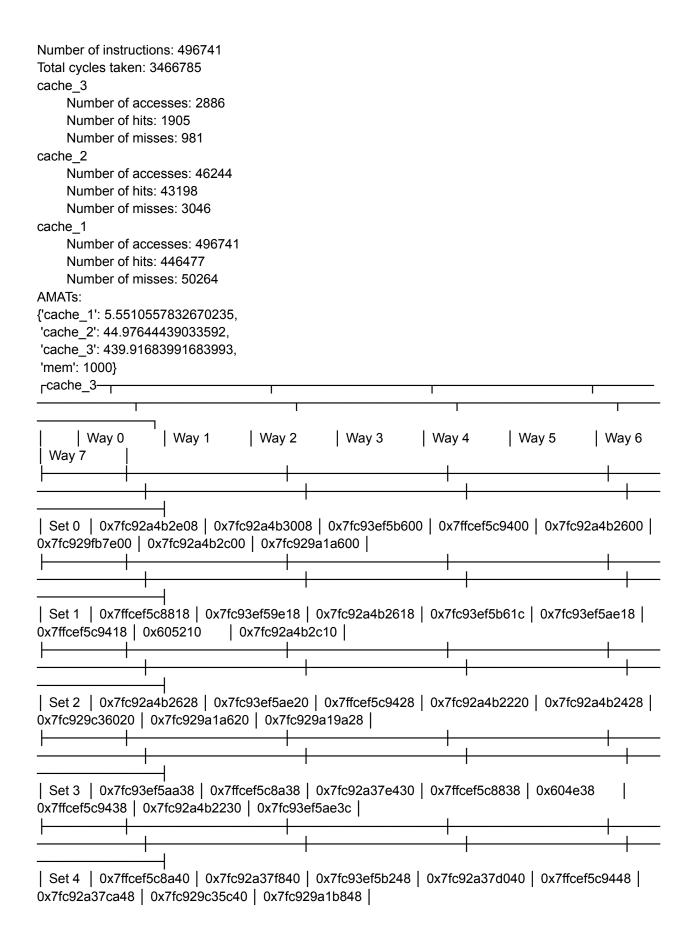
AMATs:

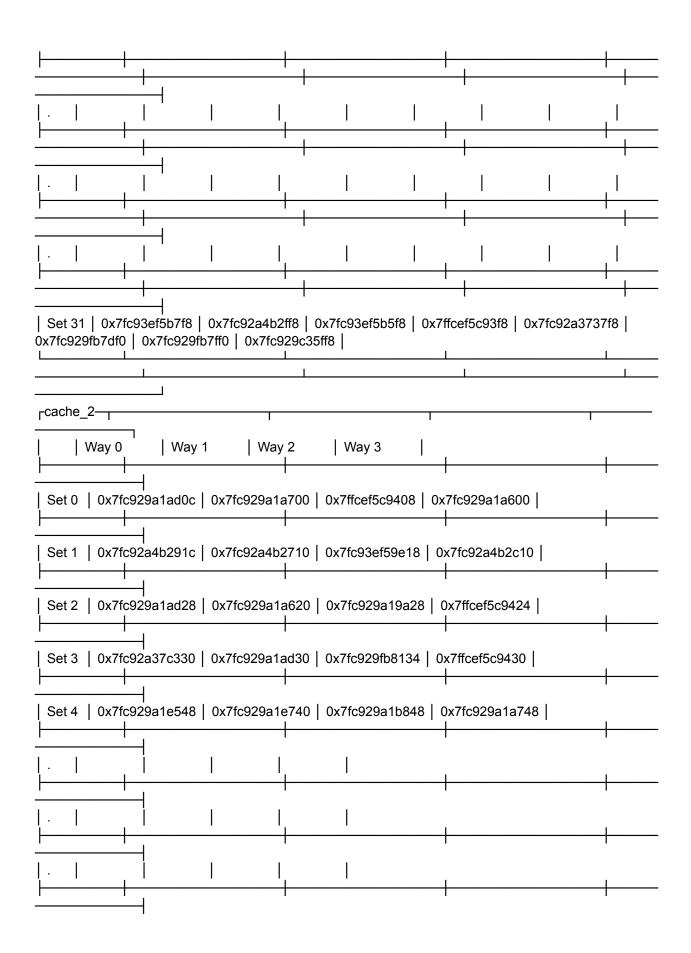
{'cache_1': 9.1917811165229, 'cache_2': 89.51348563766696, 'mem': 1000}

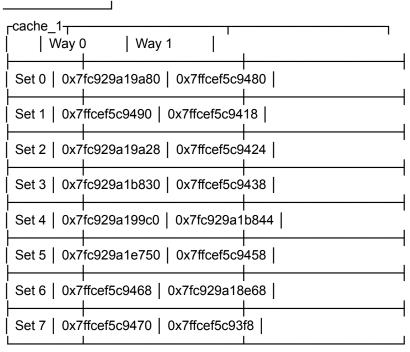




Using 3 Cache:







real 4m17.298s user 0m40.936s sys 0m5.006s