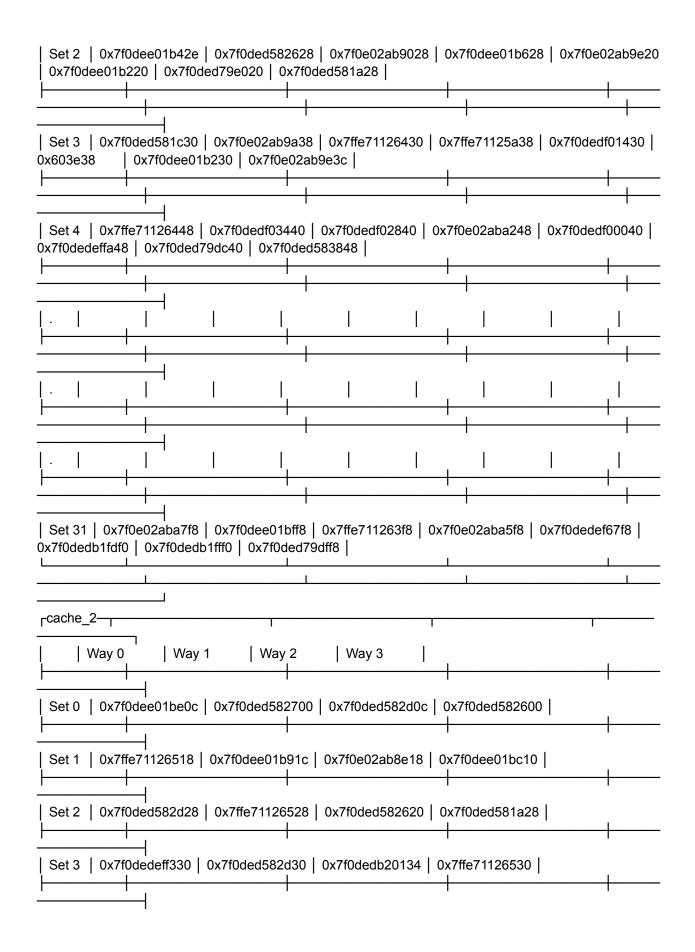
Scatter Operation:

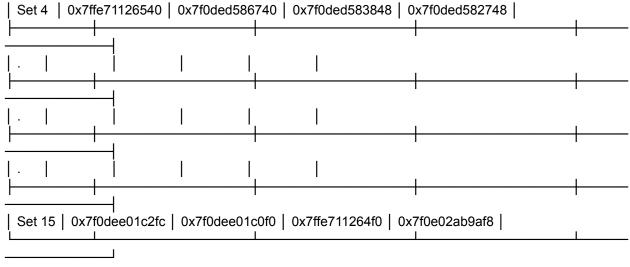
Varying Block Size:

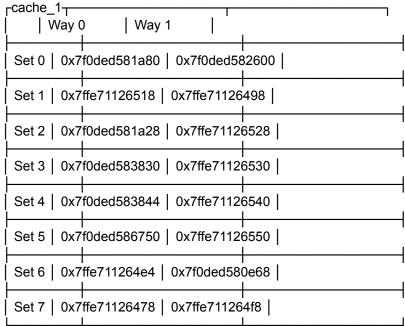
Matrix Size 128x128:

Block Size 16

Number of instructions: 240677 Total cycles taken: 12129641 cache_3 Number of accesses: 12871 Number of hits: 7516 Number of misses: 5355 cache 2 Number of accesses: 55522 Number of hits: 41382 Number of misses: 14140 cache 1 Number of accesses: 240677 Number of hits: 176617 Number of misses: 64060 AMATs: ('cache 1': 40.23945650492939, 'cache_2': 147.42483099027305, 'cache_3': 516.0515888431357, 'mem': 1000} rcache_3-r Way 2 Way 5 Way 0 Way 1 Way 3 Way 4 Way 6 Way 7 Set 0 | 0x7f0e02aba808 | 0x7f0dee01be08 | 0x7f0dee01c008 | 0x7f0e02aba600 | 0x7f0dee01b600 0x7f0dedb1fe00 | 0x7f0dee01bc00 | 0x7f0ded582600 | Set 1 | 0x7f0ded582610 | 0x7f0dedf00a18 | 0x7f0e02ab8e18 | 0x7f0dee01b618 | 0x7f0e02ab9e18 0x7f0e02aba61c | 0x604210 0x7f0dee01bc10







real 2m6.676s user 0m21.227s sys 0m2.459s

/home/grads/vmn5118/CachePerformanceOnMatMul

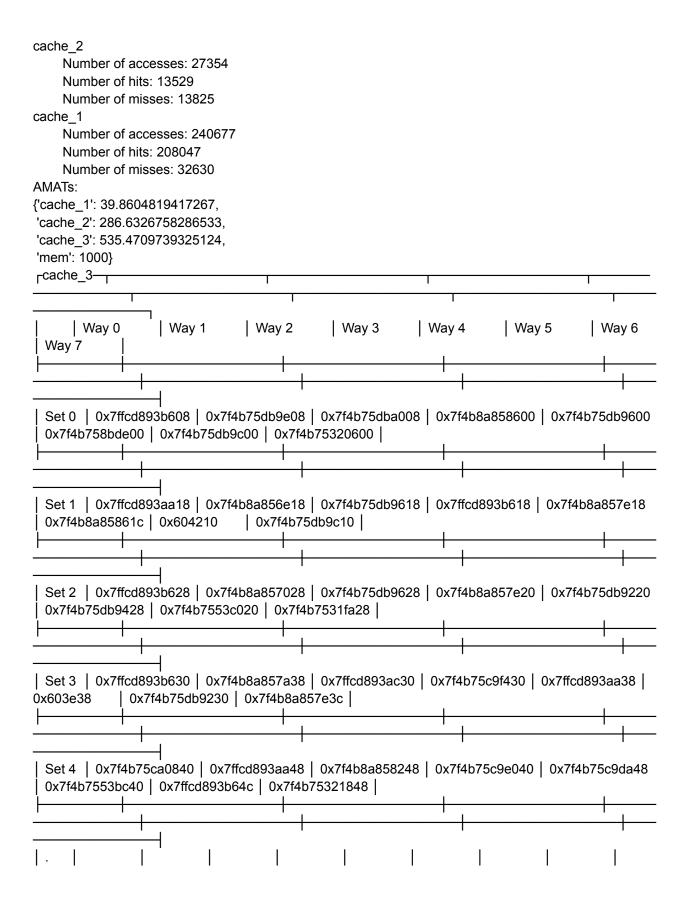
Block size 32

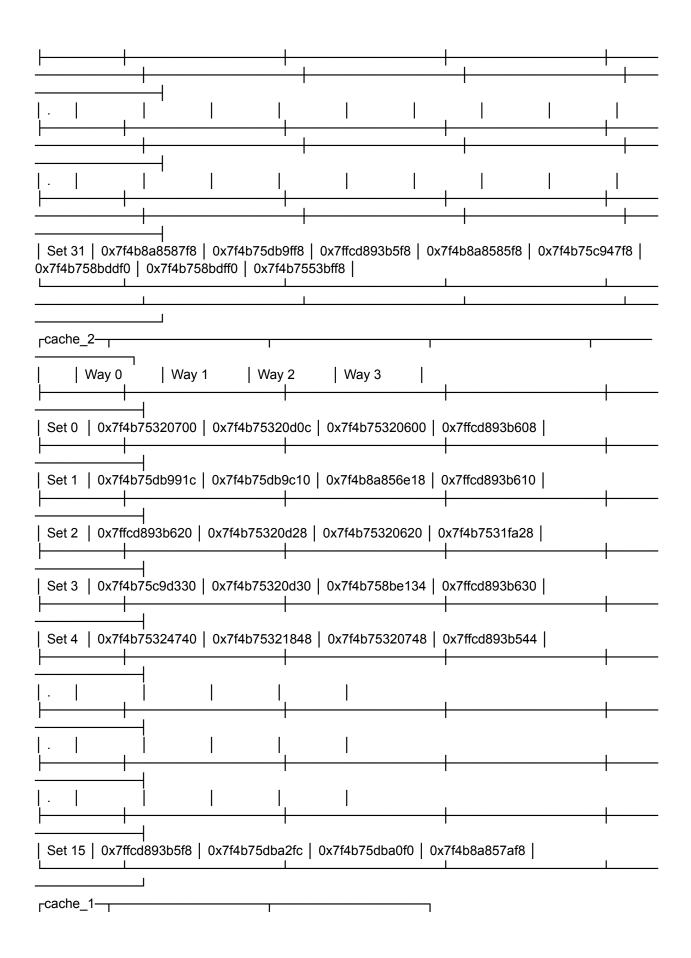
Number of instructions: 240677 Total cycles taken: 11459193

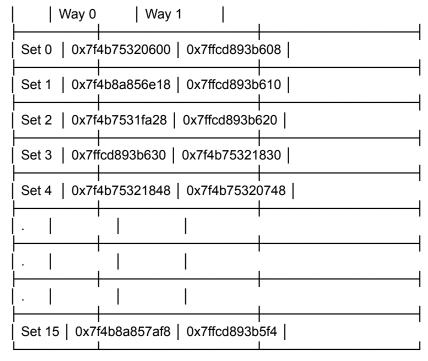
cache 3

Number of accesses: 12506

Number of hits: 7060 Number of misses: 5446







real 6m29.509s user 0m12.900s sys 0m1.262s

Block Size 64

Number of instructions: 240677 Total cycles taken: 10740613

cache_3

Number of accesses: 9074 Number of hits: 3649 Number of misses: 5425

cache_2

Number of accesses: 16171 Number of hits: 5858 Number of misses: 10313

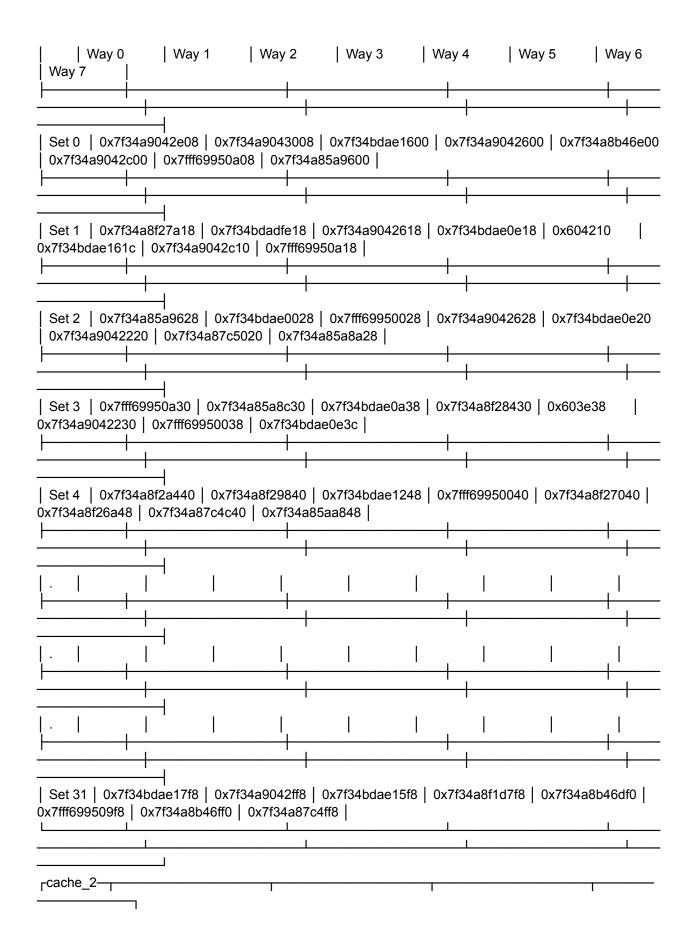
cache 1

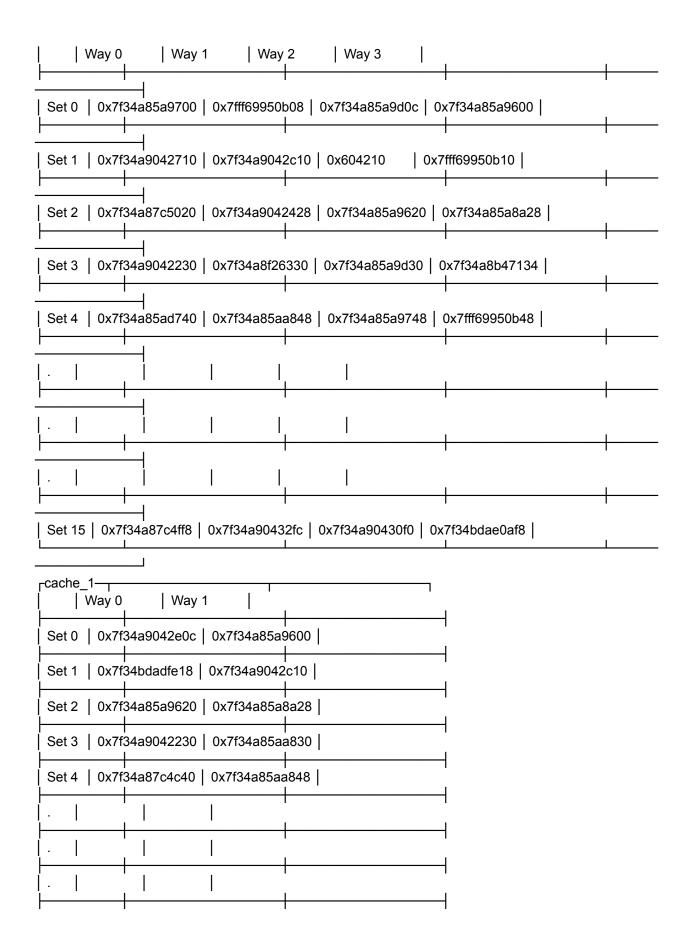
Number of accesses: 240677 Number of hits: 221324 Number of misses: 19353

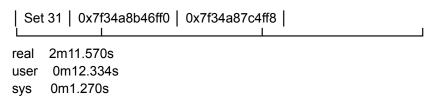
AMATs:

{'cache_1': 38.07407508908841, 'cache_2': 461.05912107768984, 'cache_3': 697.862023363456,

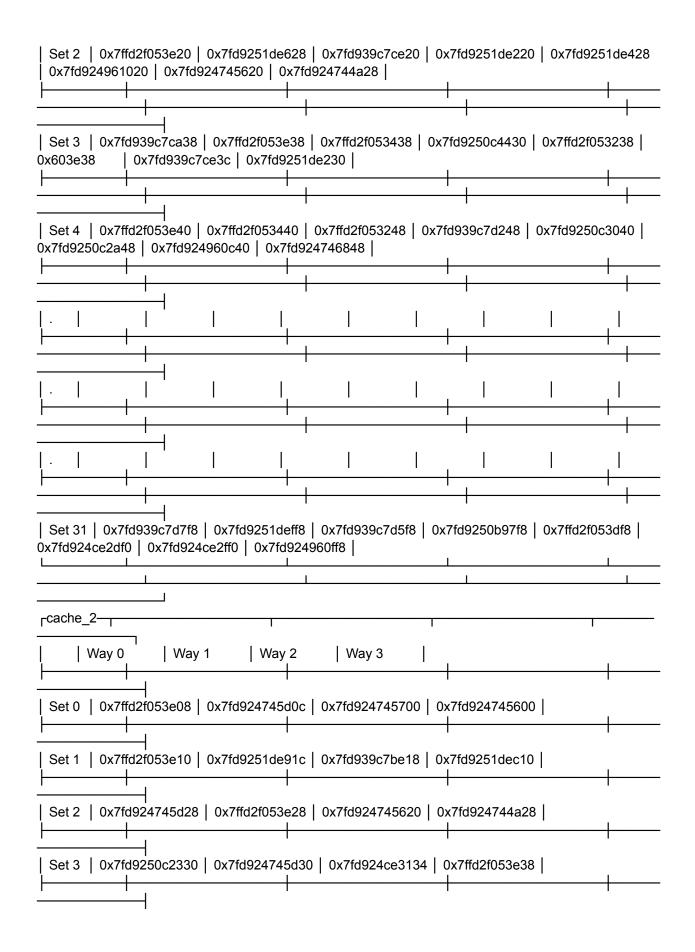
'mem': 1000} rcache_3—

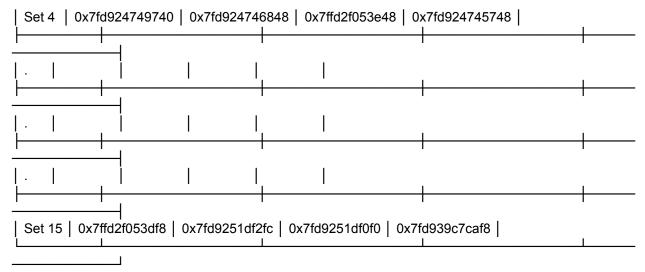


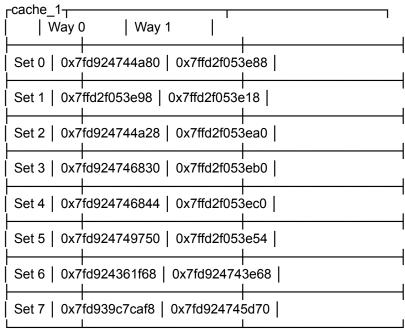




Matrix Size 150x150: Block size 16: Number of instructions: 262352 Total cycles taken: 14909288 cache 3 Number of accesses: 16217 Number of hits: 9162 Number of misses: 7055 cache 2 Number of accesses: 55964 Number of hits: 38005 Number of misses: 17959 cache 1 Number of accesses: 262352 Number of hits: 199271 Number of misses: 63081 AMATs: {'cache_1': 46.130144978142695, 'cache 2': 187.6949286679934, 'cache_3': 535.0373065301844, 'mem': 1000} rcache_3---| Way 0 Way 1 Way 2 Way 3 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7fd939c7d808 | 0x7fd9251dee08 | 0x7fd9251df008 | 0x7fd939c7d600 | 0x7fd9251de600 0x7fd924ce2e00 | 0x7fd9251dec00 | 0x7fd924745600 | Set 1 | 0x7ffd2f053218 | 0x7fd939c7be18 | 0x7fd9251de618 | 0x7fd939c7ce18 | 0x7fd939c7d61c | 0x7ffd2f053e18 | 0x604210 0x7fd9251dec10







real 2m43.532s user 0m14.599s sys 0m1.458s

Block size 32

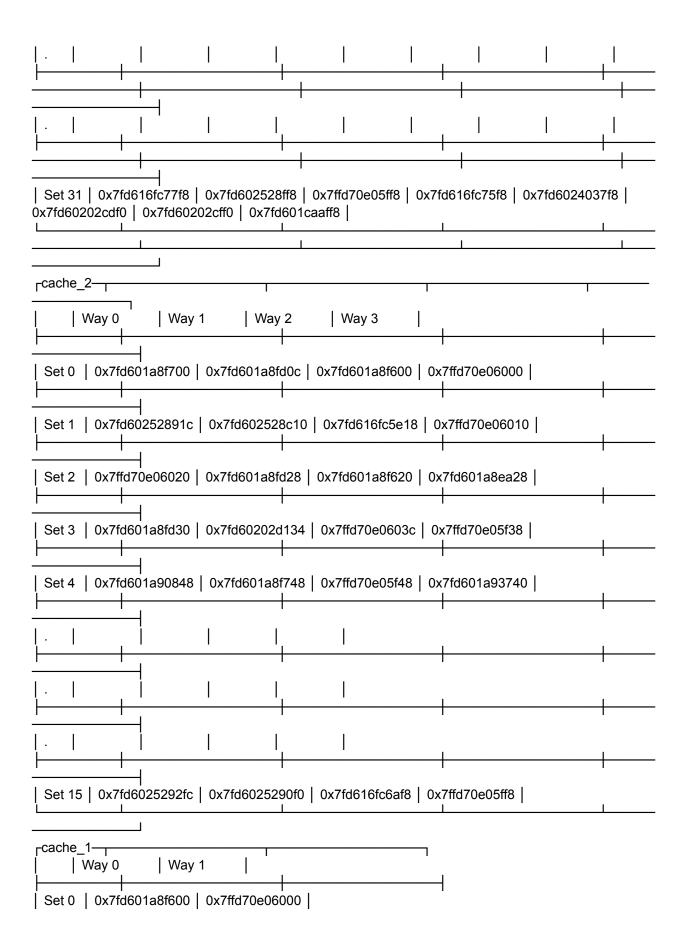
umber of instructions: 262352 Total cycles taken: 14496496

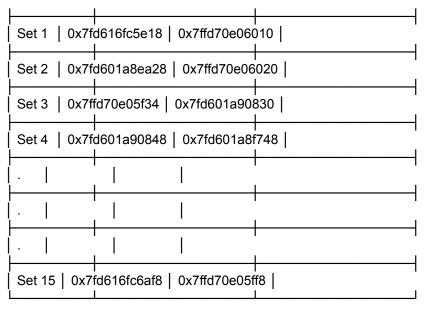
cache_3

Number of accesses: 15459 Number of hits: 8467 Number of misses: 6992

cache_2

Number of accesses: 34008 Number of hits: 16734 Number of misses: 17274 cache 1 Number of accesses: 262352 Number of hits: 223707 Number of misses: 38645 AMATs: ('cache 1': 44.67969836102323, 'cache_2': 296.5314070231897, 'cache 3': 552.2931625590271, 'mem': 1000} rcache_3-r Way 1 Way 2 Way 3 Way 0 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7ffd70e06008 | 0x7fd602528e08 | 0x7fd602529008 | 0x7fd616fc7600 | 0x7fd602528600 0x7fd60202ce00 | 0x7fd602528c00 | 0x7fd601a8f600 | Set 1 | 0x7ffd70e06018 | 0x7fd60240da18 | 0x7fd616fc5e18 | 0x7fd602528618 | 0x7fd616fc6e18 | 0x7fd616fc761c | 0x604210 0x7fd602528c10 Set 2 | 0x7ffd70e06020 | 0x7fd602528628 | 0x7fd616fc6e20 | 0x7fd602528220 | 0x7fd602528428 0x7fd601cab020 | 0x7fd601a8f620 | 0x7fd601a8ea28 | Set 3 | 0x7ffd70e06030 | 0x7fd616fc6a38 | 0x7ffd70e05630 | 0x7fd60240e430 | 0x7ffd70e05438 | 0x7fd602528230 | 0x7fd616fc6e3c | 0x603e38 Set 4 | 0x7ffd70e05640 | 0x7fd60240f840 | 0x7ffd70e05448 | 0x7fd616fc7248 | 0x7fd60240d040 | 0x7fd60240ca48 | 0x7fd601caac40 | 0x7fd601a90848 |





real 3m2.989s user 0m13.988s sys 0m1.422s

Block size 64

Number of instructions: 262352 Total cycles taken: 13651720 cache_3

Number of accesses: 11623 Number of hits: 4789 Number of misses: 6834

cache_2

Number of accesses: 20328 Number of hits: 7049 Number of misses: 13279

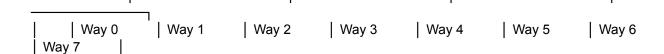
cache_1

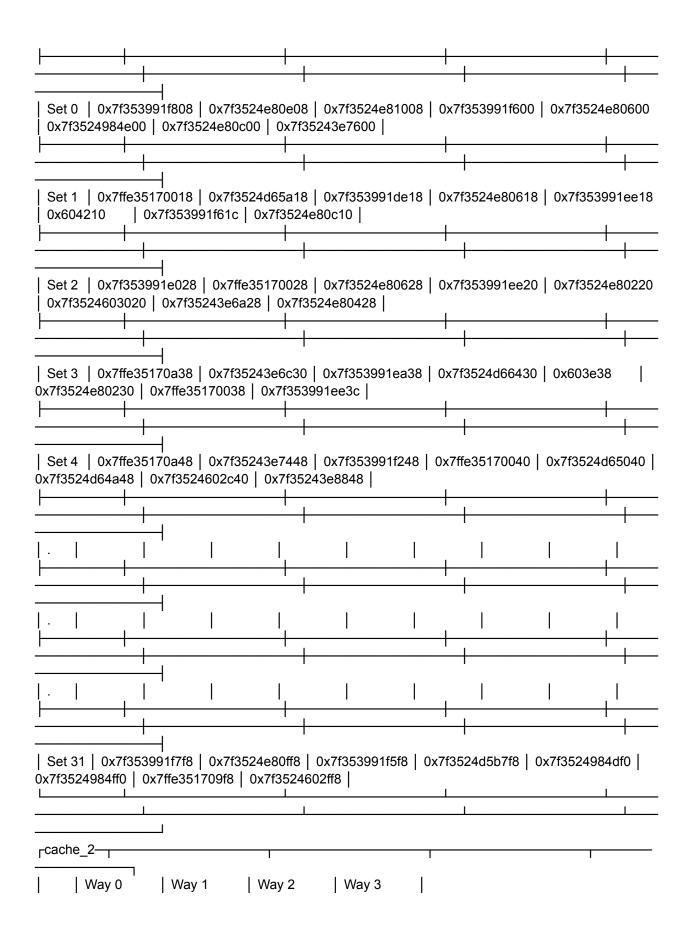
Number of accesses: 262352 Number of hits: 238301 Number of misses: 24051

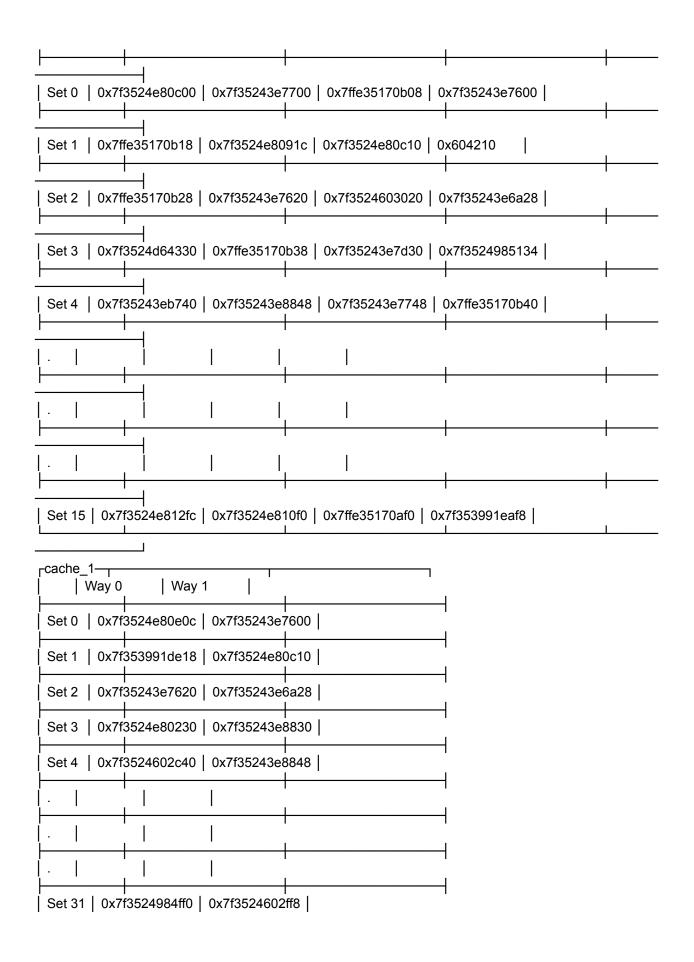
AMATs:

{'cache_1': 43.66613844984418, 'cache_2': 465.408787767391, 'cache_3': 687.9721242364277,

'mem': 1000} rcache_3---







real 5m10.111s user 0m13.685s sys 0m1.382s

Varying Associativity:

Matrix Size 128x128:

Associativity: 2

Number of instructions: 240677

Total cycles taken: 12129641

cache 3

Number of accesses: 12871 Number of hits: 7516 Number of misses: 5355

cache 2

Number of accesses: 55522 Number of hits: 41382 Number of misses: 14140

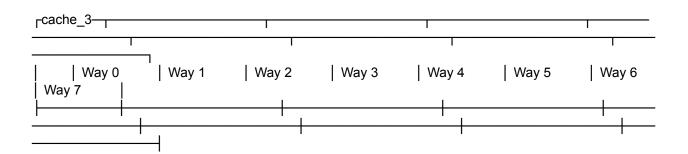
cache_1

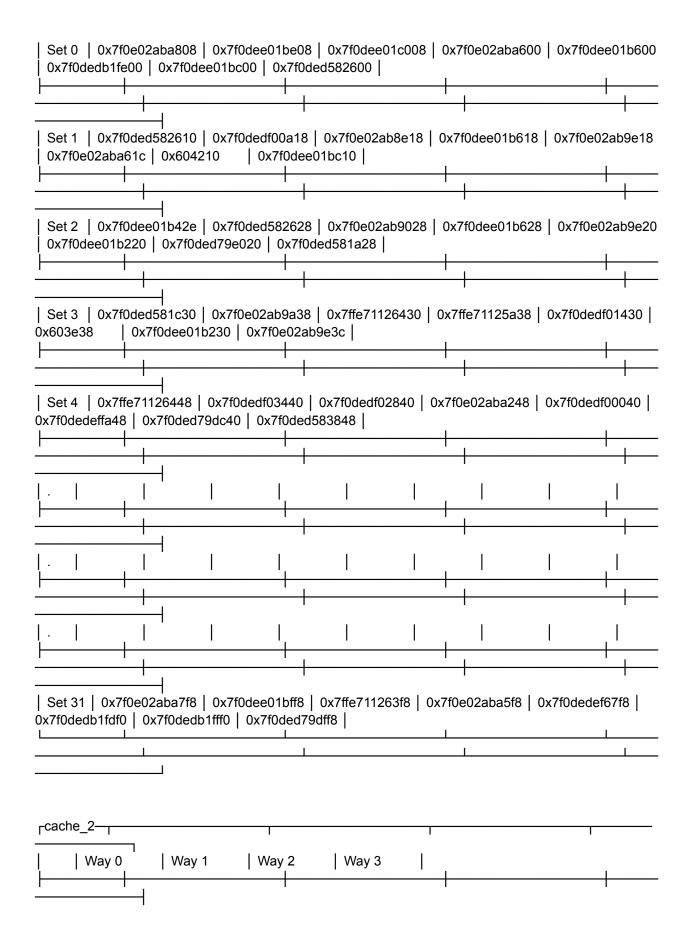
Number of accesses: 240677 Number of hits: 176617 Number of misses: 64060

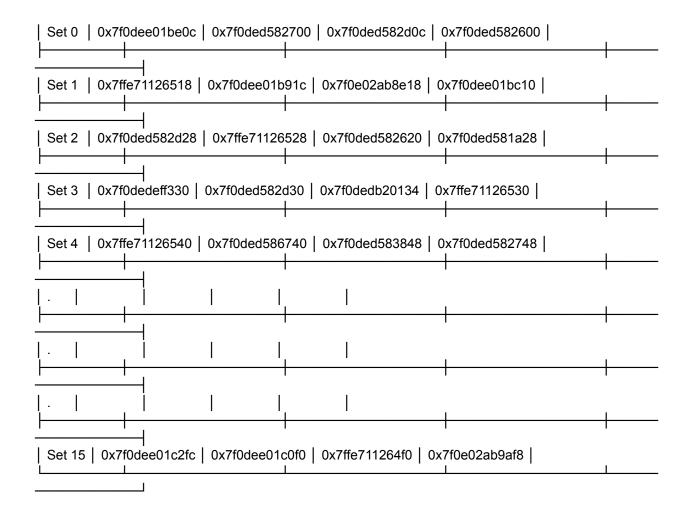
AMATs:

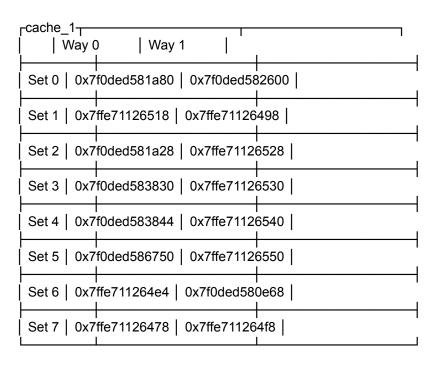
{'cache_1': 40.23945650492939, 'cache_2': 147.42483099027305, 'cache_3': 516.0515888431357,

'mem': 1000}





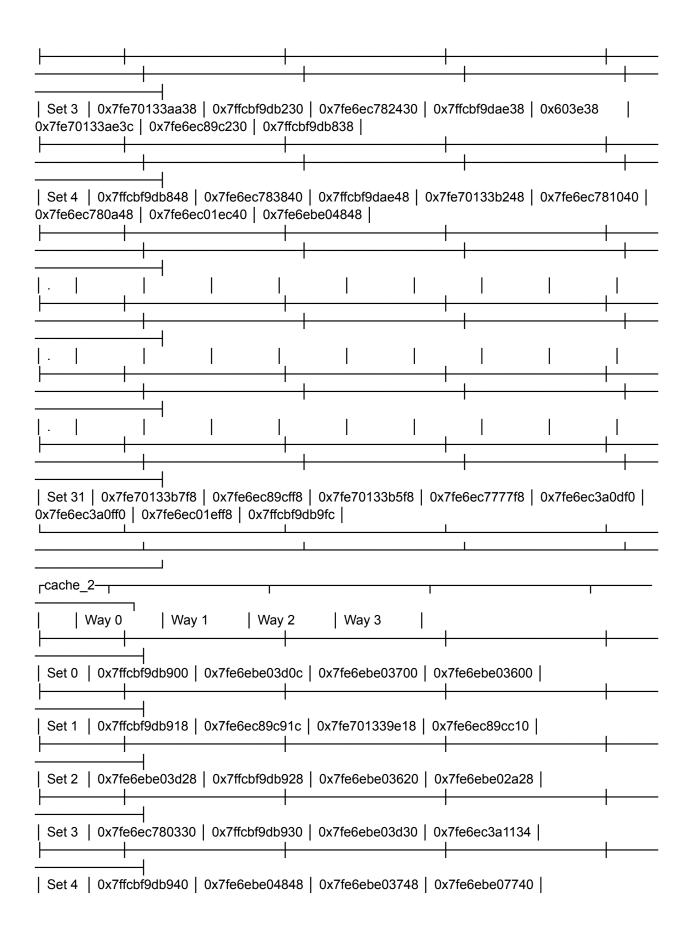


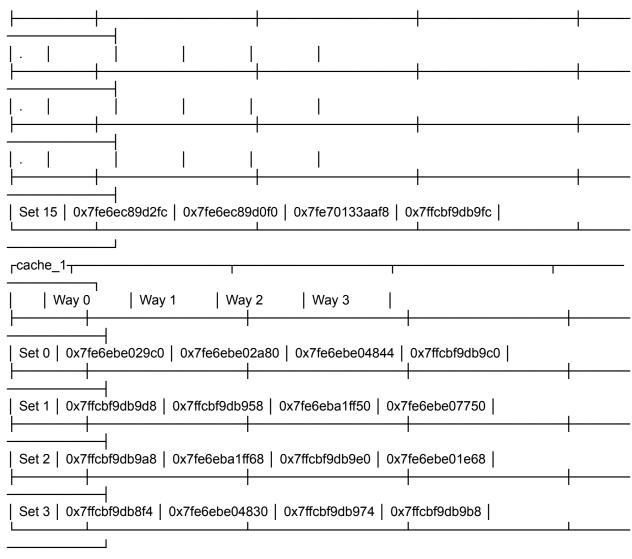


real 2m6.676s user 0m21.227s sys 0m2.459s

Associativity: 4

Number of instructions: 240677 Total cycles taken: 11928753 cache 3 Number of accesses: 11412 Number of hits: 6213 Number of misses: 5199 cache_2 Number of accesses: 54094 Number of hits: 41532 Number of misses: 12562 cache 1 Number of accesses: 240677 Number of hits: 179998 Number of misses: 60679 AMATs: \'cache 1': 37.56168936561113, 'cache_2': 145.0181728678322, 'cache 3': 555.5730809674028, 'mem': 1000} rcache 3-r Way 1 Way 2 Way 3 Way 0 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7fe70133b808 | 0x7fe6ec89ce08 | 0x7fe6ec89d008 | 0x7fe70133b600 | 0x7fe6ec89c600 0x7fe6ec3a0e00 | 0x7fe6ec89cc00 | 0x7fe6ebe03600 | Set 1 | 0x7fe6ec781a18 | 0x7ffcbf9dae10 | 0x7fe701339e18 | 0x7fe6ec89c618 | 0x7fe70133ae18 | 0x7fe70133b61c | 0x604210 0x7fe6ec89cc10 Set 2 | 0x7fe6ec89c42e | 0x7fe6ec89c628 | 0x7fe70133ae20 | 0x7fe6ec89c220 | 0x7ffcbf9db828 | 0x7fe6ec01f020 | 0x7fe6ebe03620 | 0x7fe6ebe02a28 |





real 2m25.925s user 0m23.403s sys 0m2.682s

Associativity: 8

Number of instructions: 240677 Total cycles taken: 12114705

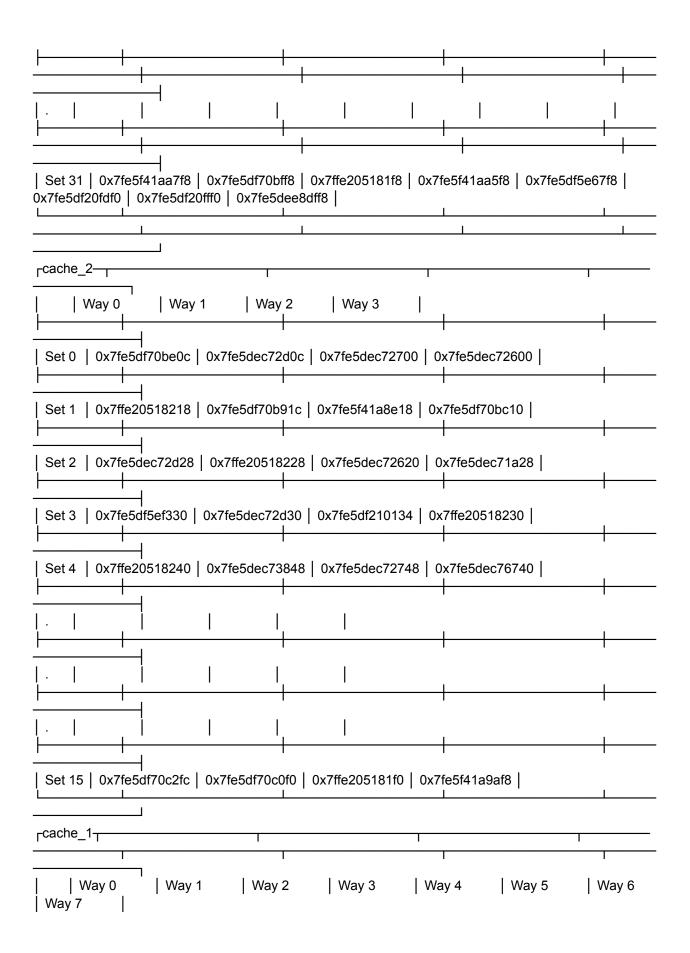
cache_3

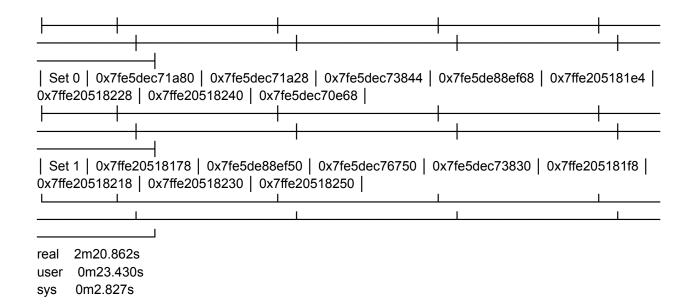
Number of accesses: 12501 Number of hits: 7306 Number of misses: 5195

cache_2

Number of accesses: 55231

Number of hits: 41511 Number of misses: 13720 cache_1 Number of accesses: 240677 Number of hits: 177116 Number of misses: 63561 AMATs: {'cache_1': 39.04848965988544, 'cache 2': 144.07256565932332, 'cache_3': 515.5667546596272, 'mem': 1000} rcache 3-r Way 0 Way 1 Way 2 Way 3 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7fe5df70be08 | 0x7fe5df70c008 | 0x7fe5f41aa600 | 0x7ffe20518208 | 0x7fe5df70b600 | 0x7fe5df20fe00 | 0x7fe5df70bc00 | 0x7fe5dec72600 | Set 1 | 0x7fe5df5f0a18 | 0x7fe5f41a8e18 | 0x7fe5df70b618 | 0x7ffe20518218 | 0x7fe5f41a9e18 | 0x7fe5f41aa61c | 0x604210 0x7fe5df70bc10 Set 2 | 0x7fe5f41a9028 | 0x7fe5df70b628 | 0x7fe5f41a9e20 | 0x7ffe20518228 | 0x7fe5df70b220 | 0x7fe5dee8e020 | 0x7fe5df70b428 | 0x7fe5dec71a28 | Set 3 | 0x7fe5f41a9a38 | 0x7ffe20517838 | 0x7fe5df5f1430 | 0x7ffe20517630 | 0x603e38 0x7ffe20518238 | 0x7fe5df70b230 | 0x7fe5f41a9e3c | Set 4 | 0x7ffe20518248 | 0x7ffe20517648 | 0x7fe5df5f2840 | 0x7fe5f41aa248 | 0x7fe5df5f0040 | 0x7fe5df5efa48 | 0x7fe5dee8dc40 | 0x7fe5dec73848 |





Matrix Size 150x150:

Associativity: 2

Associativity 2

Number of instructions: 262352 Total cycles taken: 15008504

cache 3

Number of accesses: 15863 Number of hits: 9114 Number of misses: 6749

cache_2

Number of accesses: 57681 Number of hits: 40345 Number of misses: 17336

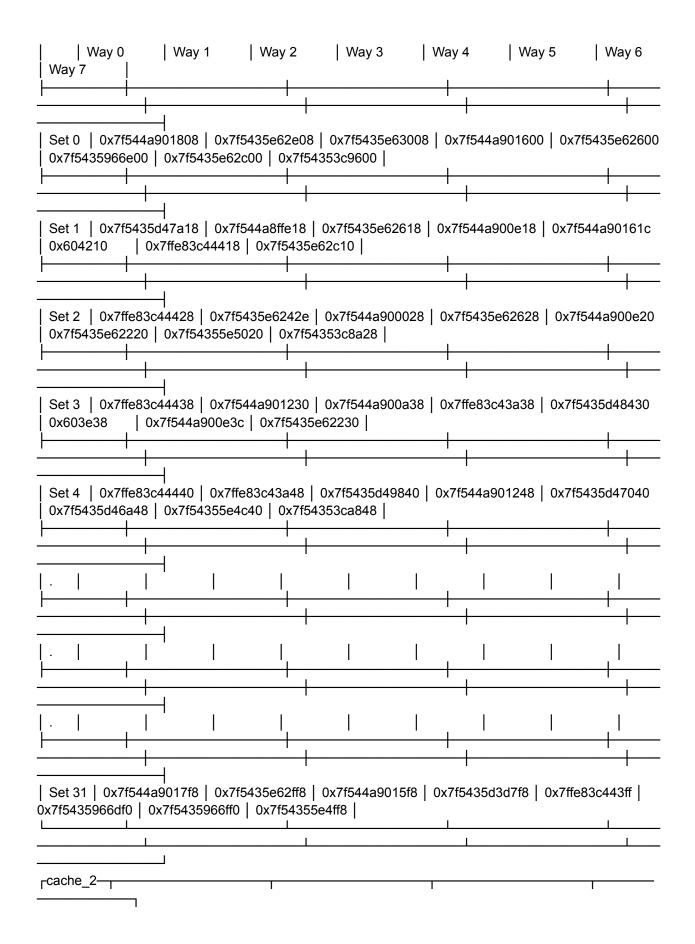
cache 1

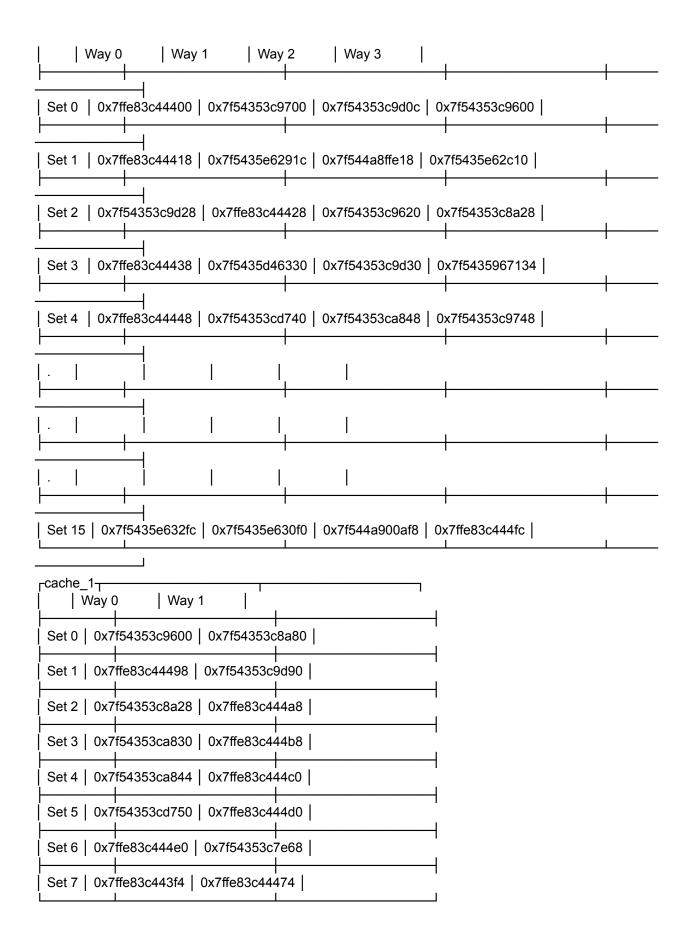
Number of accesses: 262352 Number of hits: 197694 Number of misses: 64658

AMATs:

{'cache_1': 43.86481338284825, 'cache_2': 173.92541558070158, 'cache_3': 525.4554623967724,

'mem': 1000}

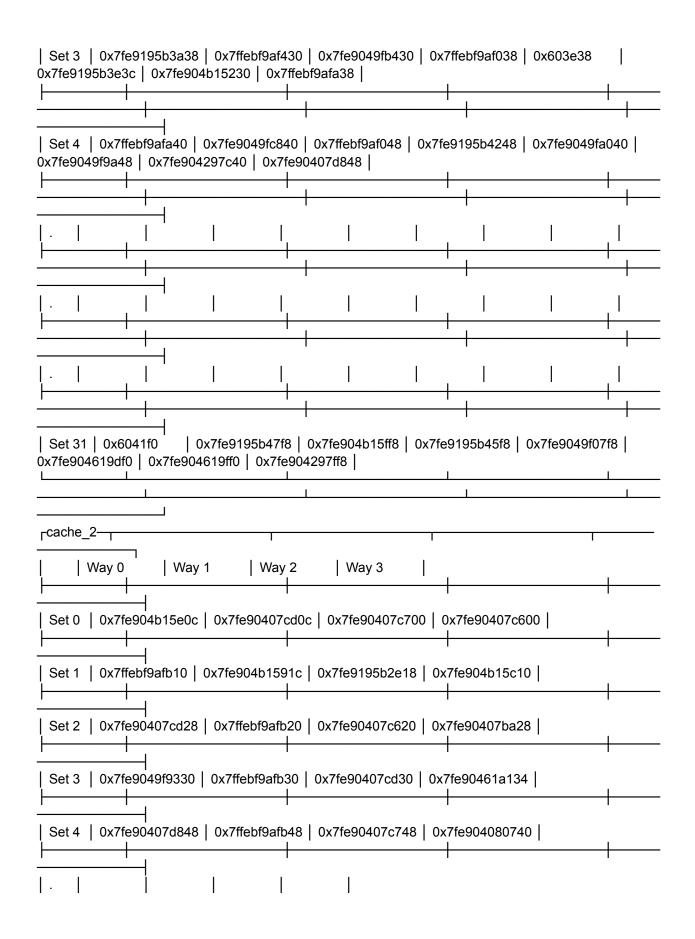


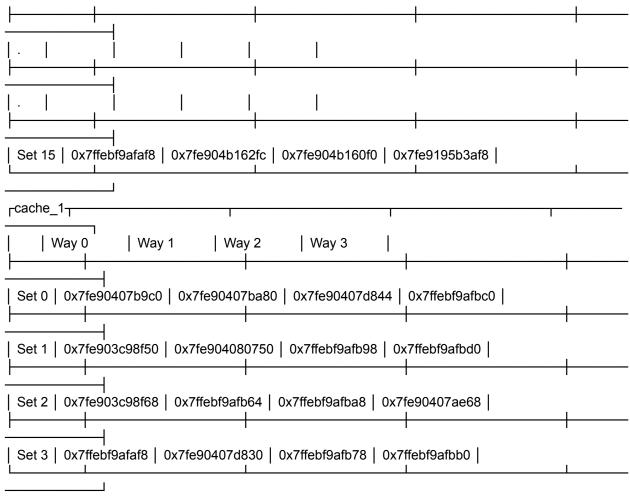


real 3m30.723s user 0m26.742s sys 0m3.196s

Associativity: 4

Number of instructions: 262352 Total cycles taken: 14978804 cache 3 Number of accesses: 16031 Number of hits: 9163 Number of misses: 6868 cache 2 Number of accesses: 56691 Number of hits: 38912 Number of misses: 17779 cache_1 Number of accesses: 262352 Number of hits: 196318 Number of misses: 66034 AMATs: ('cache 1': 46.738683263756215, 'cache 2': 181.71903915578292, 'cache_3': 528.4199363732769, 'mem': 1000} rcache_3-r Way 0 Way 1 Way 2 Way 3 Way 4 Way 5 Way 6 Way 7 Set 0 | 0x7fe9195b4808 | 0x7fe904b15e08 | 0x7fe904b16008 | 0x7fe9195b4600 | 0x7fe904b15600 0x7fe904619e00 | 0x7fe904b15c00 | 0x7fe90407c600 | Set 1 | 0x7ffebf9af010 | 0x7fe9195b2e18 | 0x7fe904b15618 | 0x7fe9195b3e18 | 0x7fe9195b461c | 0x7ffebf9afa18 | 0x7fe904b15c10 | 0x604210 Set 2 | 0x7fe904b1542e | 0x7fe904b15628 | 0x7fe9195b3e20 | 0x7fe904b15220 | 0x7ffebf9afa28 | 0x7fe904298020 | 0x7fe90407c620 | 0x7fe90407ba28 |





real 2m49.764s user 0m25.717s sys 0m3.167s

Associativity: 8

Number of instructions: 262352 Total cycles taken: 14786272

cache_3

Number of accesses: 15443

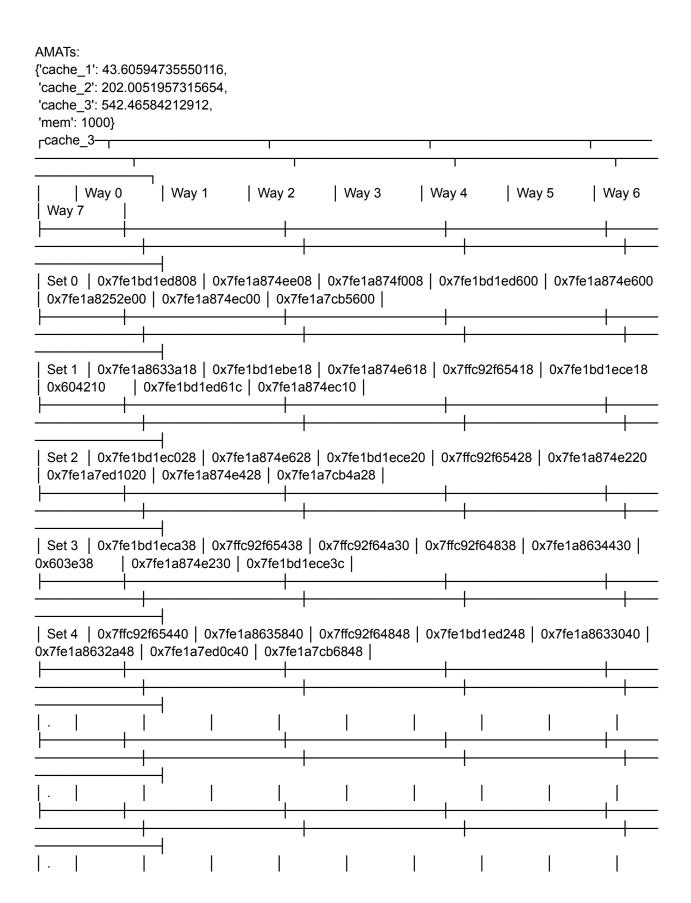
Number of hits: 8610 Number of misses: 6833

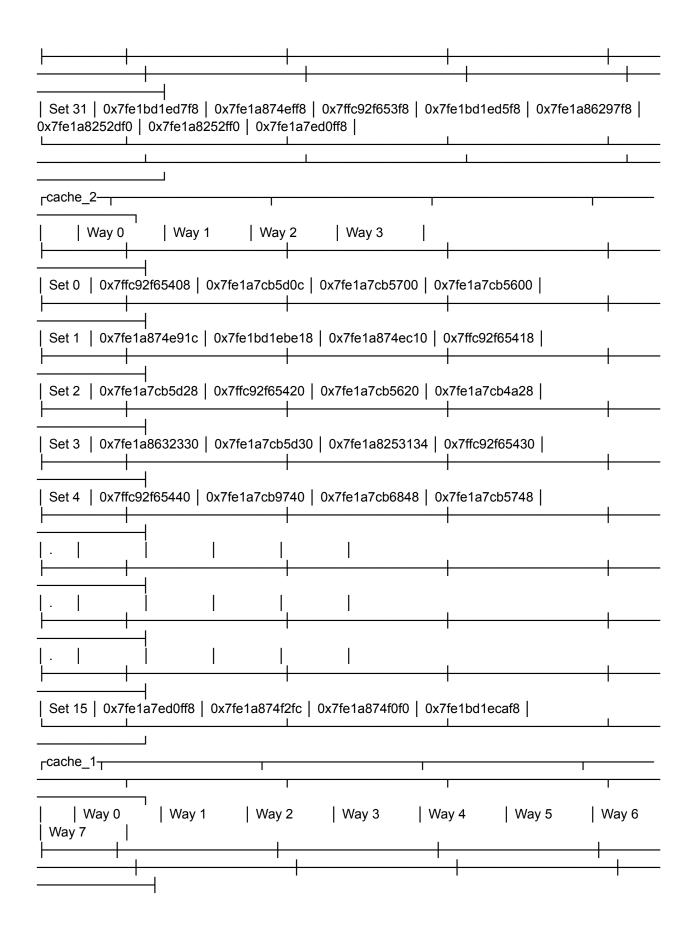
cache_2

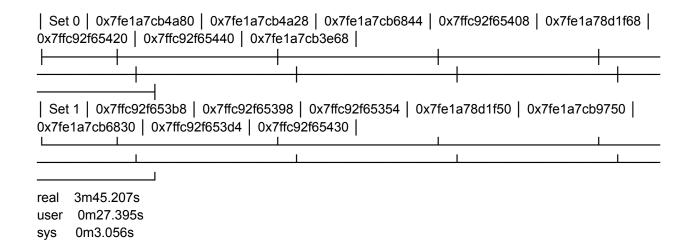
Number of accesses: 49503 Number of hits: 32529 Number of misses: 16974

cache_1

Number of accesses: 262352 Number of hits: 207018 Number of misses: 55334







Varying Number of Cache:

Matrix Size 128x128:

Using 1 Cache:

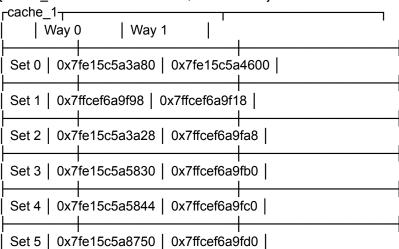
Number of instructions: 240677 Total cycles taken: 76937677

cache 1

Number of accesses: 240677 Number of hits: 176626 Number of misses: 64051

AMATs:

{'cache_1': 267.1284626283359, 'mem': 1000}





real 1m50.739s user 0m18.504s sys 0m2.226s

/home/grads/vmn5118/CachePerformanceOnMatMul

Using 2 Cache:

Number of instructions: 240677 Total cycles taken: 20140349

cache 2

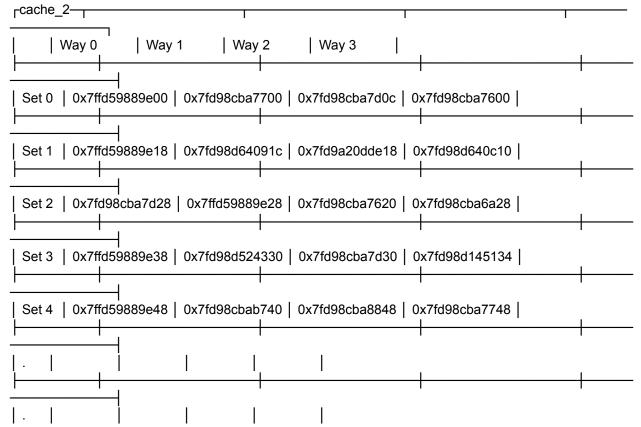
Number of accesses: 50091 Number of hits: 37477 Number of misses: 12614

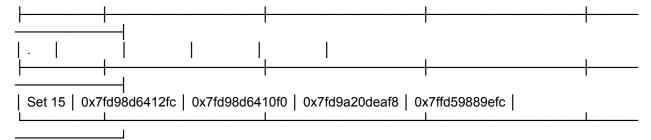
cache 1

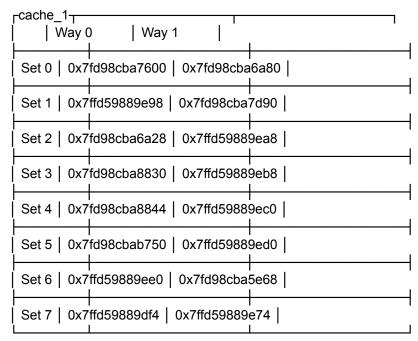
Number of accesses: 240677 Number of hits: 184319 Number of misses: 56358

AMATs:

{'cache_1': 63.714320425198515, 'cache_2': 267.82168453414783, 'mem': 1000}







real 2m8.047s user 0m21.266s sys 0m2.574s

Using 3 Cache:

Number of instructions: 240677 Total cycles taken: 12129641

cache 3

Number of accesses: 12871 Number of hits: 7516 Number of misses: 5355

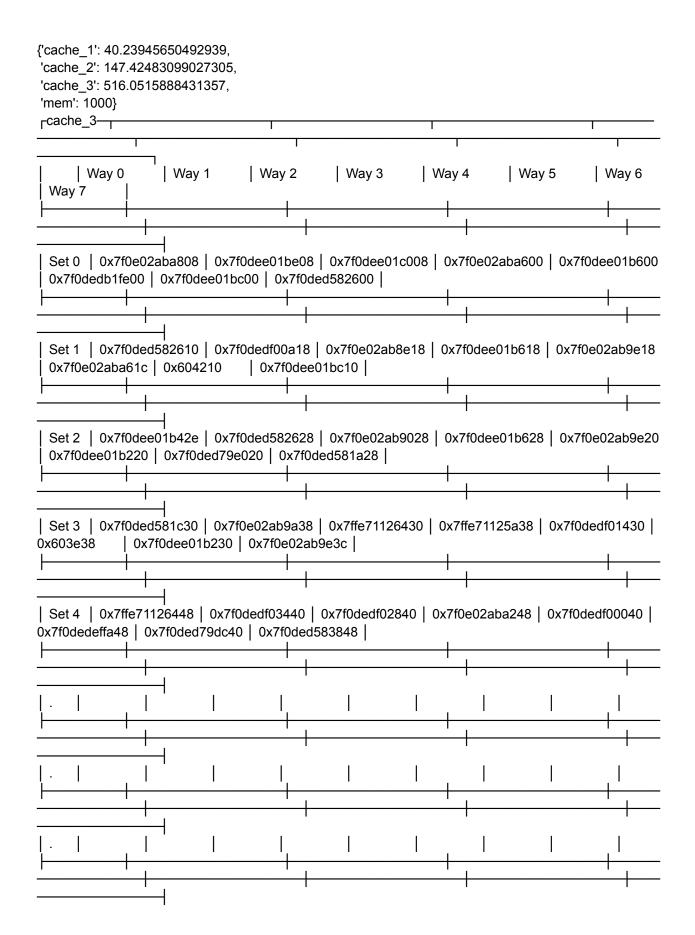
cache_2

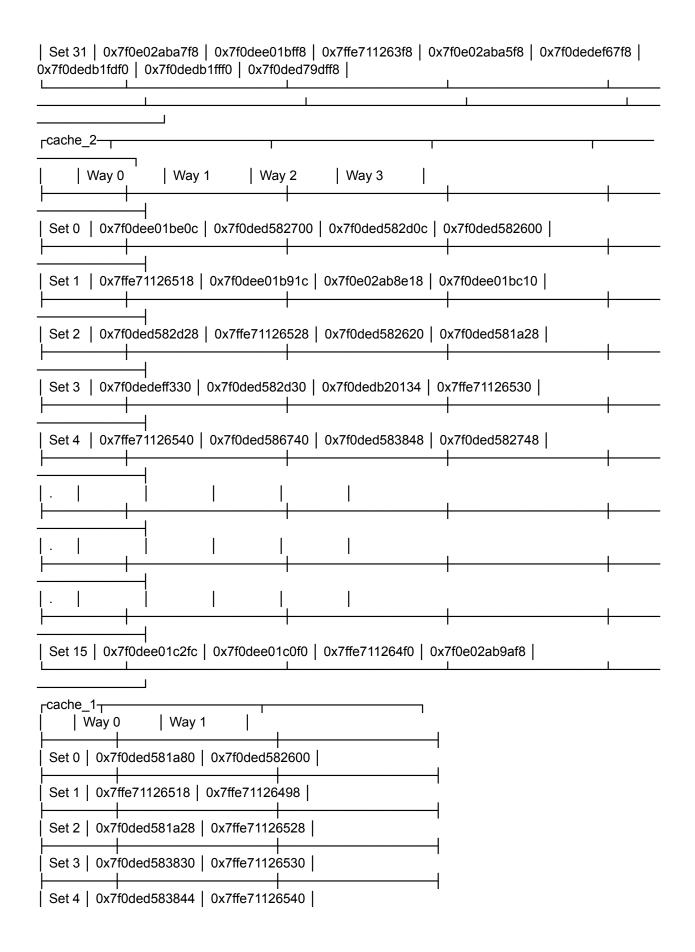
Number of accesses: 55522 Number of hits: 41382 Number of misses: 14140

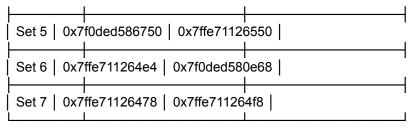
cache_1

Number of accesses: 240677 Number of hits: 176617 Number of misses: 64060

AMATs:







real 2m6.676s user 0m21.227s sys 0m2.459s

/home/grads/vmn5118/CachePerformanceOnMatMul

Matrix Size 150x150:

Using 1 Cache:

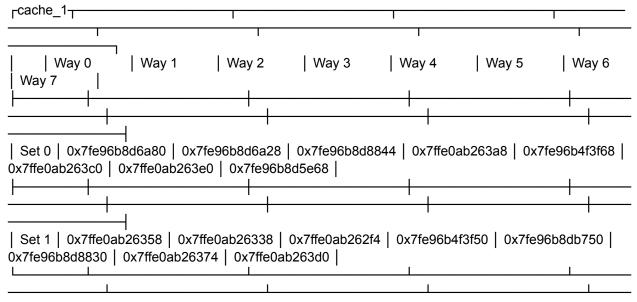
Number of instructions: 262352 Total cycles taken: 65085352

cache 1

Number of accesses: 262352 Number of hits: 206986 Number of misses: 55366

AMATs:

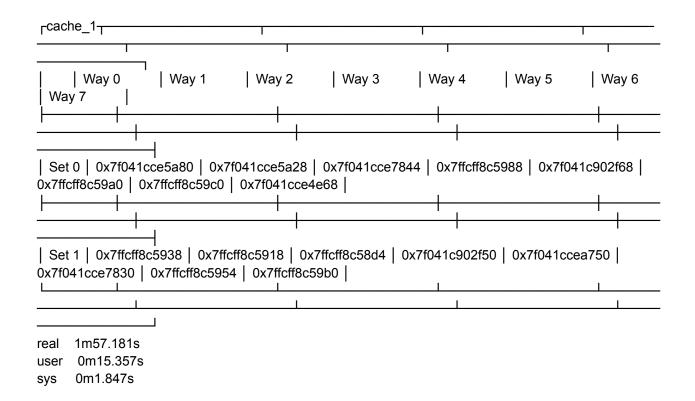
{'cache_1': 212.03707995365005, 'mem': 1000}



real 1m57.909s user 0m14.962s

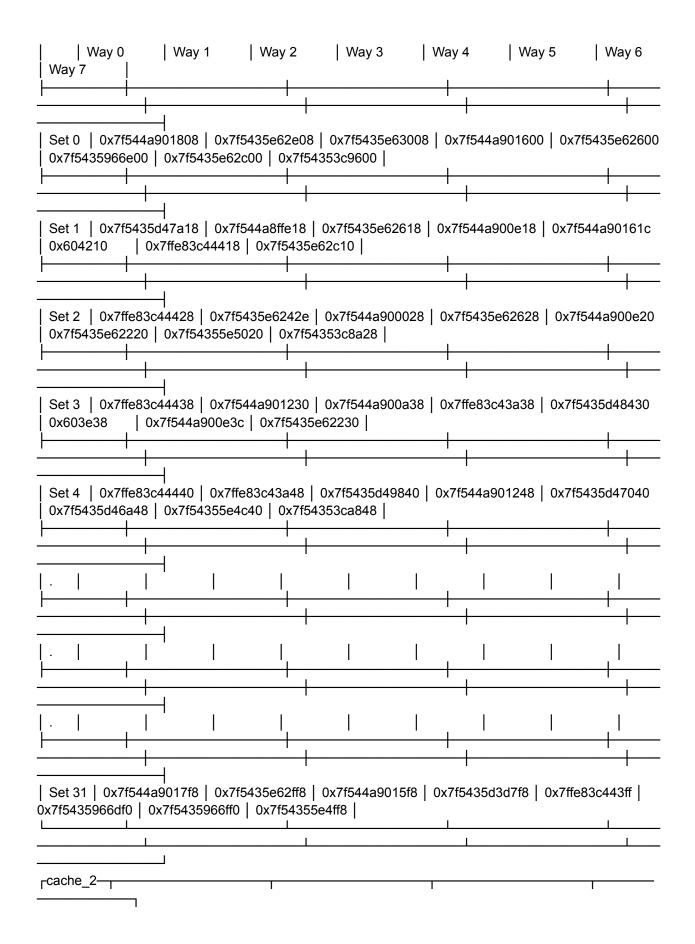
Using 2 Cache:

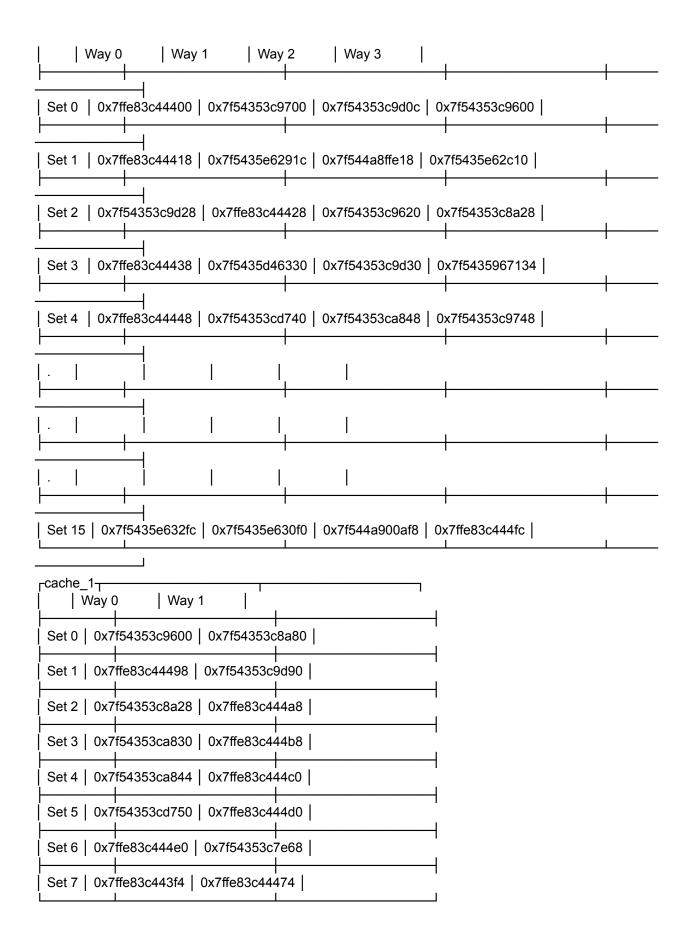
Number of instructions: 262352 Total cycles taken: 26534944 cache 2 Number of accesses: 49500 Number of hits: 31720 Number of misses: 17780 cache 1 Number of accesses: 262352 Number of hits: 206985 Number of misses: 55367 AMATs: {'cache_1': 80.18083715732675, 'cache_2': 375.19191919191917, 'mem': 1000} rcache_2-r Way 0 Way 1 Way 3 Way 2 Set 0 | 0x7ffcff8c5908 | 0x7f041cce6d0c | 0x7f041cce6700 | 0x7f041cce6600 | Set 1 | 0x7ffcff8c5910 | 0x7f041d77f91c | 0x7f043221ce18 | 0x7f041d77fc10 | Set 2 | 0x7ffcff8c5920 | 0x7f041cce6d28 | 0x7f041cce6620 | 0x7f041cce5a28 | Set 3 | 0x7f041d77f230 | 0x7f041d663330 | 0x7f041cce6d30 | 0x7f041d284134 | Set 4 | 0x7f041ccea740 | 0x7f041cce7848 | 0x7ffcff8c5948 | 0x7f041cce6748 | Set 15 | 0x7ffcff8c58f8 | 0x7f041d7802fc | 0x7f041d7800f0 | 0x7f043221daf8 |



Using 3 Cache:

Number of instructions: 262352 Total cycles taken: 15008504 cache 3 Number of accesses: 15863 Number of hits: 9114 Number of misses: 6749 cache 2 Number of accesses: 57681 Number of hits: 40345 Number of misses: 17336 cache 1 Number of accesses: 262352 Number of hits: 197694 Number of misses: 64658 AMATs: {'cache_1': 43.86481338284825, 'cache 2': 173.92541558070158, 'cache_3': 525.4554623967724, 'mem': 1000} rcache_3-r





real 3m30.723s user 0m26.742s sys 0m3.196s