

Comparison of delay

Delay before optimization

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	Fmax	Restricted Fmax	Clock Name	Note
1	953.29 MHz	250.0 MHz	clk	limit due to minimum period restriction (max I/O toggle rate)

1.05ns

Delay after optimization

Slow 1200mV 85C Model Fmax Summary

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	Fmax	Restricted Fmax	Clock Name	Note
1	1109.88 MHz	250.0 MHz	clk	limit due to minimum period restriction (max I/O toggle rate)

0.90ns