

Vivekanand Education Society's Institute of Technology

Department of Computer Engineering

TIFR - VESIT Projects

Minutes of Meeting

Venue: Online mode

Date: 23rd December, 2023

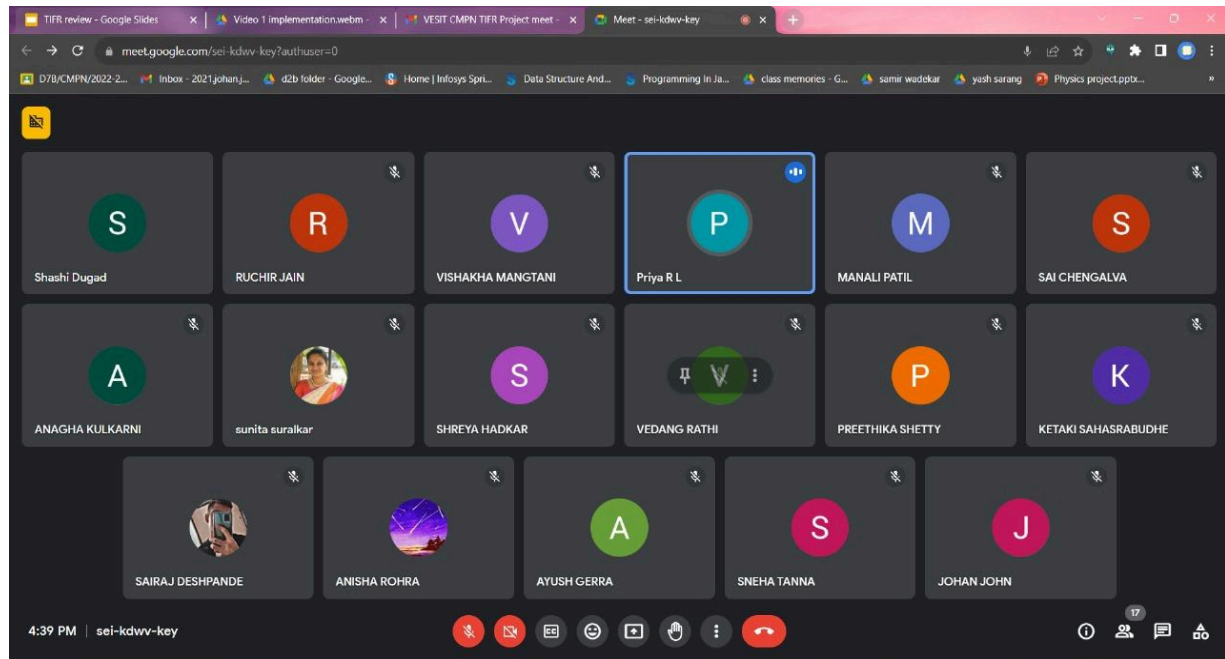
Time : 11.00 AM to 12.30 PM

Participants Attendance List:

- Dr. Shashi Dugad - Department of High Energy Physics, TIFR
- Mrs. Priya R.L - Assistant Professor, Computer Engineering, VESIT
- Mrs. Sunita Suralkar - Assistant Professor, Computer Engineering, VESIT
- Mr. Richard Joseph - Assistant Professor, Computer Engineering, VESIT
- Students, Computer Engineering, VESIT

Agenda:

- To present the work done since the previous meeting and share the progress.
- Plan further work to be done based on the suggestions given.



Points Discussed:

Applications of CUDA for data processing:

1. Ratio of the time taken on cpu and gpu for sum of series and matrix multiplication code.
2. Calculate time taken for the distance matrix code.
3. Meet with Pruthvi sir to get access to the TIFR GPU.

CMS Detector Quality Assurance using ML methods:

1. Solve examples of Hough Transform.

Wireless Data Extraction Of Industrial Data

Implementation of dynamic IP to access video stream remotely