Vivekanand Education Society's Institute of Technology

Department of Computer Engineering

TIFR - VESIT Projects
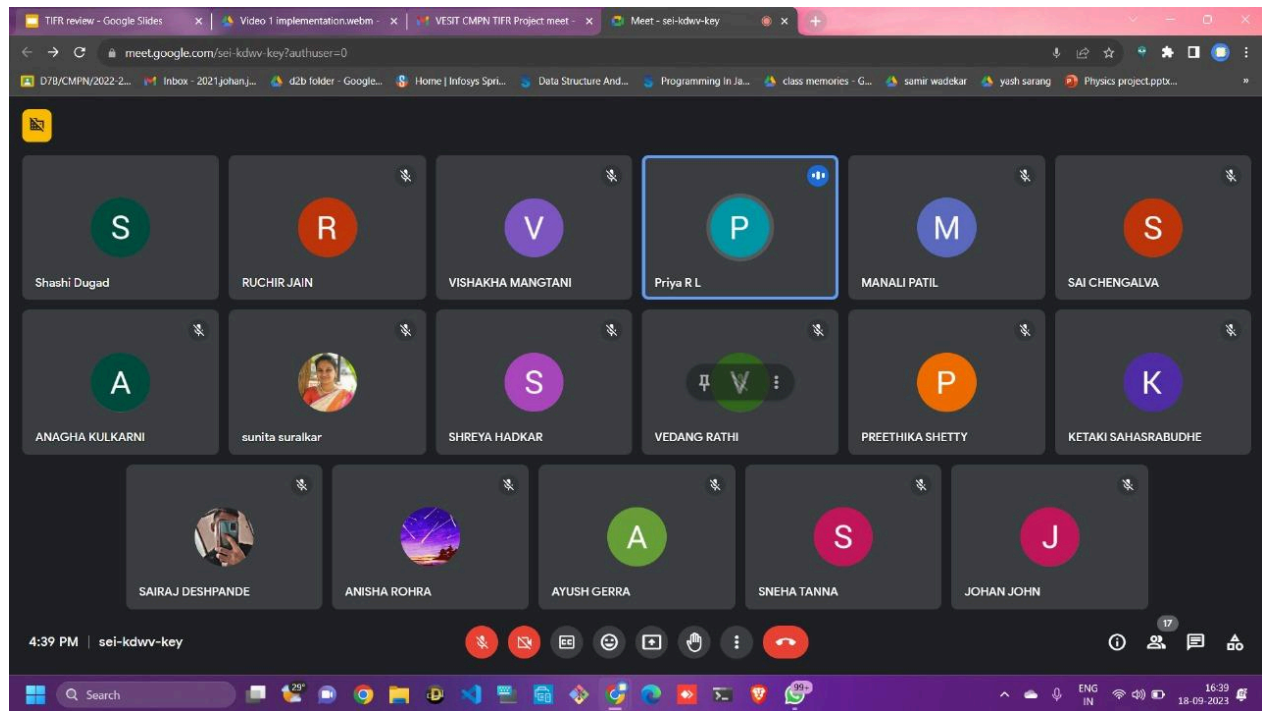
Minutes of Meeting

Venue: Online mode

Date: 18th August, 2023 Time : 4:15 PM to 5.30 PM

Participants Attendance List:

● Dr. Shashi Dugad - Department of High Energy Physics, TIFR ●

● Mrs. Priya R.L - Assistant Professor, Computer Engineering, VESIT

● Mrs. Sunita Suralkar - Assistant Professor, Computer Engineering, VESIT

● Students, Computer Engineering, VESIT

Agenda:

- To demonstrate the implementations completed till now.

- To discuss the challenges faced and recommended solutions for the previous year cookbook projects.

- To track the progress of the task assigned to each group.

Points Discussed:

Applications of CUDA for data processing:

Grp 1:(Mentor: Richard Joseph Sir)

1. **The implementation of the Cuda code should be done on a High computing device which has a CUDA capable GPU.**
2. **Research more on the GPU features.**

Group 2 : (Mentor: Sunita Suralkar Ma'am)

1. To setup CUDA environment on the machine.
2. To implement CUDA program - matrix multiplication and sum of series.

CMS detector quality assurance using ML methods:

Grp 3 (Mentor: Mr. Richard Joseph)

a. To do a market survey for flatbed scanners.
b. Visit TIFR for a better understanding of the project.

Wireless Extraction of Display Panels using ML:
Grp 4 (Mentor: Priya RL)

a. The implementation should be done by using 4 esp32 cameras with proper fixtures of the cameras so that stable monitoring of the panels can be achieved.
b. The connection of the cameras and the system should be made robust.