

Diagrama de Reg. de Mem. Mayor.

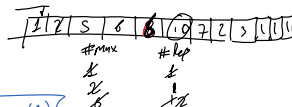
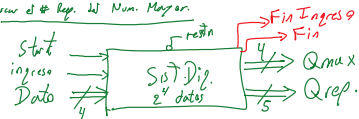
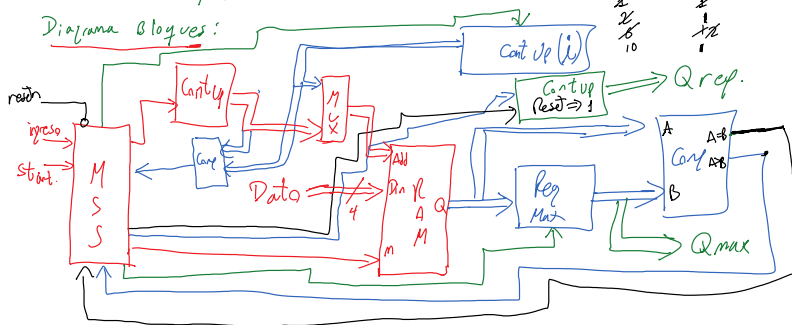


Diagrama Bloques:



Pseudo código:

```

While (start = 0) {
    if (ingreso = 1) {
        wr = 1; -- sRAM[Cnt] ← Data;
        if (Cnt Max = 1) {
            Fin ingreso = 1;
        } else {
            enCnt = 1;
        }
    }
}
While (start = 1) {
    restri = 1;

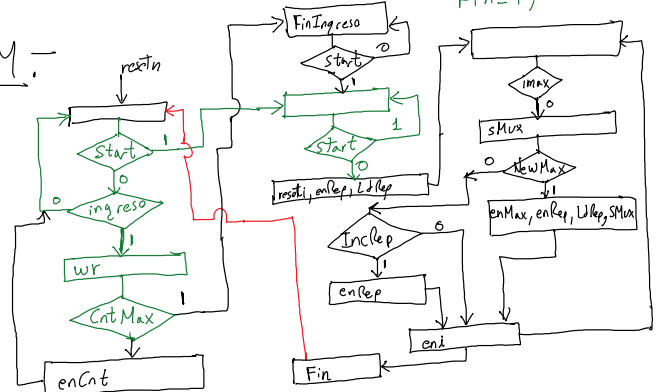
```

```

While (imax = 0) {
    smux = 1;
    if (New Max = 1) {
        enMax = 1;
        enRep = 1; LdRep = 1;
    }
    if (inCRep = 1) {
        enRep = 1;
    }
    eni = 1;
}
Fin = 1;

```

ASM:-



Partición Funcional:

