

# SMART DISPLAY MODULE SPECIFICATION

ESP32-P4-SmartDisplay development board	
<b>Model:</b>	ESP32-P4-SmartDisplay
<b>Version:</b>	V1.0
<b>Date:</b>	2025-09-21

## Customer Confirmation

Approved by	Notes

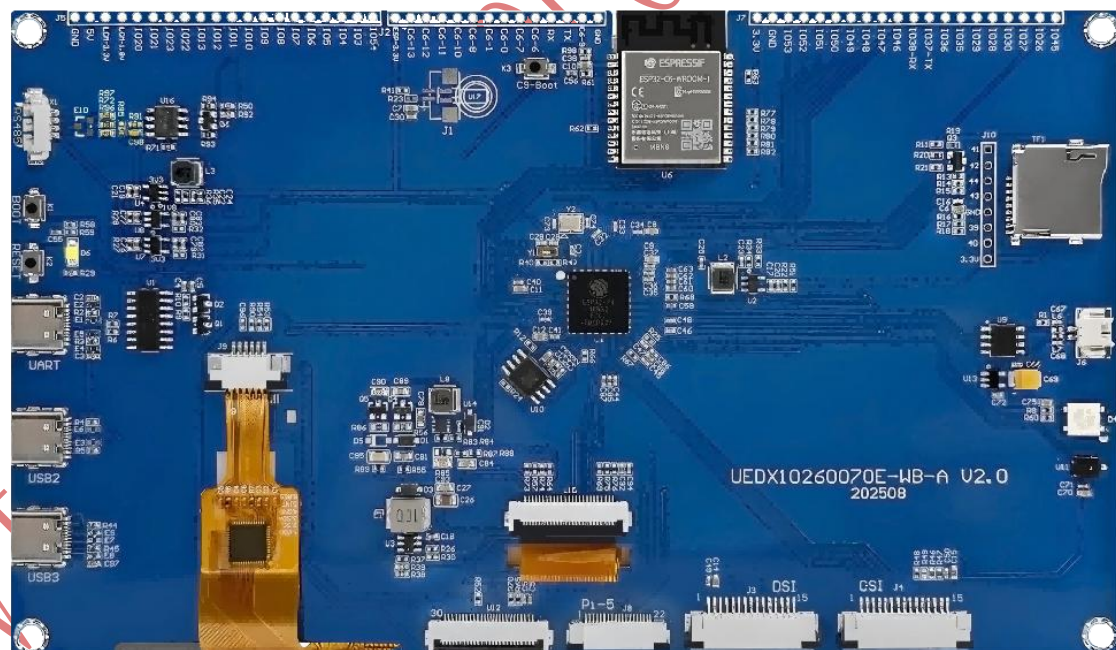
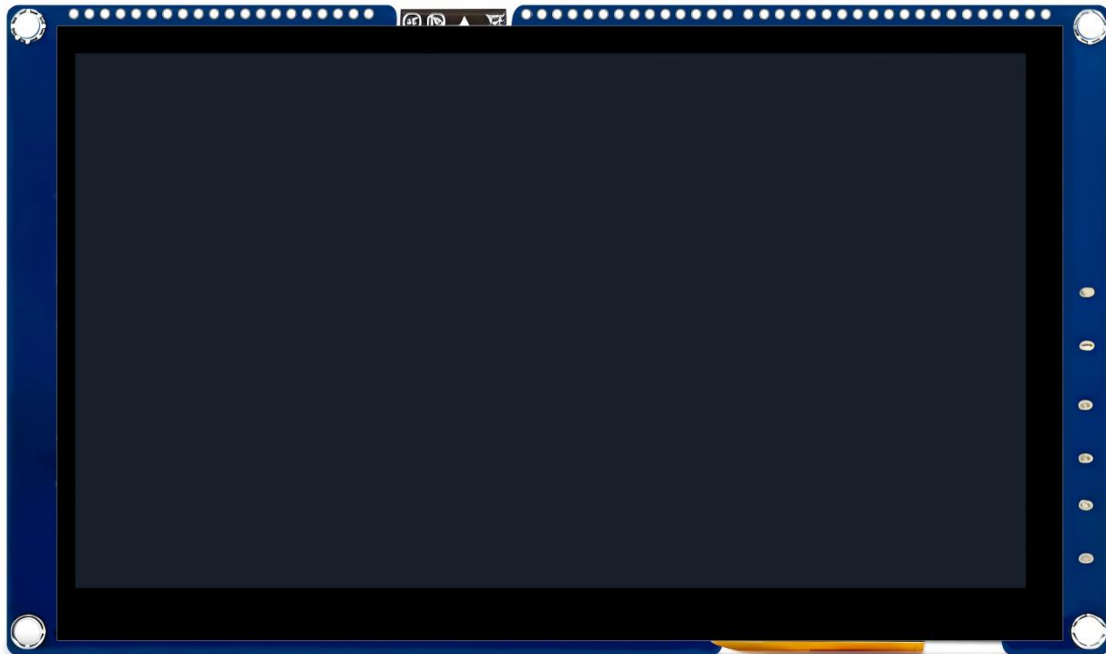
## REVISION HISTORY

Revision	Date	Contents of Revision Change	Remark
V1.0	20250921	Preliminary release	

## TABLE of CONTENTS

<b>1 INTRODUCTION .....</b>	<b>4</b>
1.1 Product Features .....	5
<b>2. HARDWARE DESCRIPTION .....</b>	<b>7</b>
2.1 Module Introduction .....	7
2.2 GPIO Introduction .....	8
<b>3. FUNCTIONAL BLOCK DIAGRAM .....</b>	<b>9</b>
<b>4. INSTRUCTIONS FOR USE .....</b>	<b>9</b>
<b>5. RELATED DOCUMENTS .....</b>	<b>10</b>
<b>6. DIMENSION DRAWING .....</b>	<b>11</b>

# 1 Introduction



ESP32-P4-SmartDisplay is a high-performance development board with a dual-core RISC-V based on the ESP32-P4 chip designed by VIEWE. The ESP32-P4 is equipped with a dual-core 400MHz RISC-V processor and supports up to 32MB of SRAM. In addition, the ESP32-P4 supports common peripherals such as SPI, I2S, I2C, LED PWM, MCPWM, RMT, ADC, UART, and TWAI™. It also supports USB OTG 2.0 HS, MIPI-CSI/DSI, H.264 encoder, Ethernet, and SDIO Host 3.0 for

high-speed connection; fully meeting the higher requirements put forward by embedded applications in aspects such as human-machine interface support, edge computing capabilities, and IO connection characteristics. It can also meet the development needs of customers for low-cost, high-performance, and low-power multimedia products.

This development board is equipped with an ESP32-C6-MINI-1 2.4 GHz Wi-Fi 6 & Bluetooth 5 (LE) module for Wi-Fi and Bluetooth communication of the development board; we have also adapted a number of VIEWE capacitive touch screens, It comes standard with a 7-inch 1024\*600 MIPI screen, and other models can also be optional. such as a 4-inch square screen with a resolution of 480 x 480/720 x 720, a 2.8-inch round screen with a resolution of 480 x 480, and a 10.1-inch screen with a resolution of 800 x 1280. It enriches the user's interaction experience. This development board is suitable for the prototype construction of products such as video doorbells, network cameras, smart home central control screens, LCD electronic price tags, and two-wheeler instrument panels. Most of the pins of the modules on the board have been led out to the header pins, and developers can easily connect a variety of peripheral devices through jumpers according to actual needs.

## 1.1 Product Features

- Processor
  - ✧ Equipped with a RISC-V 32-bit dual-core processor (HP system), with DSP and instruction set extensions, floating-point arithmetic unit (FPU), and a main frequency of up to 400 MHz
  - ✧ Equipped with a RISC-V 32-bit single-core processor (LP system), with a main frequency of up to 40 MHz
  - ✧ Equipped with an ESP32-C6 WIFI/BT coprocessor, expanding functions such as WIFI 6/Bluetooth 5 through SDIO
- Memory
  - ✧ 128 KB of high-performance (HP) system read-only memory (ROM).
  - ✧ 16 KB of low-power (LP) system read-only memory (ROM).
  - ✧ 768 KB of high-performance (HP) L2 memory (L2MEM).
  - ✧ 32 KB of low-power (LP) SRAM.
  - ✧ 8 KB of system tightly coupled memory (TCM).
  - ✧ 32 MB PSRAM is stacked and sealed inside the package, and 16MB Nor Flash is connected through the QSPI interface
- Peripheral Interfaces
  - ✧ Two 2\*20 Pin Headers are on-board to 34 programmable GPIOs, supporting a rich variety of peripheral devices
  - ✧ On-board SDIO3.0 SD card slot and Type-C UART programming port, facilitating use in different scenarios
  - ✧ On-board MIPI-CSI high-definition camera interface, supporting full HD 1080P video capture and encoding, integrating an image signal processor (ISP) and H264 video encoder,

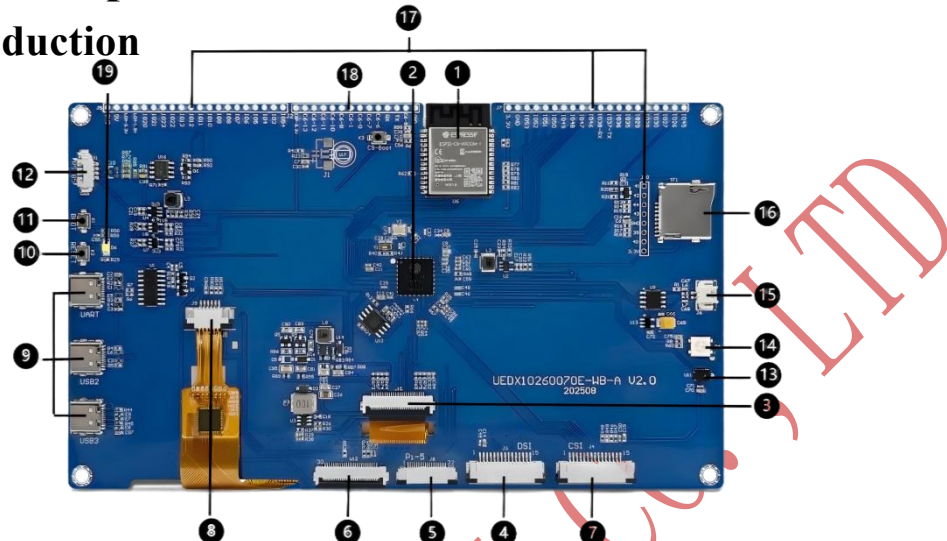
supporting H.264 & JPEG video encoding (1080P @30fps), facilitating applications in fields such as computer vision and machine vision

- ✧ On-board 2 MIPI-DSI high-definition display interfaces, integrating a pixel processing accelerator (PPA) and 2D graphics acceleration controller (2D DMA), supporting JPEG image decoding (1080P @30fps), providing strong support for high-definition displays and smooth HMI experiences, facilitating applications in scenarios such as smart home control panels, industrial control panels, and vending machines

VIEWE TECHNOLOGY CO., LTD

## 2. Hardware Description

### 2.1 Module Introduction



#### 1、ESP32-P4NRW32

ESP32-P4 stacked with 32MB PSRAM

#### 2、ESP32-C6

SDIO interface protocol, expanding  
ESP32-P4-SmartDisplay Wi-Fi 6 and Bluetooth 5

#### 3、7inch Display interface (MIPI 2-lane)

#### 4、15pin Display interface (MIPI 2-lane)

4-DSI-TOUCH

7-DSI-TOUCH

10.1-DSI-TOUCH

#### 5、5B-MIPI Display interface

#### 6、Universal Display Interface (MIPI 2-lane)

#### 7、Camera interface (MIPI 2-lane)

#### 8、7inch Touch interface

#### 9、Type-C interface (USB2、USB3、UART)

Can be used for power supply, program burning,  
and debugging, USB3 is a USB 2.0 full-speed OTG  
interface

#### 10、RESET button

#### 11、BOOT button

Press when powering on or resetting to enter  
download mode

#### 12、RS485

Industrial-grade serial communication  
standard

#### 13、SMD microphone

#### 14、RGB-LED (WS2812B)

#### 15、Speaker interface

#### 16、TF card slot (SDIO 3.0)

#### 17、P4 GPIO interface

#### 18、C6 GPIO interface

#### 19、USER-LED

Power indicator light



## 2.2 GPIO Introduction

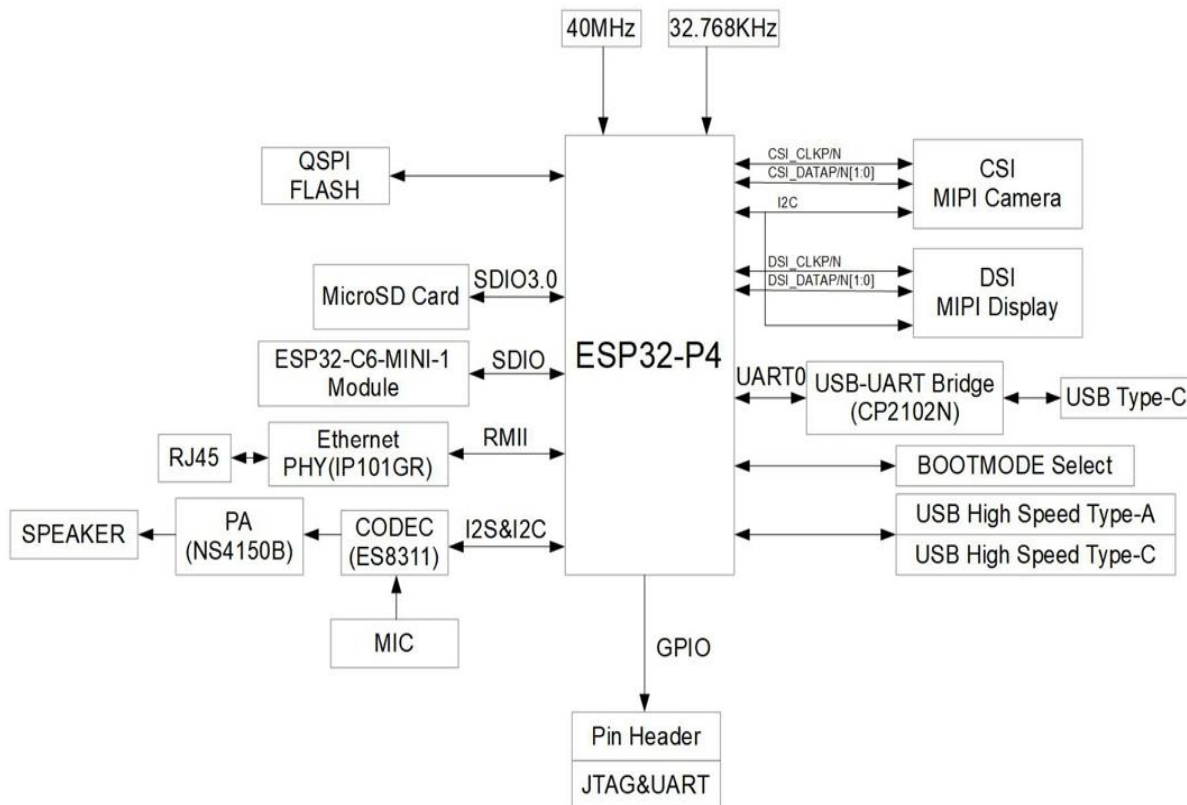
GPIO2	GPIO2	GPIO29	GPIO29
GPIO3	GPIO3	GPIO30	RGB-LED
GPIO4	EN-LCM1V8	GPIO31	I2S-CTRL
GPIO5	EN-LCM3V3	GPIO32	I2S-LRCLK
GPIO6	C6-WP	GPIO33	I2S-BCLK
GPIO7	I2C-SDA	GPIO34	I2S-SDATA
GPIO8	I2C-SCL	GPIO35	GPIO35
GPIO9	GPIO9	GPIO36	GPIO36
GPIO10	GPIO10	GPIO37	UART0-RX
GPIO11	UART1-RX	GPIO38	UART0-TX
GPIO12	UART1-TX	GPIO39	SD1-D0
GPIO13	LCD-TE	GPIO40	SD1-D1
GPIO14	SD3-D0	GPIO41	SD1-D2
GPIO15	SD3-D1	GPIO42	SD1-D3
GPIO16	SD3-D2	GPIO43	SD1-CLK
GPIO17	SD3-D3	GPIO44	SD1-CMD
GPIO18	SD3-CLK	GPIO45	SD1-PWRn
GPIO19	SD3-CMD	GPIO46	GPIO46
GPIO20	CTP-RST	GPIO47	GPIO47
GPIO21	CTP-INT	GPIO48	GPIO48
GPIO22	LCM-RST	GPIO49	GPIO49
GPIO23	LCD-BL-EN	GPIO50	GPIO50
GPIO24	USB-P1-N	GPIO51	GPIO51
GPIO25	USB-P1-P	GPIO52	GPIO52
GPIO26	MIC-CLK	GPIO53	PA-CTRL
GPIO27	MIC-DATA	GPIO54	C6-CHIP-PU
GPIO28	GPIO28		

	UART1
	USB2.0
	RGB-LED
	LCD Power
	GPIO
	LCD
	ESP32-C6
	I2C
	Micro SD
	Touch
	I2S
	UART0
	MIC
	AUDIO



### 3. Functional Block Diagram

The main components and connection methods of the ESP32-P4-SmartDisplay are shown in the following figure:



**Note:** This board is the most basic version, and there are no external Ethernet. And we have also replaced the audio part, which consists of inmp441 and ns4168. We will lead out the pin and can directly insert the expansion board later, and also reserve more creative possibilities for everyone.

### 4. Instructions for Use

This tutorial aims to guide users to set up the software environment for ESP32-P4 hardware development, and demonstrates how to use the ESP-IDF configuration menu, compile, and download firmware to the ESP32-P4 development board through simple examples.

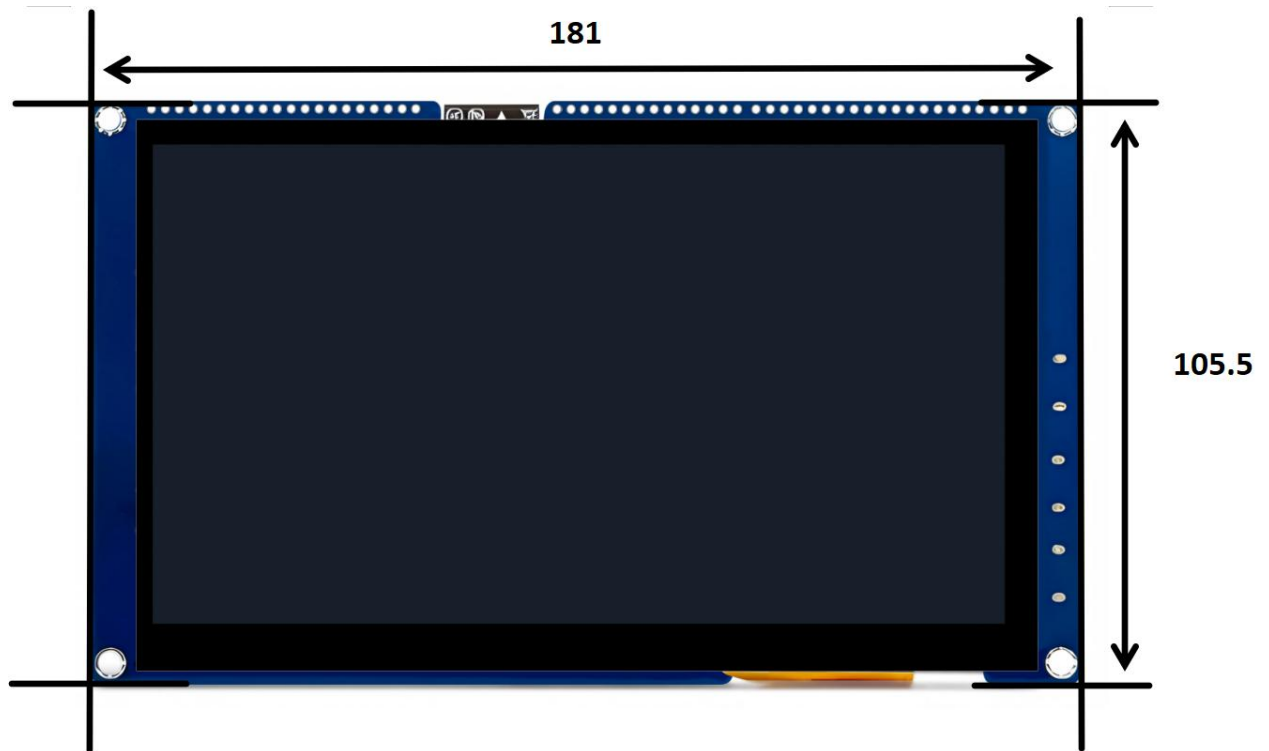
- Preparation
  - Hardware
    - ✧ ESP32-P4-SmartDisplay Development Board
    - ✧ USB data cable (Type-A to Type-C, prepared as needed)
    - ✧ Computer (Windows, Linux or macOS)

- Software (It is recommended to install ESP-IDF using an integrated development environment. If you are familiar with ESP-IDF, you can start directly from the ESP-IDF terminal. You can choose any of the following development methods.)
  - ✧ VSCode + ESP-IDF plugin (recommended)
  - ✧ Eclipse + ESP-IDF plugin (Espressif-IDE)
  - ✧ Arduino IDE
- Getting-start
  - Please go to ESP-IDF Quick Start to see how to quickly set up the development environment and burn the application to your development board.
  - The application examples for the development board are stored in Examples. You can configure the project options by entering idf.py menuconfig in the example directory.

## 5. Related Documents

- [ESP32-P4-SmartDisplay Schematic Diagram \(PDF\)](#)
- [ESP32-P4 Chip Specification \(PDF\)](#)
- [Camera Specification \(PDF\)](#)
- [Display Specification \(PDF\)](#)
- [Display Chip Specification \(PDF\)](#)
- [ESP32-P4-SmartDisplay Specification\(PDF\)](#)

## 6. dimension drawing



VIEWE TECHNOLOGY