

LCD MODULE SPECIFICATION

Model:	UE043WV-RB40-L037A	
Version:	V1.0	4
Date:	20201112	

Customer Confirmation

Approved by	Notes

Please return one of the copies of the specification with your signature to us within two weeks after you receive this document. If it is not returned, we will assume that you agree to the entire contents of this specification document.

VIEWE Confirmation

Prepared by	Reviewed by	Approved by



REVISION HISTORY

Revision	Date	Contents of Revision Change	Remark
V1.0	20201112	Preliminary release	
	<u></u>		

Page: 3 / 16



TABLE of CONTENTS

1. GENERAL INFORMATION	4
1.1 Features	
1.2 Mechanical Specification	4
2. ABSOLUTE MAXIMUM RATINGS	
3. MECHANICAL DRAWING	
4. I/O CONNECTION & BLOCK DIAGRAM	7
4.1 I/O Connection	
4.2 Block Diagram	8
5 FI FCTDICAL CHARACTERISTICS	
5.1 TFT-LCD Panel Driving Section 5.2 Back Light Driving Section 5.3 Power On/Off Sequence 5.4 Timing Characteristics	9
5.2 Back Light Driving Section	9
5.3 Power On/Off Sequence	10
5.4 Timing Characteristics	10
5.5 Timing Diagram	11
5.5 Timing Diagram	12
7. RELIABILITY	15
8. PACKAGE DRAWING	16

telephone: 400-660-3306



1. GENERAL INFORMATION

1.1 Features

Pixel Arrangement: RGB Vertical Stripe
 Interface Mode: 40PIN RGB 24bits

3) Driver IC: ST7262E43-G4

4) Operation Temperature: -20~70°C
5) Storage Temperature: -30~80°C
6) Backlight Type: White LED

7) Display mode: Normally black, Transmissive

8) Pixel Density: 217 PPI9) LED life time: 30,000 Hours



1.2 Mechanical Specification

Item	Specification	Unit	Remark
Pixel Driving element	IPS TFT	-	-
Screen Size	4.3	Inch	Diagonal
Resolution	800(W)*3(RGB)*480(H)	Dots	-
Interface	RGB 24bits	-	40PIN
Module Power Consumption	1.04	Watt	Typ.
Active Area	95.04(W)*53.86(H)	mm	-
Pixel pitch (W*H)	0.1188(W)*0.1122(H)	mm	-
Module Size (W*H*D)	105.52(W)*67.17(H)*2.8(D)	mm	Tolerance: ± 0.2
Luminance	400	cd/m ²	Typ.
Viewing Direction	All	O'clock	-
Display Color	16.2M	Colors	24bits



2. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remark
Power supply voltage	VDD	-0.5	3.96	V	Note1
LED forward current	I F	-0.001	30	mA	For each led,Note1
LED Reverse Voltage	VR	-	5	V	For each led, Note1
Operating temperature	Тор	-30	85	°C	Note1,2
Storage temperature	Tst	-40	85	°C	Note1,2
Humidity	Hst	10	90	%RH	Note1,3

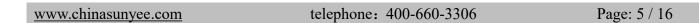
 $(Ta=+25^{\circ}C,GND=0V)$

Note1:If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also if the module operates with the absolute maximum ratings for a long time, the reliability may drop.

Note2: In case of temperature below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one

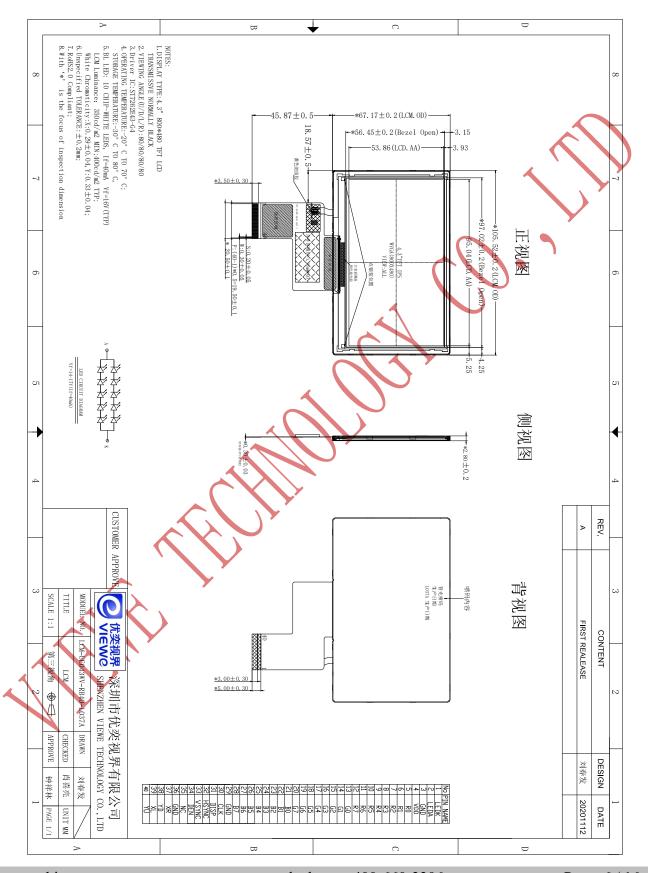
Note3: Temp. \leq 60°C , 90% RH MAX.

Temp. $>60^{\circ}$ C, Absolute humidity shall be less than 90% RH at 60° C.





3. MECHANICAL DRAWING





4. I/O CONNECTION & BLOCK DIAGRAM

4.1 I/O Connection

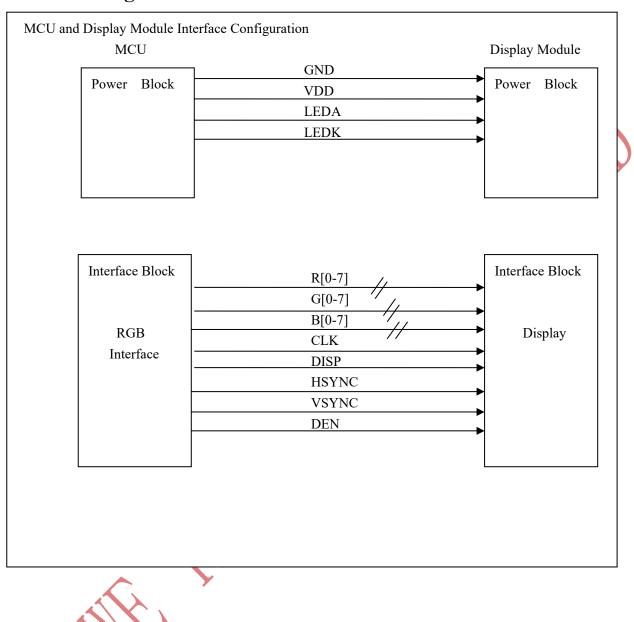
Pin No.	Symbol	I/O	Description				
1	LEDK	P	Power supply for backlight cathode				
2	LEDA	Р	Power supply for backlight anode				
3	GND	P	Power Ground				
4	VDD	Р	Power supply to the internal logic power regulator (3.3V)				
5-12	R0-R7	I	Red data input.				
13-20	G0-G7	I	Green data input.				
21-28	B0-B7	I	Blue data input.				
29	GND	Р	Power Ground				
30	CLK	I	Pixel clock input pin, Negative polarity				
31	DISP	I	Standby mode. Normally pulled high.				
32	HSYNC	I	Horizontal sync signal, Negative polarity				
33	VSYNC		Vertical sync signal, Negative polarity				
34	DEN		Data input enable. Display access is enabled when DE is "H"				
35	NC	I	Dummy				
36	GND	P	Power Ground				
37	XR	-	Dummy				
38	YD	-	Dummy				
39	XL	-	Dummy				
40	YU	-	Dummy				

I: Input; O: Output; P: Power

www.chinasunyee.com telephone: 400-660-3306 Page: 7 / 16



4.2 Block Diagram





5. ELECTRICAL CHARACTERISTICS

5.1 TFT-LCD Panel Driving Section

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Power Supply Voltage	VDD	3.0	3.3	3.6	V	-
Power Supply Current	Ivdd	-	122	180	mA	Note1
Logic Input High Voltage	V _{IH}	0.7VDD	-	VDD	V	Y
Logic Input Low Voltage	V _{IL}	0	-	0.3VDD	V	_
Panel Power Consumption	P _{VDD}	-	0.4	-	Watt	Note1
Module Power Consumption	P _{LCM}	-	1.04	(-)	Watt	Note1,2

 $(Ta=+25^{\circ}C,GND=0V)$

Note1:Measurement Conditions (Video Mode): Full Screen Red Pattern, VDD=3.3V,60Hz Refresh.

Note2: PLCM= PVDD+ PBL, About PBL information, inference to 5.2 Back Light Driving Section.

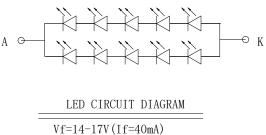
5.2 Back Light Driving Section

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Forward Voltage	VF	14	16	17	V	Note1
Forward Current	IF	-	40	50	mA	Note1
Backlight Power consumption	PBL	-	0.64	-	Watt	Note1
LED life time		30000	-	-	Hrs	Note2
LED Quantity			10		PCS	

 $(Ta=+25^{\circ}C,GND=0V)$

Note1: The LED driving condition is defined for each LED module (5 LED Serial, 2 LED Parallel) For each LED : IF=20mA,VF=3.2V(Typ.)/3.4V(Max.),Ta=25°C .

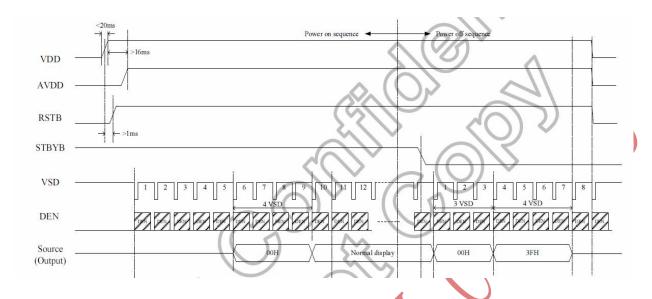
Note2:The "LED life time" is defined as the module brightness decrease to 50% of original brightness at ILED=20mA(Per Led). The LED life time could be decreased if operating ILED is larger than 20mA.



www.chinasunyee.com telephone: 400-660-3306 Page: 9 / 16



5.3 Power On/Off Sequence



5.4 Timing Characteristics

5.4.1 Timing Parameters

Parameter	Cumbal		Spec.		Unit
	Symbol	Min.	Тур.	Max.	Offic
HS setup time	T _{hst}	8	i - 1	-	ns
HS hold time	T _{hhd}	8	U= 1	(- 2)	ns
VS setup time	T _{vst}	8	·-	(e)	ns
VS hold time	T _{vhd}	8	-	- <	ns
Data setup time	T _{dsu}	8	(a_1)	-	ns
Data hold time	T _{dhd}	8	-	- (0.	ns
DE setup time	T _{esu}	8	-	924/0	ns
DE hold time	T _{ehd}	8		WILL	ns
VDD Power On Slew rate	T _{POR}	5.1	U= 1	20	ms
RSTB pulse width	T _{Rst}	10	- 4		us
CLKIN cycle time	T _{cph}	20	- (1	\\\ -	ns
CLKIN pulse duty	T _{cwh}	40	50	60	%
Output stable time	T _{sst}	21	(-0)	6	us

 $(Ta=+25^{\circ}C,GND=0V)$

5.4.2 Horizontal Timing

Parameter	Cymbol		Unit		
	Symbol	Min.	Тур.	Max.	Offic
Horizontal Display Area	thd		800		DCLK
DCLK frequency	fclk	2550	30	50	MHz
One Horizontal Line	th	889	928	1143	DCLK
HS pulse width	thpw	1	48	255	DCLK
HS Back Porch (Blanking)	thb		88		DCLK
HS Front Porch	thfp	1	40	255	DCLK
DE mode Blanking	th-thd	85	128	(512)//	DCLK

<u>www.chinasunyee.com</u> telephone: 400-660-3306 Page: 10 /

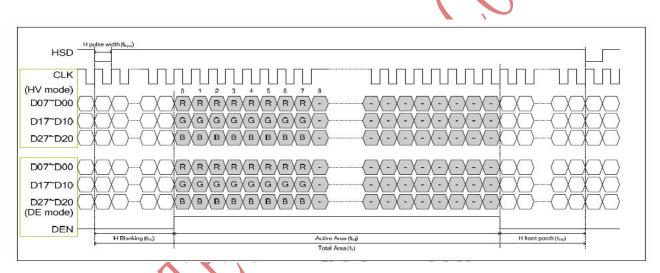


5.4.3 Vertical Timing

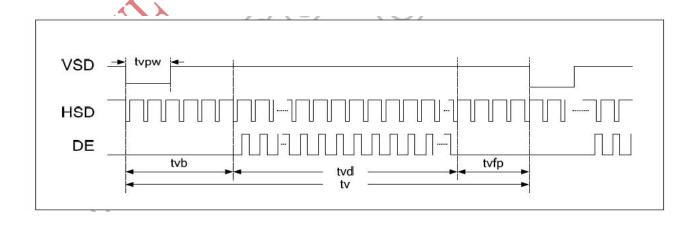
Dovemeter	Cumbal		Unit			
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Vertical Display Area	tvd		480		T _H	
VS period time	tv	513	525	767	TH	
VS pulse width	tvpw	3	3	255	T _H	
VS Back Porch (Blanking)	tvb	50	32 <	(())	TH	
VS Front Porch	tvfp		13	255	T _H	
DE mode Blanking	tv-tvd	(4)	45 (255	TH	

5.5 Timing Diagram

5.5.1 Horizontal Timing



5.5.2 Vertical Timing





6. OPTICAL CHARACTERISTICS

Parameter 参数	Symbol 符号	Condition 条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Contrast Ratio	C/R	$\theta = 0$ °	960	1200	-		Note(4)
NTSC Ratio	S	θ =0°	45	50	-	%	Note(7)
Luminance	L	θ=0°	350	400	١	cd/m2	Note(5)
Luminance uniformity	Uw	θ=0°	1	80		%	Note(3)
Response Time	$T_R + T_F$	25 °C	25	30	40	ms	Note(2)
	Wx			0.311			
Color Coordination	WY	θ = 0° (Center) Normal viewing angle B/L On	-0.04	0.338	+0.04	NTSC (x,y)	Note(6)
	Rx			0.631			
	Ry			0.329			
	Gx			0.328			
	Gy			0.548			
	Вх			0.136			
1/1/	Вч			0.141			
	θ L		75	80	-		
Viewing Angle	θR	C/R>10	75	80	-	Degree	Note(1)
	θυ		75	80			
	θр		75	80	-		

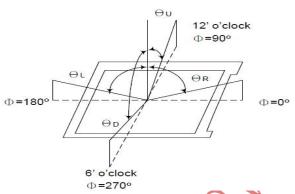
telephone: 400-660-3306



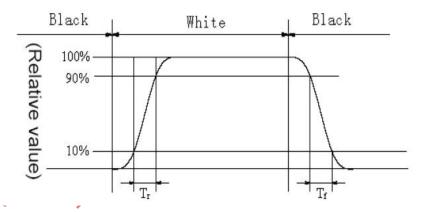
Test Conditions:

- 1. VDD=3.3V, I_F=40mA (Backlight current), the ambient temperature is+25°C.
- 2. The test systems refer to Note 8.

Note1: Definition of Viewing Angle: The viewing angle range that the CR>10



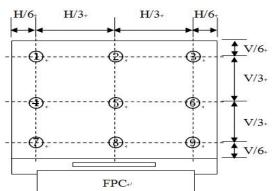
Note2: Definition of Response time: Sum of TR and TF



Note 3: Definition of Luminance Uniformity: Active area is divided into 9 measuring areas, every measuring point is placed at the center of each measuring area.

Luminance Uniformity = Min Luminance of white among 9-points

Max Luminance of white among 9-points x100%



optical



Note4: Definition of Contrast Ratio (CR): measured at the center point of panel

Contrast ratio (CR) = $\frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$

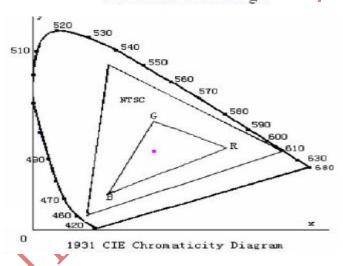
Note 5: Definition of Luminance: Center Luminance of white is defined as luminance values of 1 point average across the LCD surface.

Note 6: Definition of Color Chromaticity (CIE 1931)

Color coordinates of white & red, green, blue measured at center point of LCD.

Note 7: Definition of NTSC ratio:

$$NTSC ratio = \frac{Area of RGB triangle}{Area of NTSC triangle}$$



Note 8: Definition of measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by

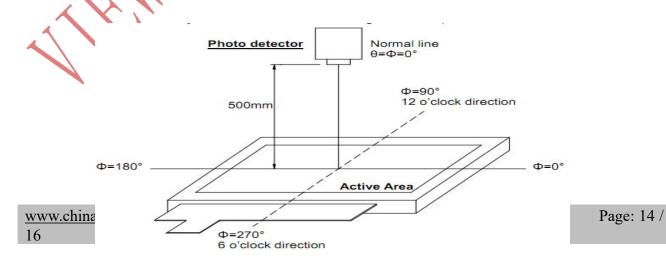


Photo detector TOPCON BM-7, Field of view: 1°/Height: 500mm.)



7. RELIABILITY

Item	Test Condition	Remark		
High Temperature Storage	Ta =+80°C / 96Hours	Note1,2,3		
Low Temperature Storage	Ta =-30°C / 96Hours	Note1,2,3		
High Temperature Operating	Ta =+70°C / 96Hours	Note1,2,3		
Low Temperature Operating	Ta =-20°C / 96Hours	Note1,2,3		
Temperature Cycle storage Test	-30°C/30min Δ+80°C /30min for	Note2,3		
	30cycles,Transfer time less than 5min			
Thermal humidity storage Test	60°C x 90%RH / 96Hours	Note2,3		
Package Vibration Test	Frequency: 10Hz~55Hz,Amplitude:15mm, 1	Note2		
	hrs for each direction of X , Y , Z			
Packing shock test	Drop to the ground from 60cm height,	Note2		
	1 corner, 3 edges, 6 surfaces.			

Inspection after Test:

Note1:Ta is the ambient temperature of samples.

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 3: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.



8. PACKAGE DRAWING

