

# Technology Industry Risk Factors

## Geopolitical, Power, and Interest Rate Analysis

### 1. Geopolitical Risks

#### **US-China Technology Decoupling:**

Export controls on advanced chips (below 7nm) and semiconductor equipment have fragmented the global market. Chinese companies cannot access Nvidia H100/A100 GPUs, creating a separate ecosystem. This affects \$50B+ in annual semiconductor exports.

#### **Taiwan Strait Tensions:**

Severity: Critical (9/10)

TSMC produces 92% of advanced chips globally. Military conflict or blockade scenarios would halt production, causing immediate supply shortages for AI, automotive, and consumer electronics. Economic impact estimated at \$1T+ globally.

#### **EU Technology Sovereignty:**

EU is investing €43B in semiconductor manufacturing to reduce dependency on Asia. However, timeline extends to 2030, providing limited near-term risk mitigation.

### 2. Power Supply Constraints

#### **Data Center Power Consumption:**

A single H100 GPU consumes 700W. A 100,000-GPU cluster requires 200+ MW of power, equivalent to a small city. Major cloud providers are facing power allocation constraints in key markets (Virginia, Singapore).

#### **Semiconductor Manufacturing Energy Intensity:**

A modern 3nm fab consumes 150-200 MW continuously. Taiwan already faces periodic power shortages, risking production interruptions. TSMC's Arizona fabs will require new power infrastructure.

#### **Renewable Energy Transition Pressure:**

Tech companies have committed to 100% renewable energy, but AI growth is outpacing green power availability. This creates procurement competition and cost increases.

### 3. Interest Rate Sensitivity

**CAPEX Investment Impact:**

Semiconductor fabs require \$20-30B investments with 5-7 year payback periods. Higher interest rates (5% vs 0%) increase financing costs by \$5-7B over project life. This defers expansion plans and reduces industry capacity growth.

**Valuation Multiple Compression:**

Technology stocks trade at 25-40x P/E ratios, sensitive to discount rate changes. A 100bps rate increase can reduce valuations by 10-15%, affecting M&A; activity and strategic investments.

**Corporate Debt Refinancing:**

Intel and other capital-intensive companies face higher refinancing costs. Intel's \$50B debt at 5% rates adds \$2.5B annual interest expense vs 2% historical rates.

## 4. Talent and IP Risks

**Semiconductor Expertise Shortage:**

Building new fabs requires thousands of specialized engineers. TSMC Arizona faces challenges recruiting talent, delaying production ramp. Training timelines: 2-3 years.

**IP Theft and Cyber Espionage:**

Advanced chip designs represent billions in R&D.; State-sponsored IP theft targets EUV lithography, advanced packaging, and AI chip architectures. Estimated impact: \$200-300B in lost competitive advantage.

## 5. Regulatory Compliance Risks

**Environmental Regulations:**

Semiconductor manufacturing uses hazardous chemicals and significant water resources. Stricter environmental standards in US/EU increase operating costs by 15-20% vs Asian facilities.

**AI Governance and Export Controls:**

Evolving AI regulations (EU AI Act, potential US legislation) may restrict GPU sales for certain applications. High-risk AI systems face compliance burdens, potentially reducing addressable market.