		Page No.
	VINEETH R. RAD - IBMI9CS183	
2.	That will be did to the part of the part o	
	by Niteps, interoducing suitable delay between	eleps.
\rightarrow	Hundlude (stdio.h)	
	Hunclude (oreg51.h)	
	chair xdata point at Oxe803;	
	char xdata porta-cit- Oxe 800;	
	charidalace - at - 0x30;	
	delay ()	
	[unt i;	
	for (j=0; j<800; j++)	
	1 2	
	J. J.	
	void main ()	1 1 1 -
	1 yout = 0x80	14, 14,
	ushile (1)	9023
	1 acc = 0x88;	Service Control
	porta = acc;	Way 101
	delay();	
	acc = 0 x Alli)	13 45/3/4 1
	porta = aci;	Typina Late
	delay ();	No.
	$acc = 0 \times 22$;	
	porta-acci	
	delay();	
	$acc = 0 \times 11$	1
	porta = acc;	
	delay ();	