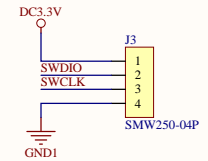
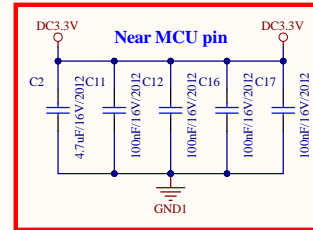
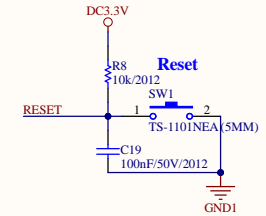
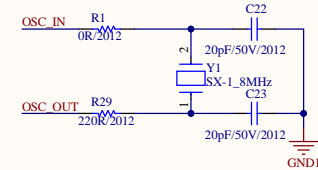
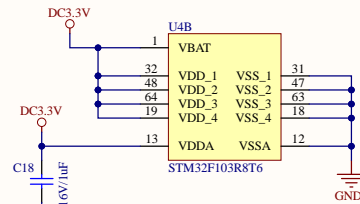
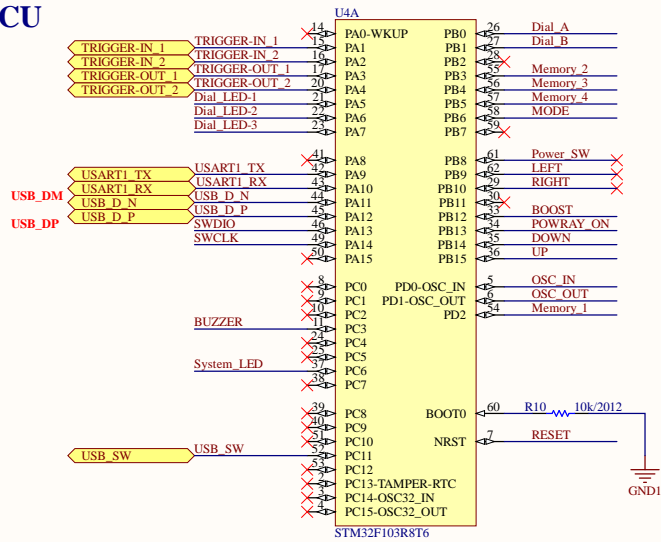
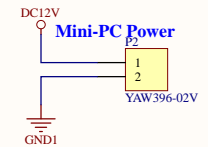
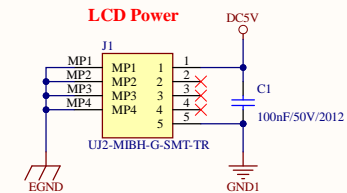
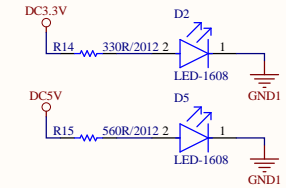
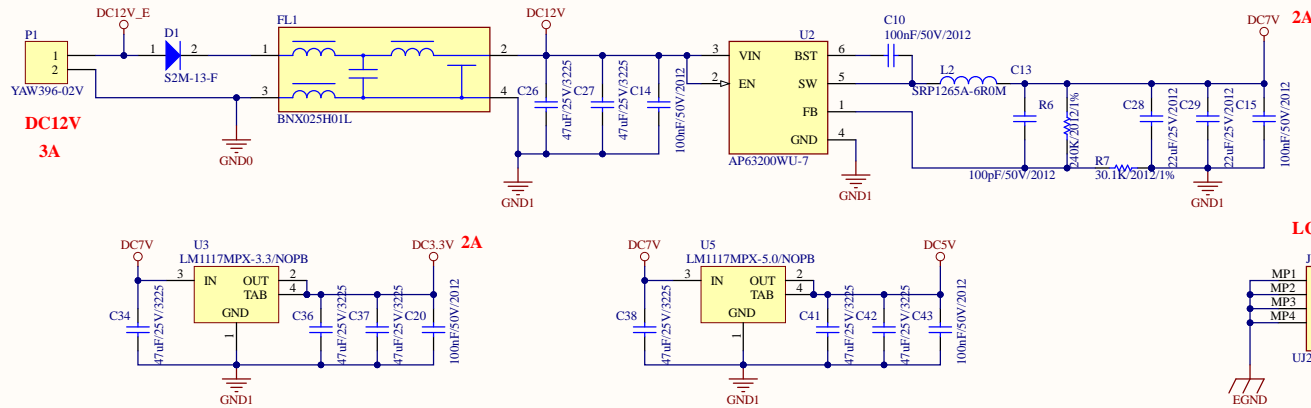


MCU

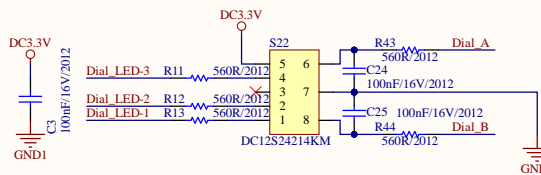


Power

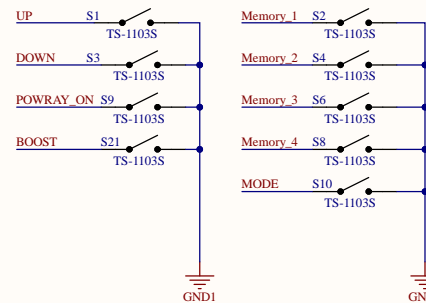


Dial

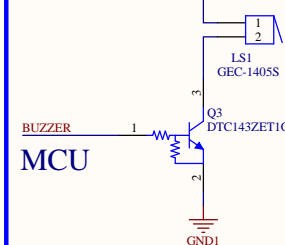
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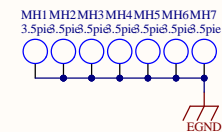
Key




Buzzer

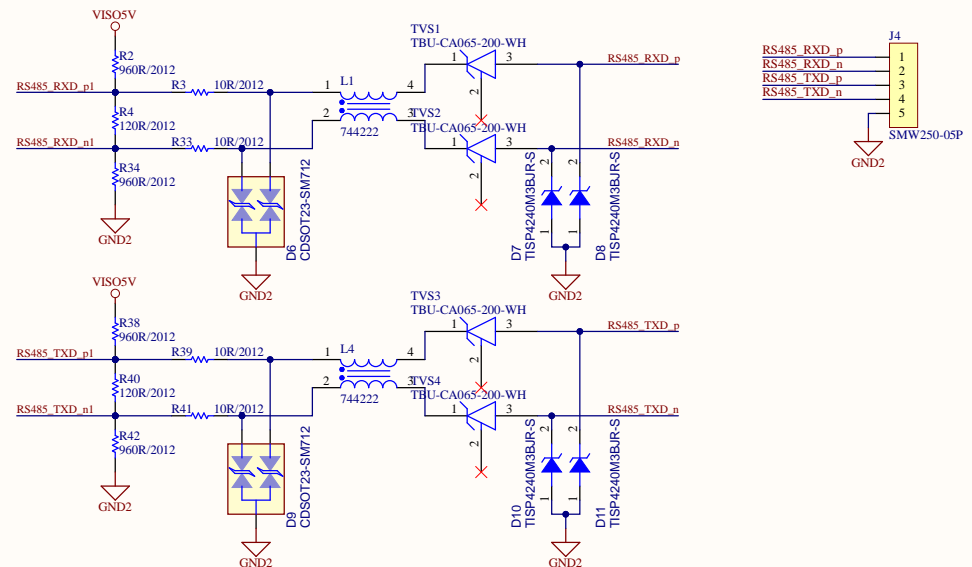



Mount Hole



Title			
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DrawnBy: *			

The schematic diagram illustrates the internal circuitry of the trigger board, featuring two input channels and two output channels. Each channel is implemented using a CPC1017NTR optocoupler (U6 and U7 for inputs, U8 and U9 for outputs). The input signals, TRIGGER-IN_1 and TRIGGER-IN_2, are connected to the LD1 pins of U6 and U7 respectively, through resistors R17 and R22, and capacitors C39 and C40. The output signals, TRIGGER-OUT_1 and TRIGGER-OUT_2, are connected to the LD2 pins of U8 and U9 respectively, through resistors R27 and R31. The board also includes a 5V regulator (U6) and a 5V regulator (U7). The inputs are connected to DC3.3V through resistors R17 and R22, and capacitors C39 and C40. The outputs are connected to VISO5V through resistors R18 and R20, and capacitors R27 and R31. The board also includes a 5V regulator (U6) and a 5V regulator (U7).

[illegible]

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