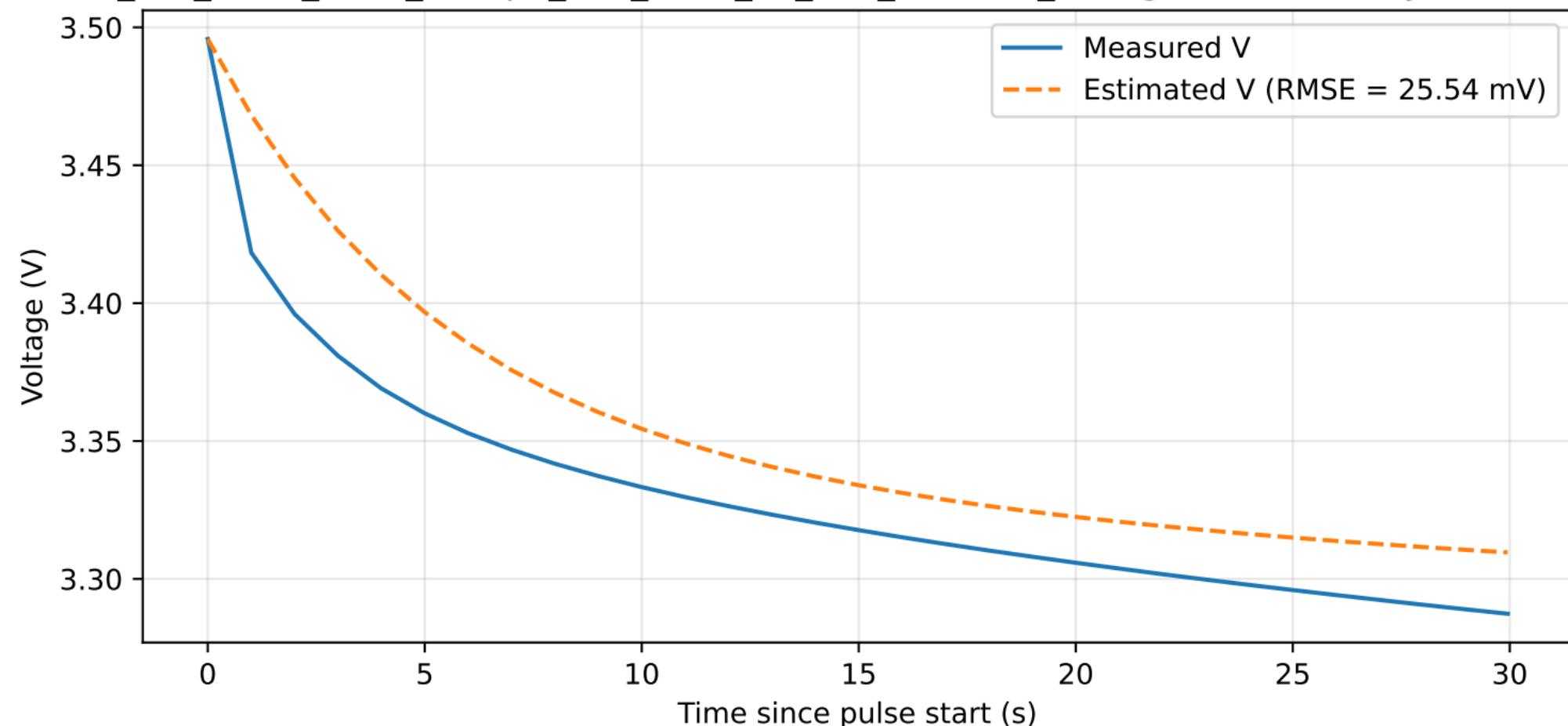
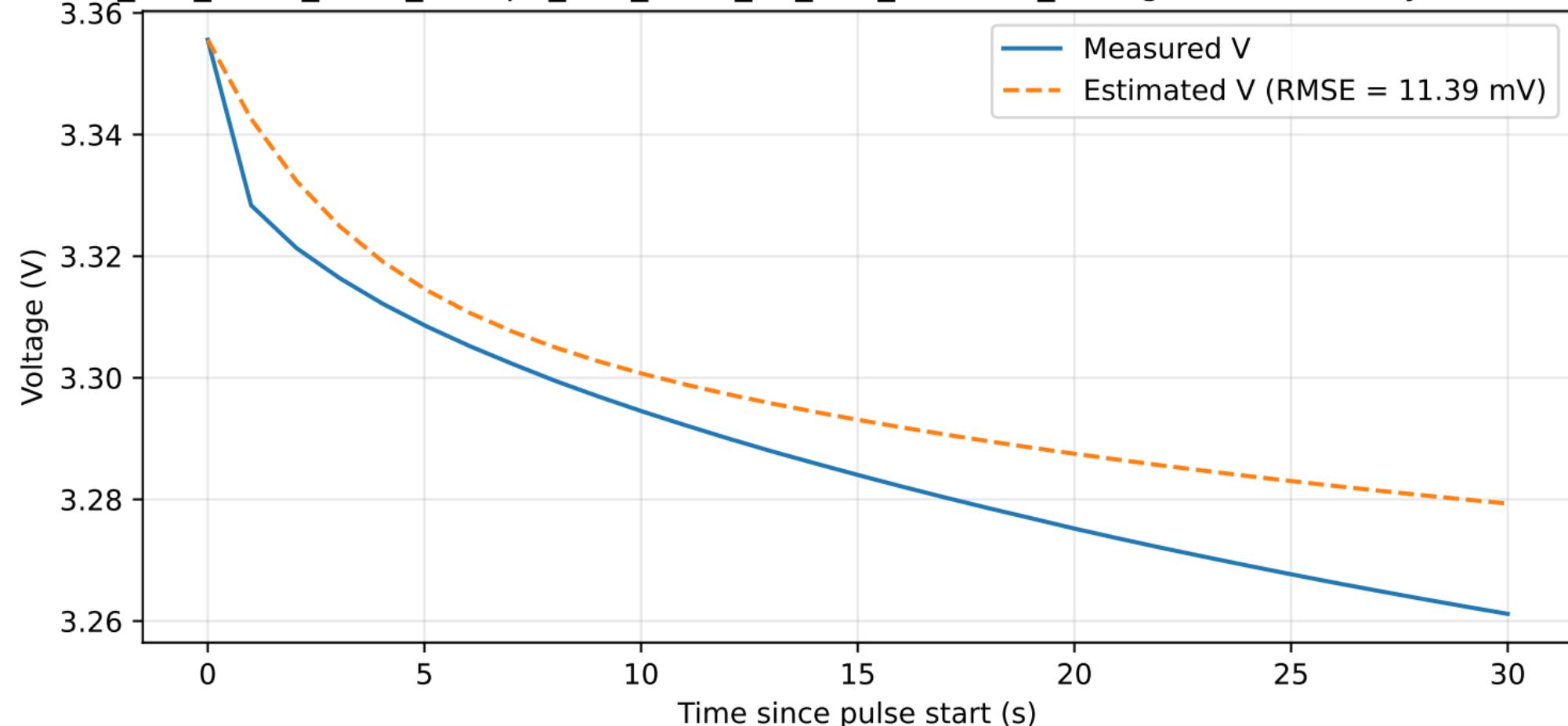


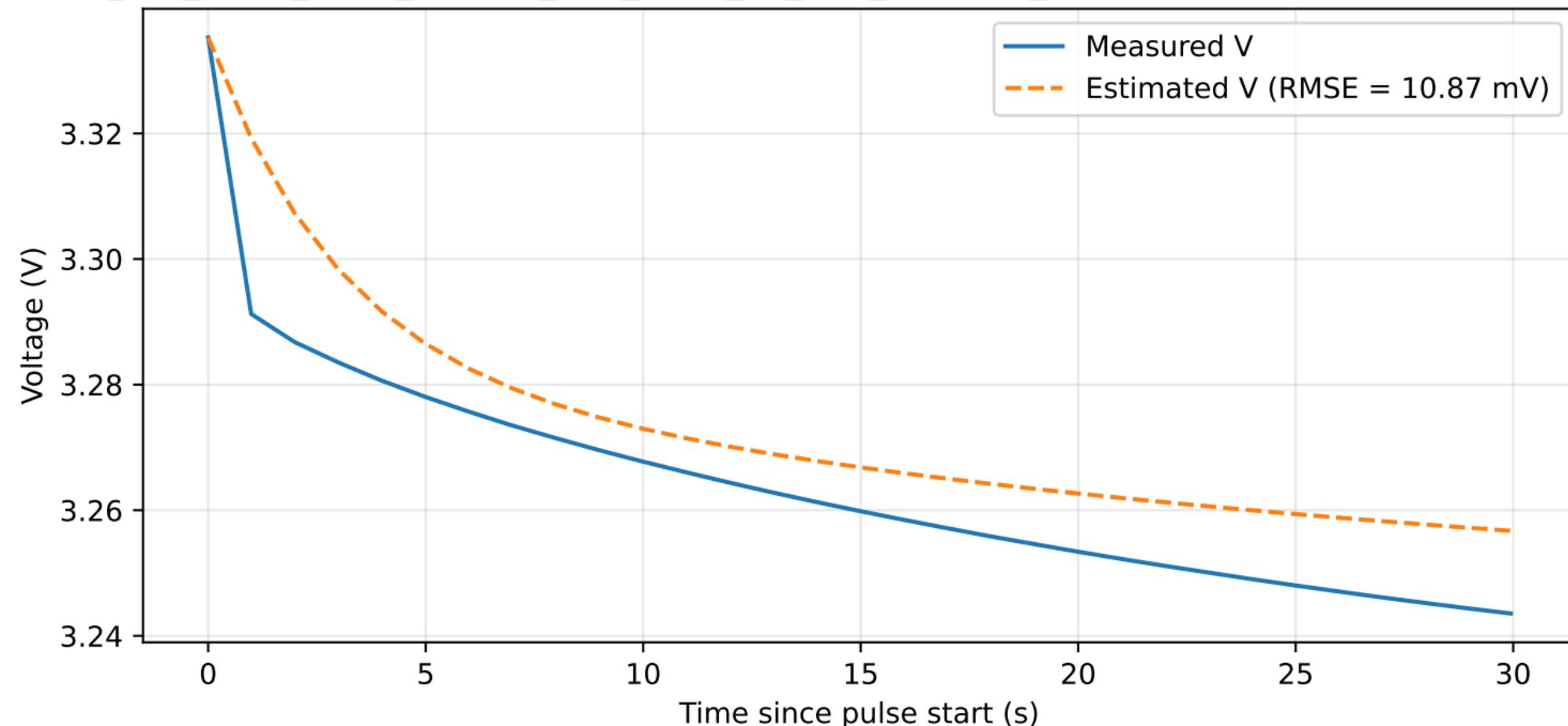
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



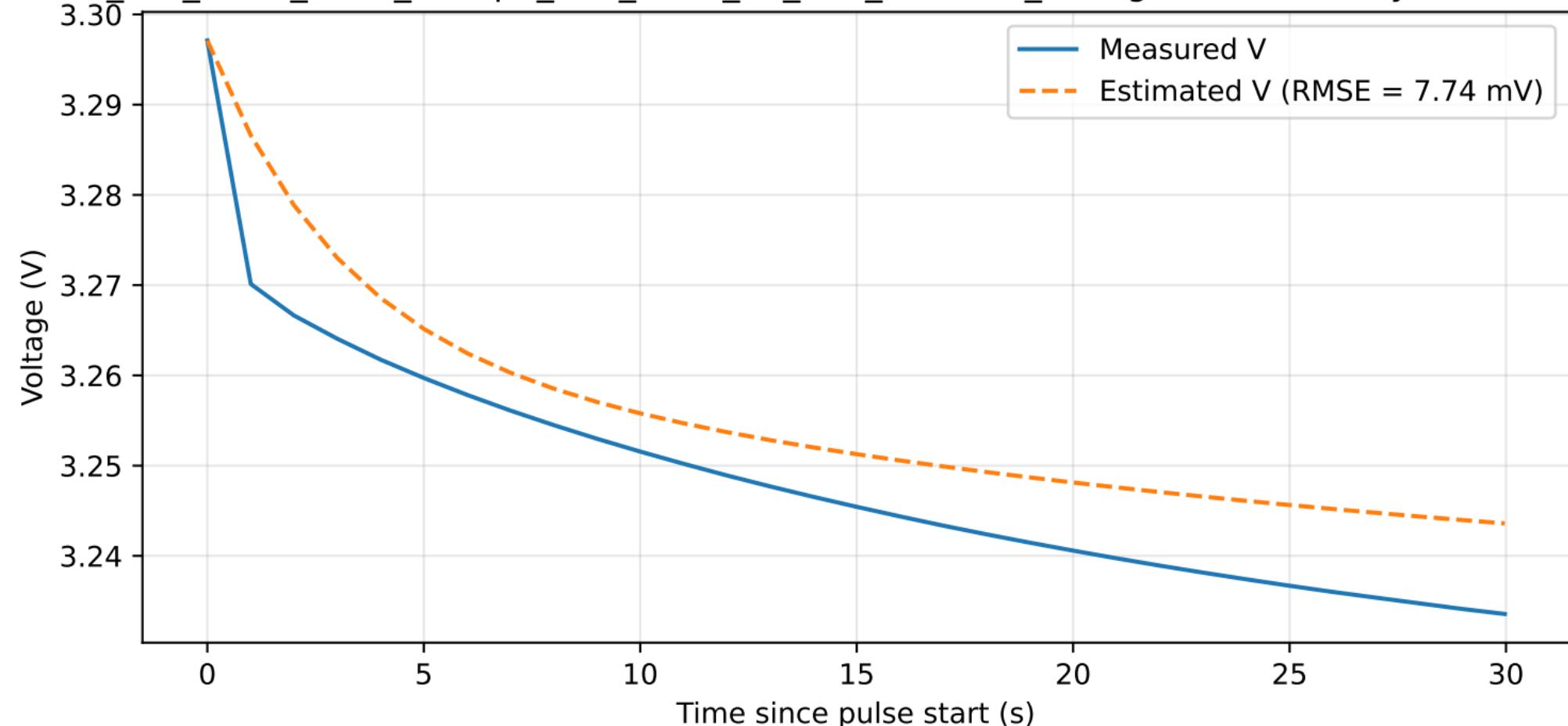
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



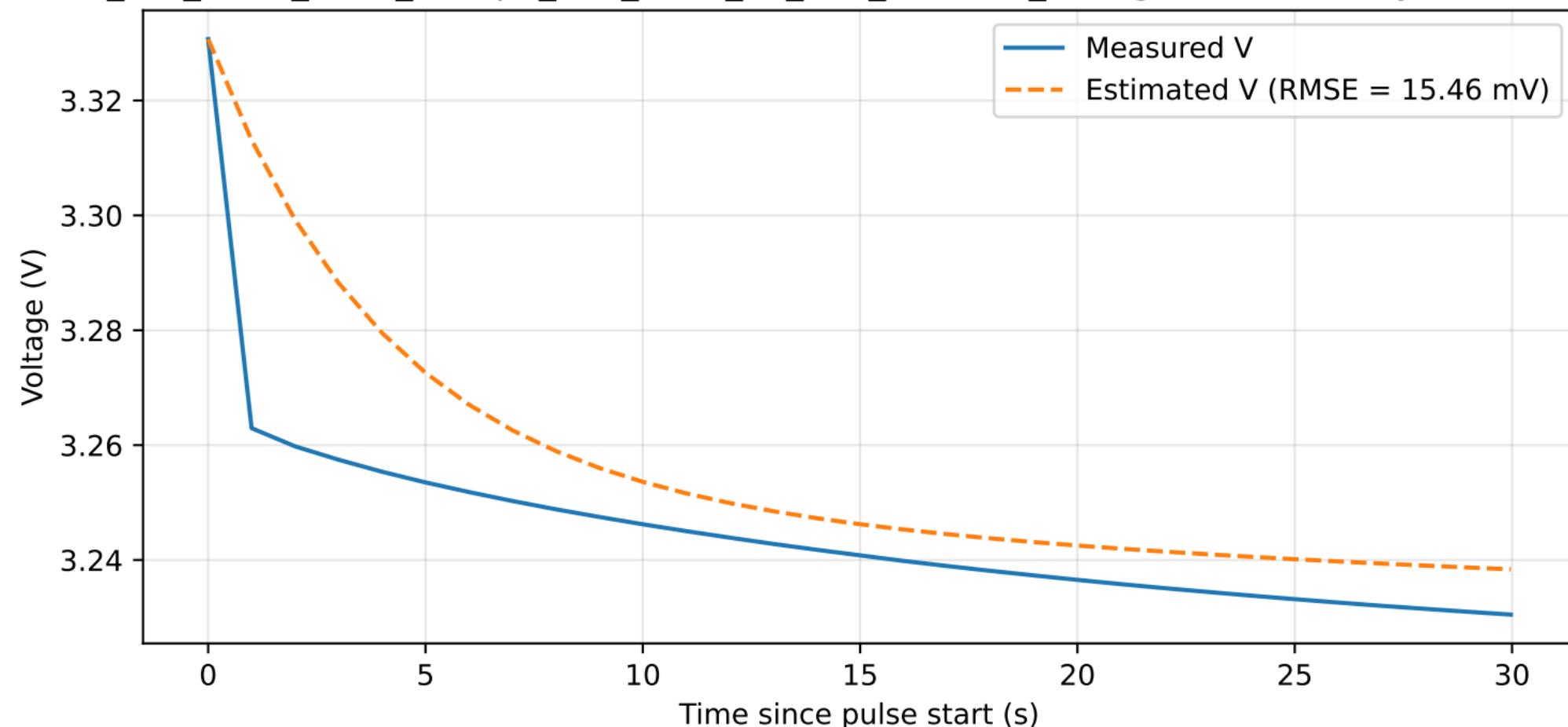
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



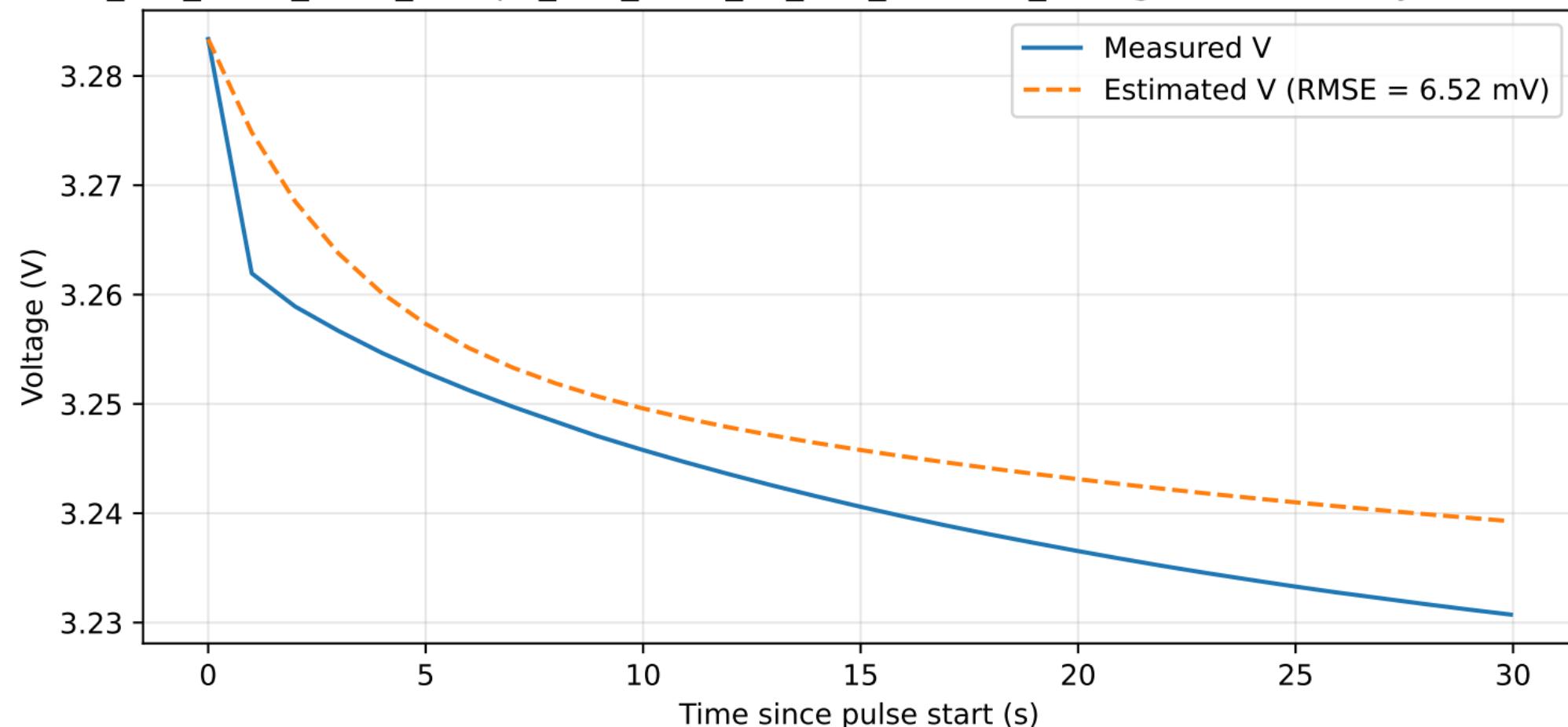
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



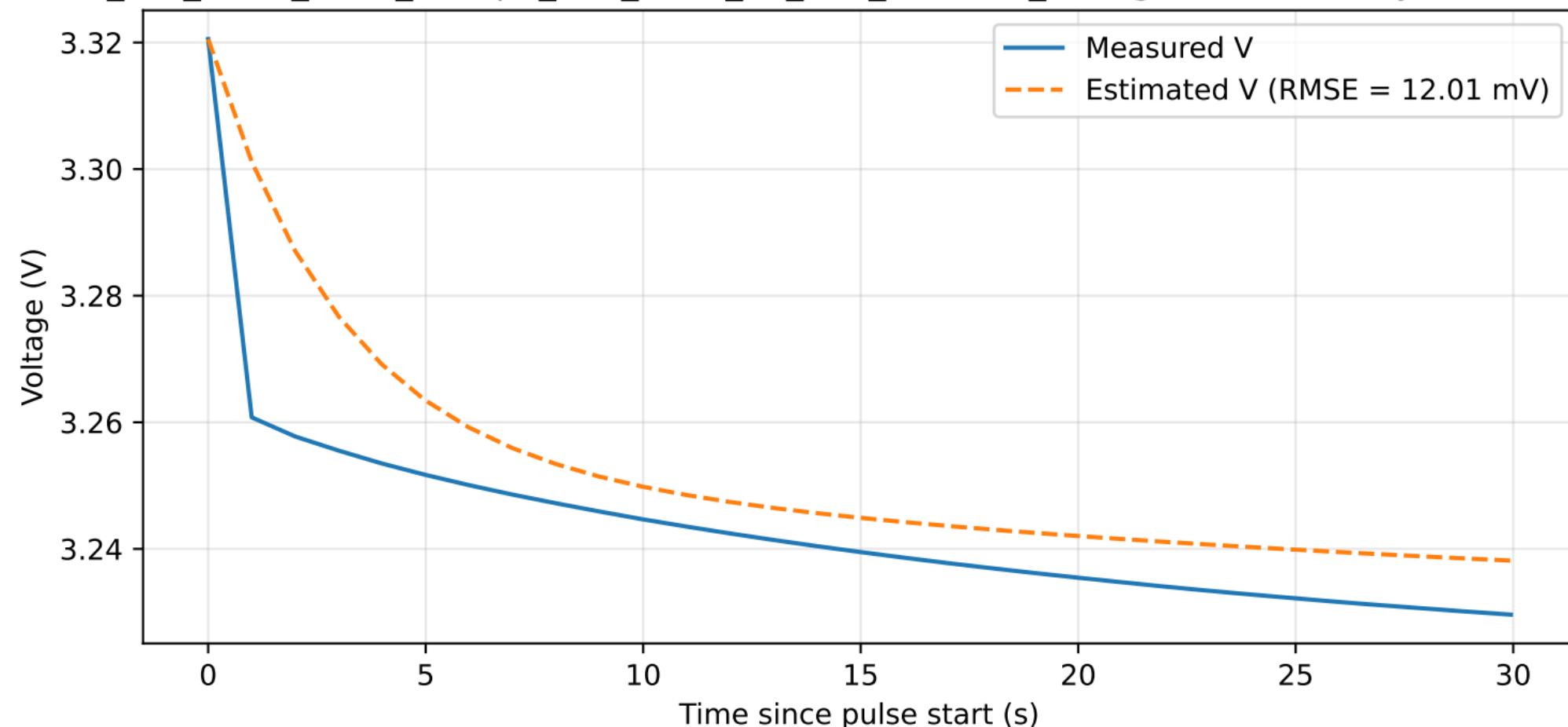
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



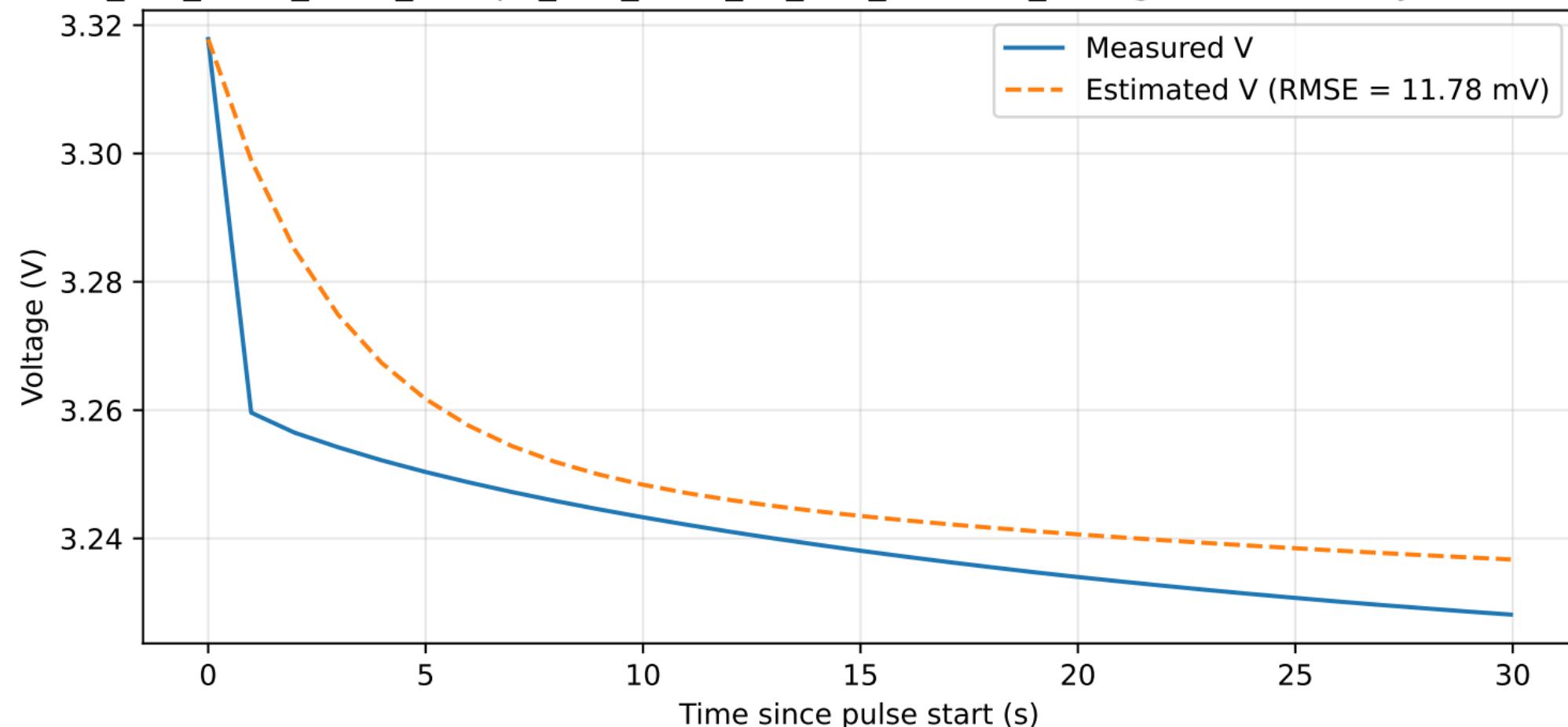
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



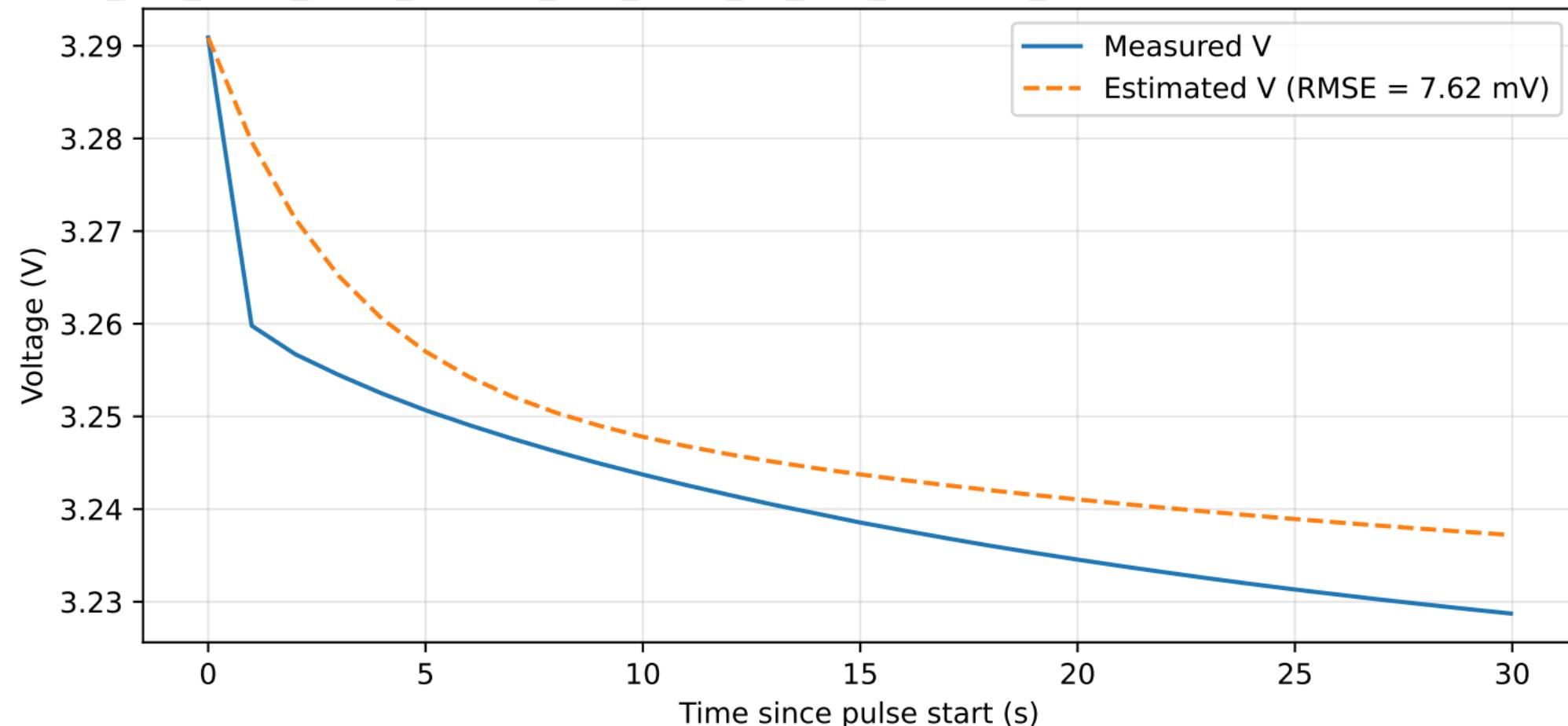
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



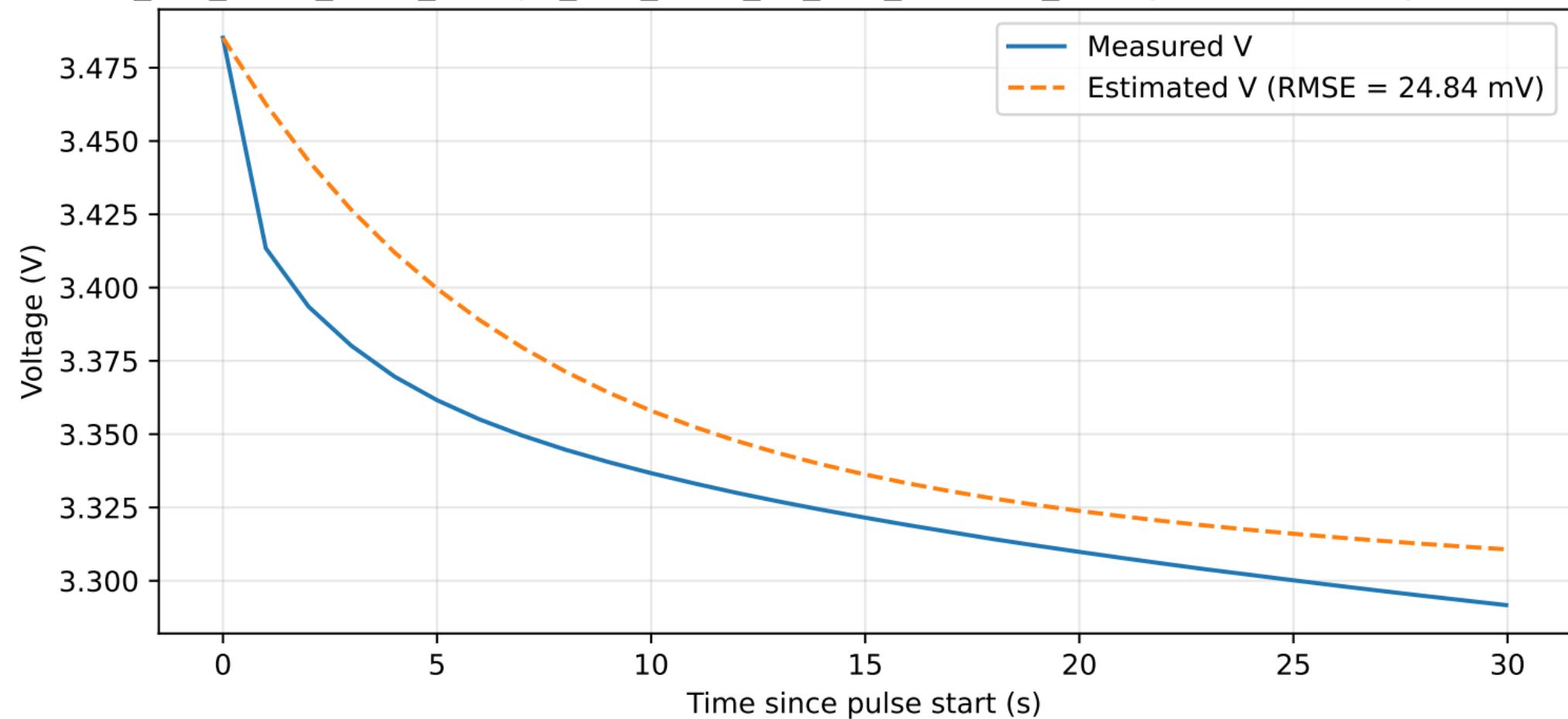
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



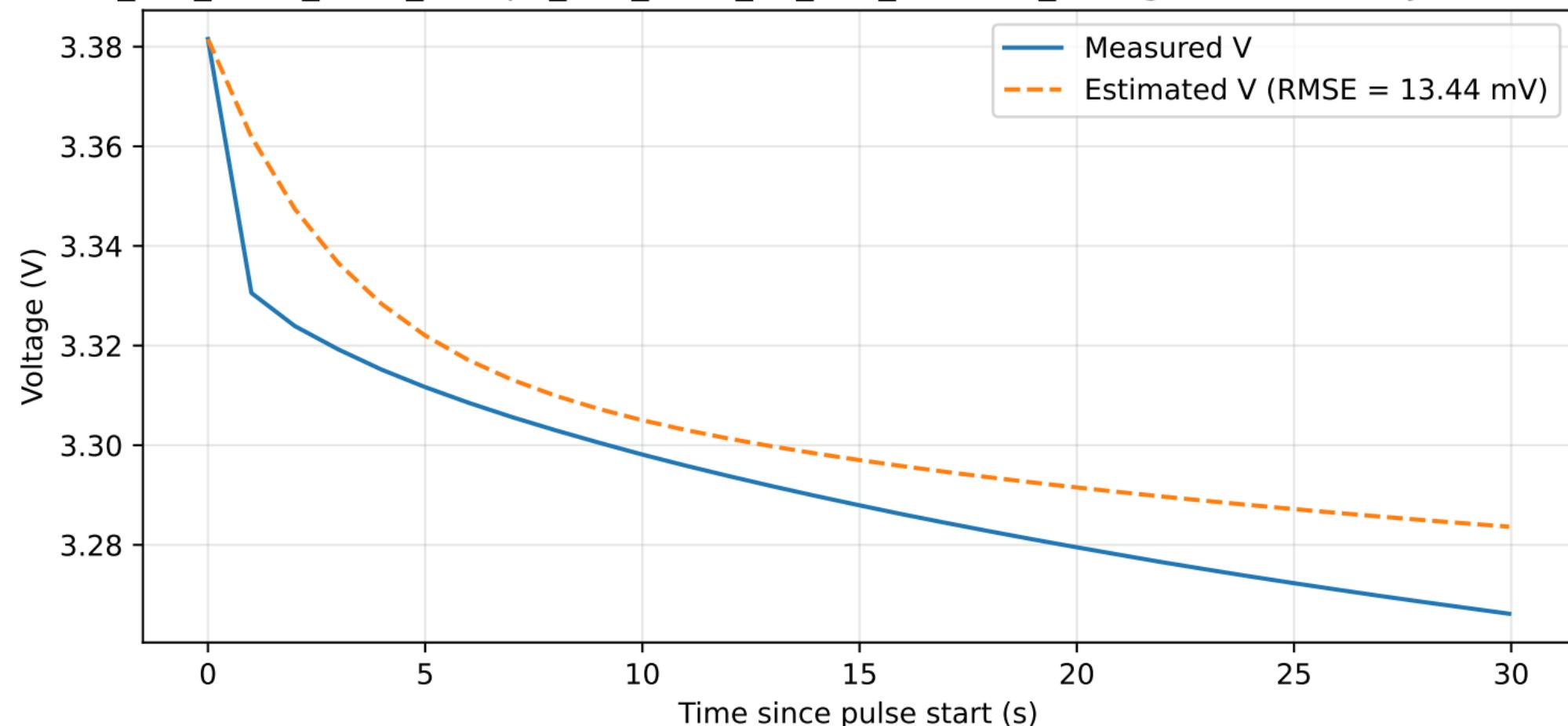
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0001\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



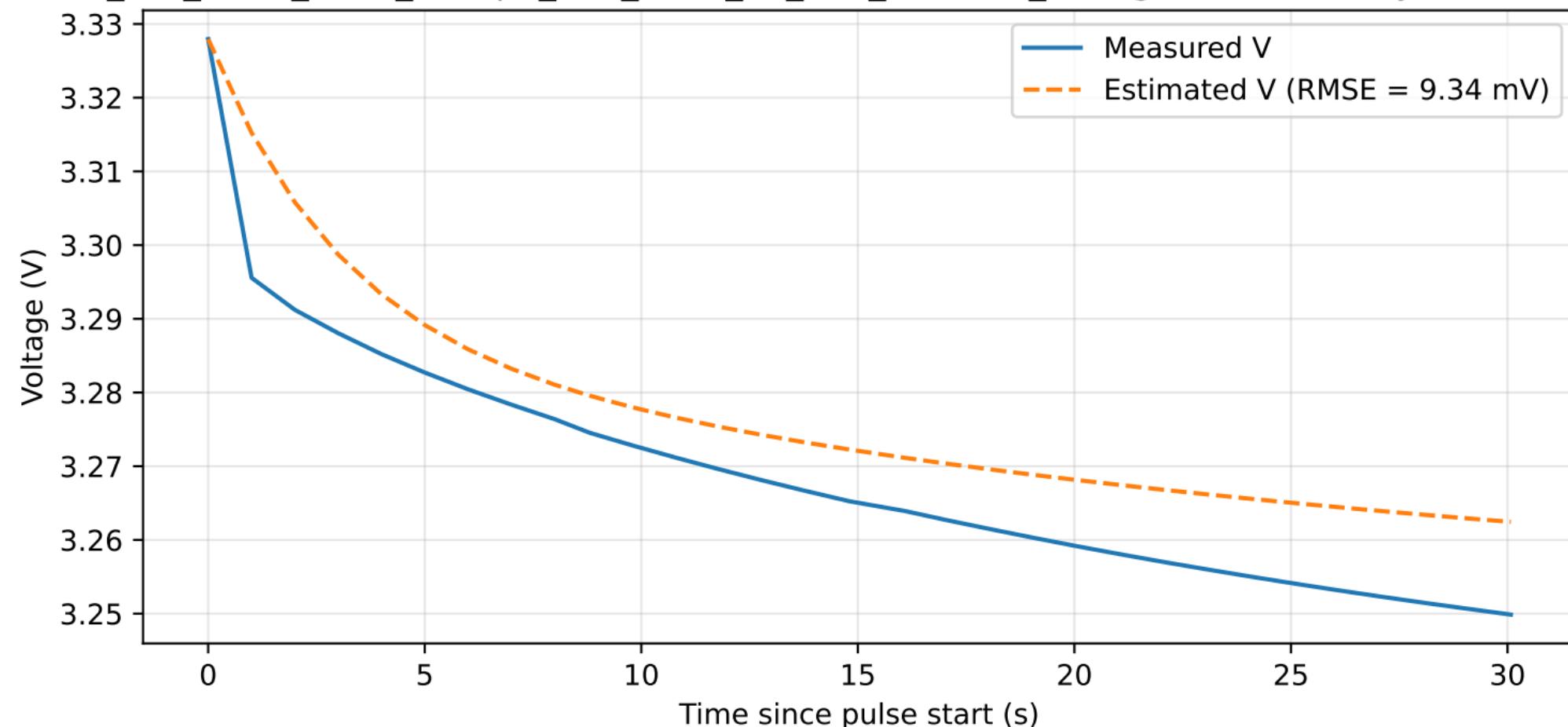
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



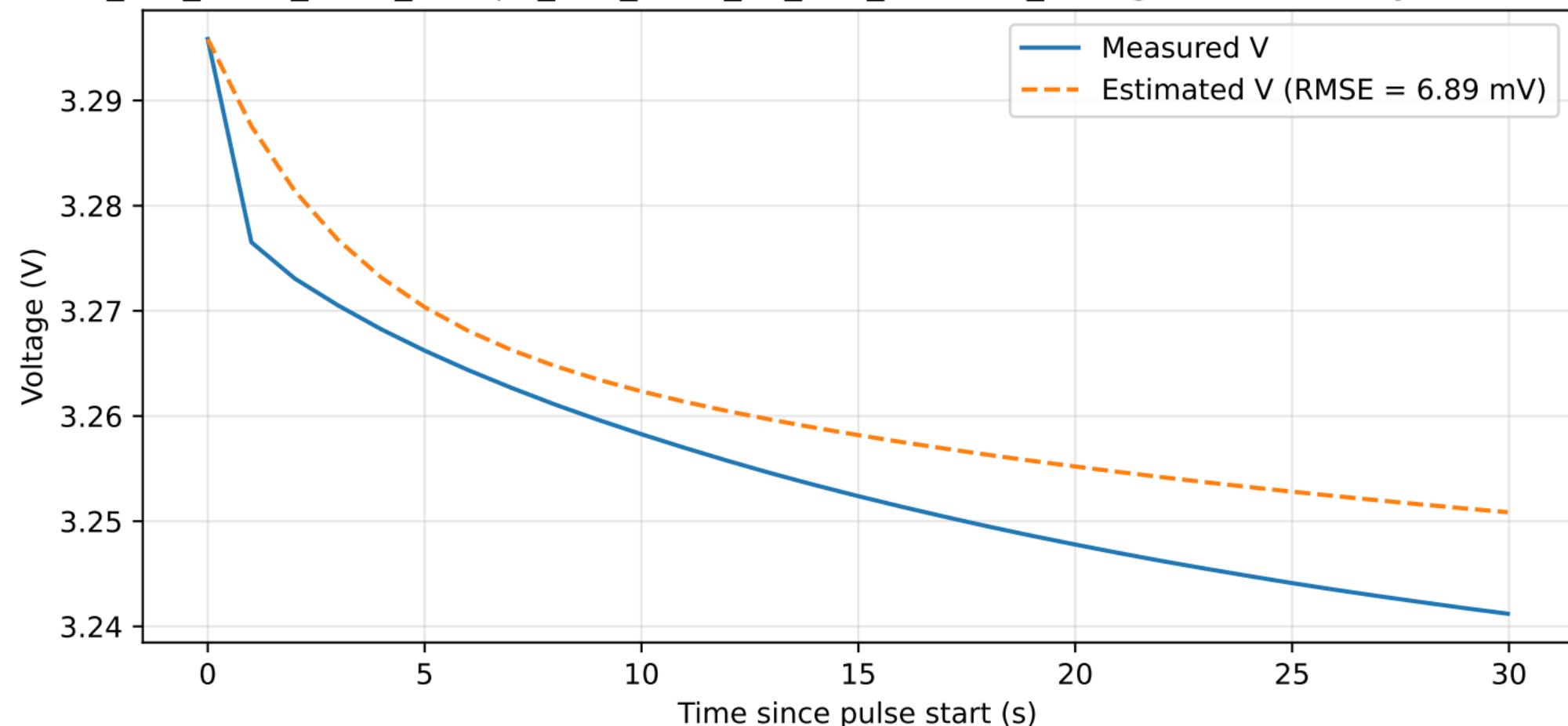
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



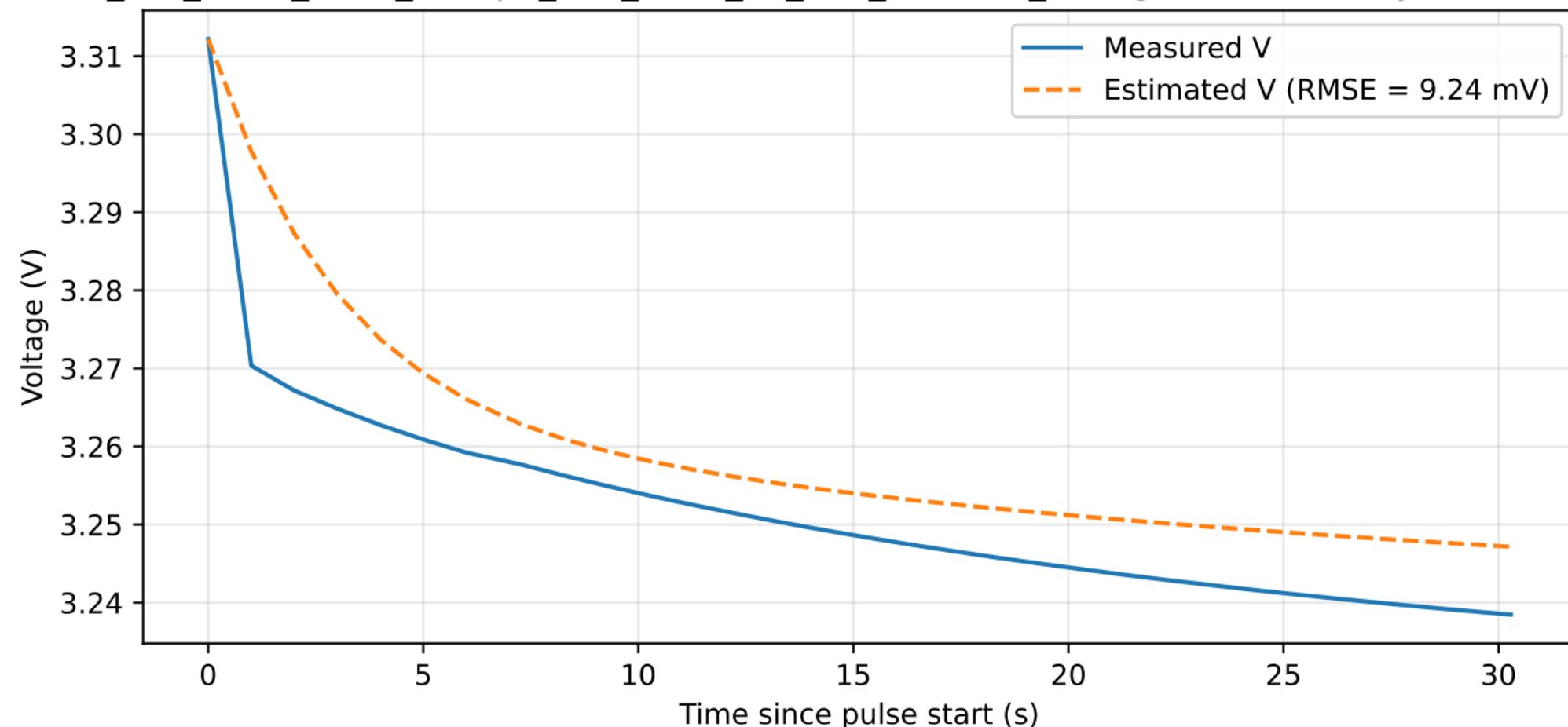
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



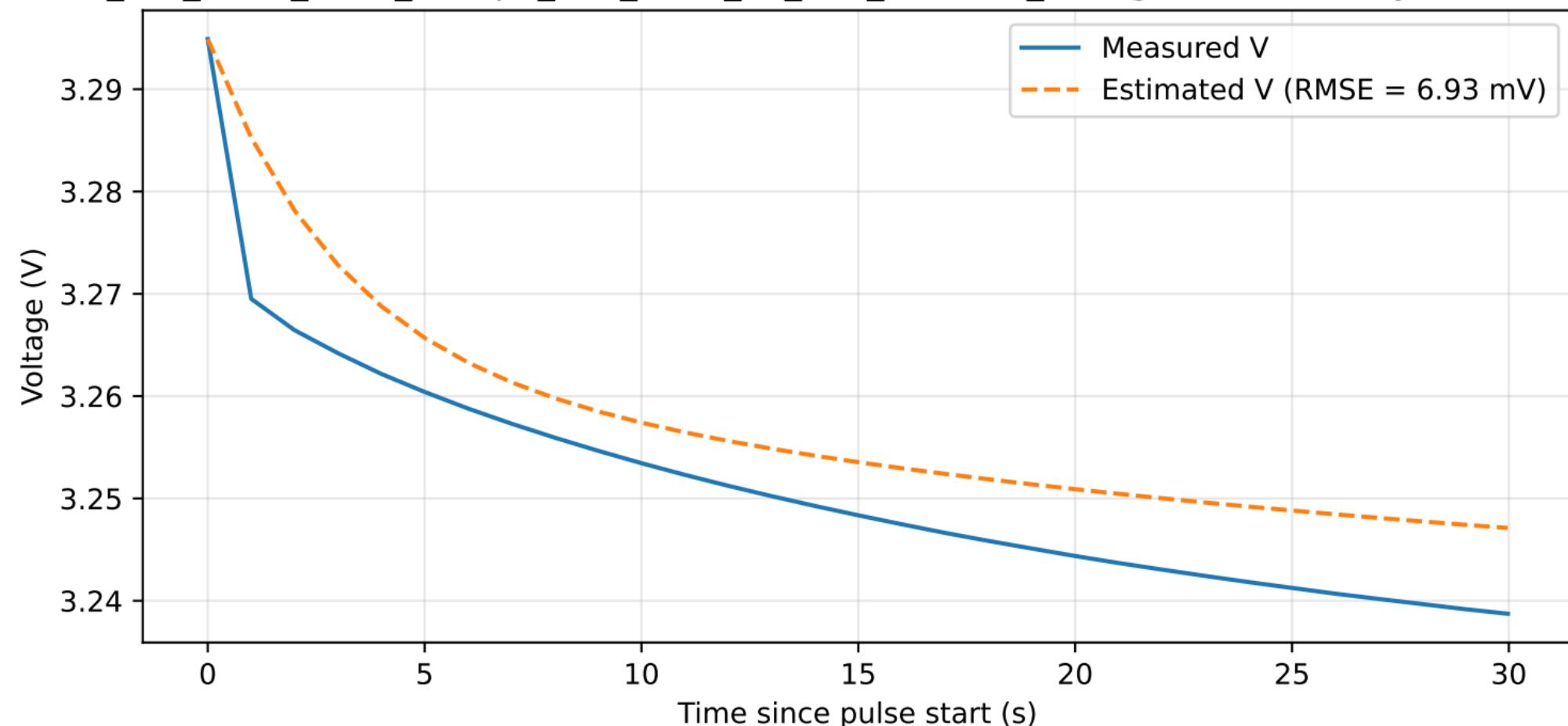
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



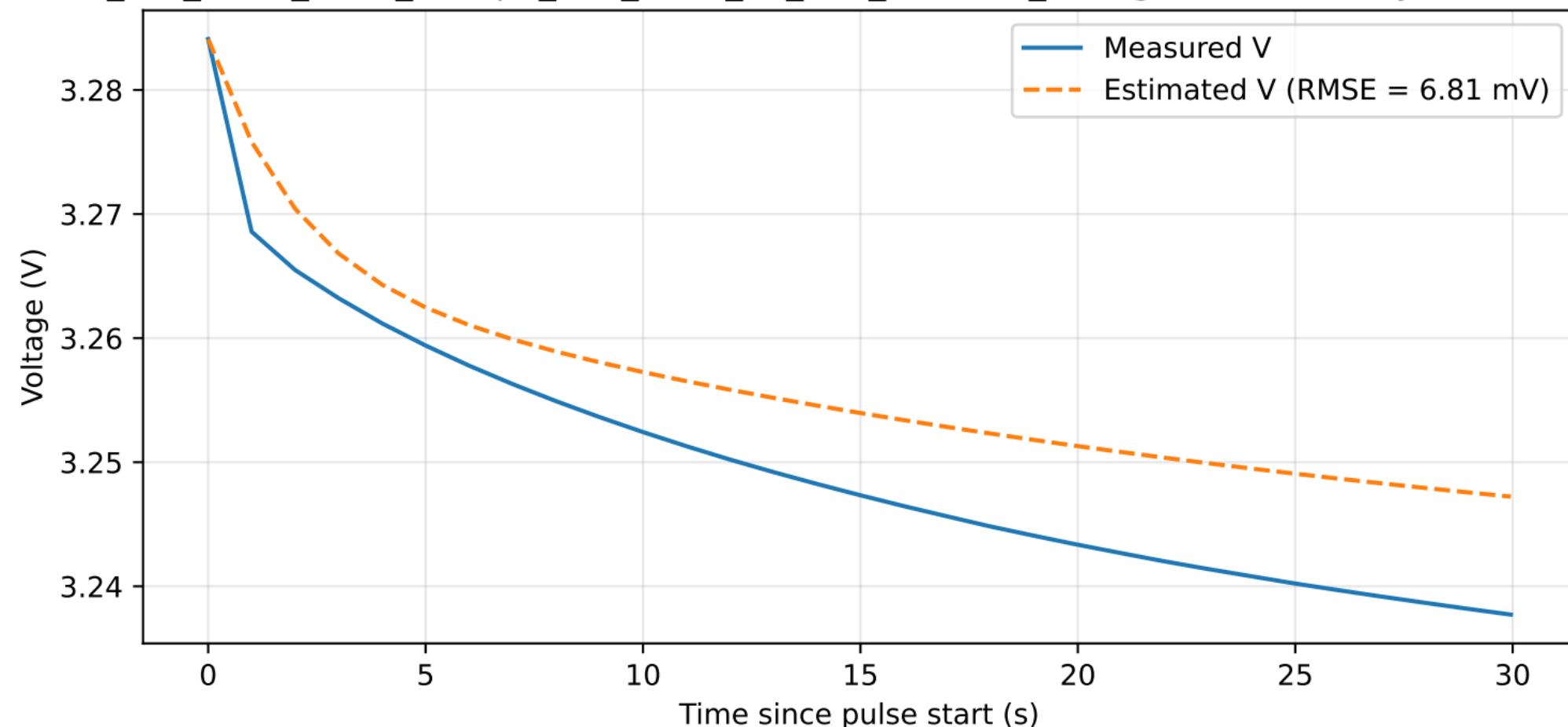
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



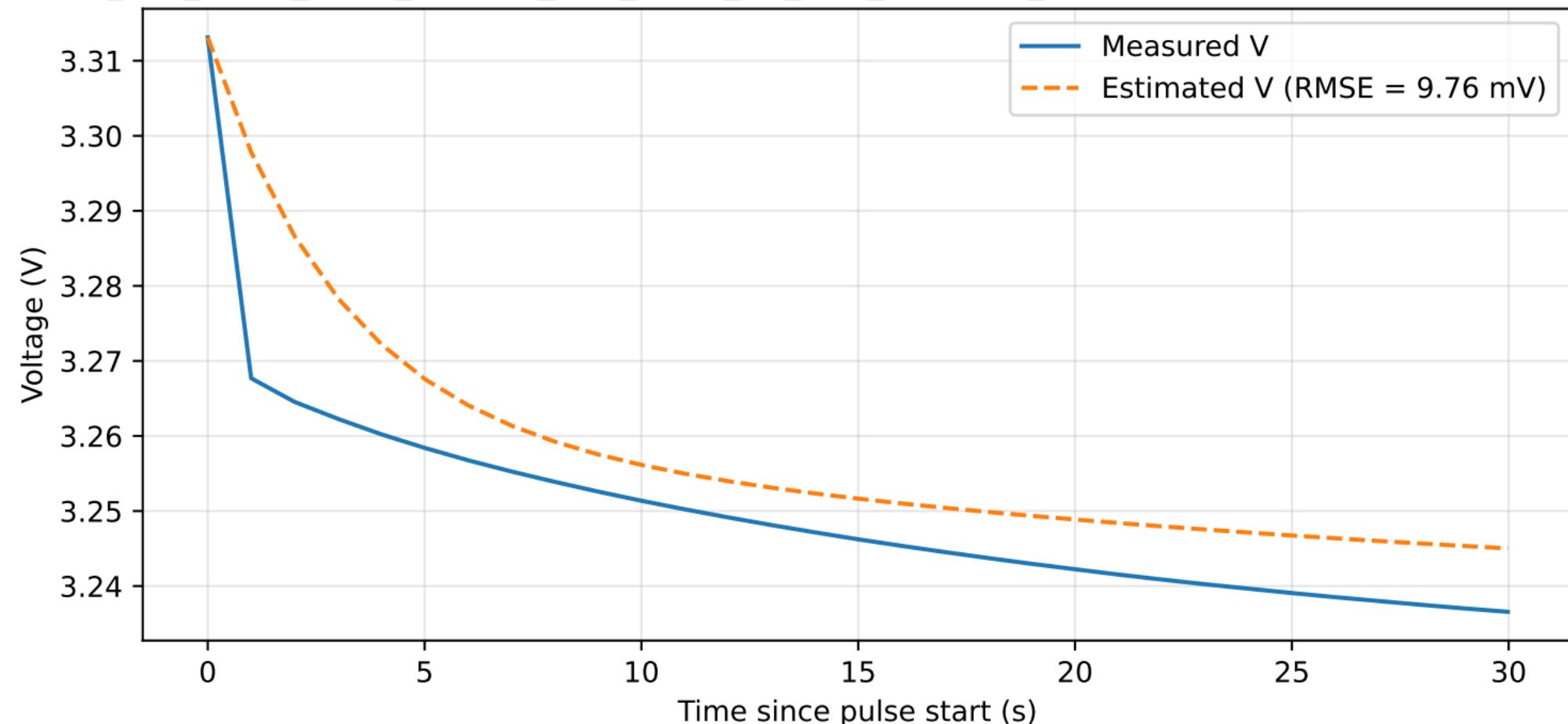
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



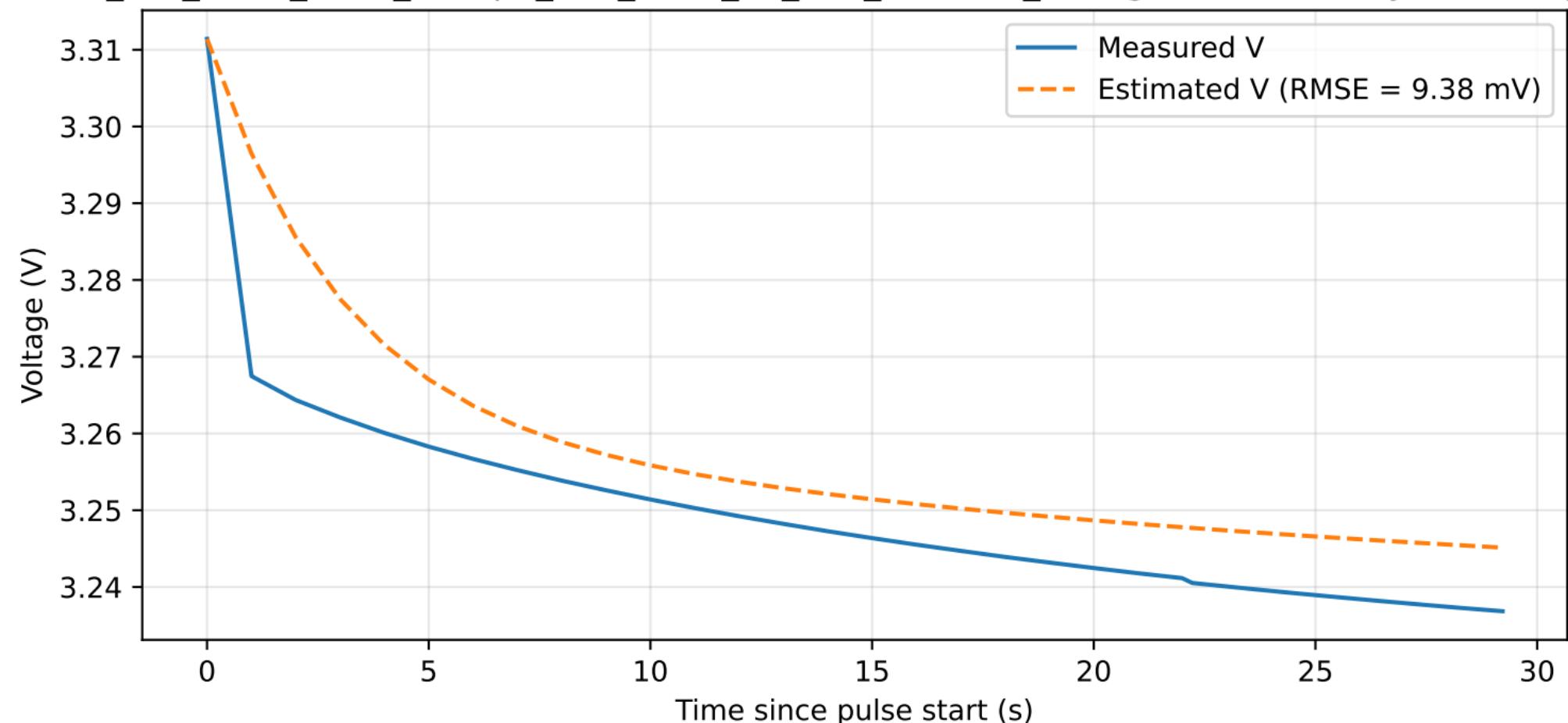
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



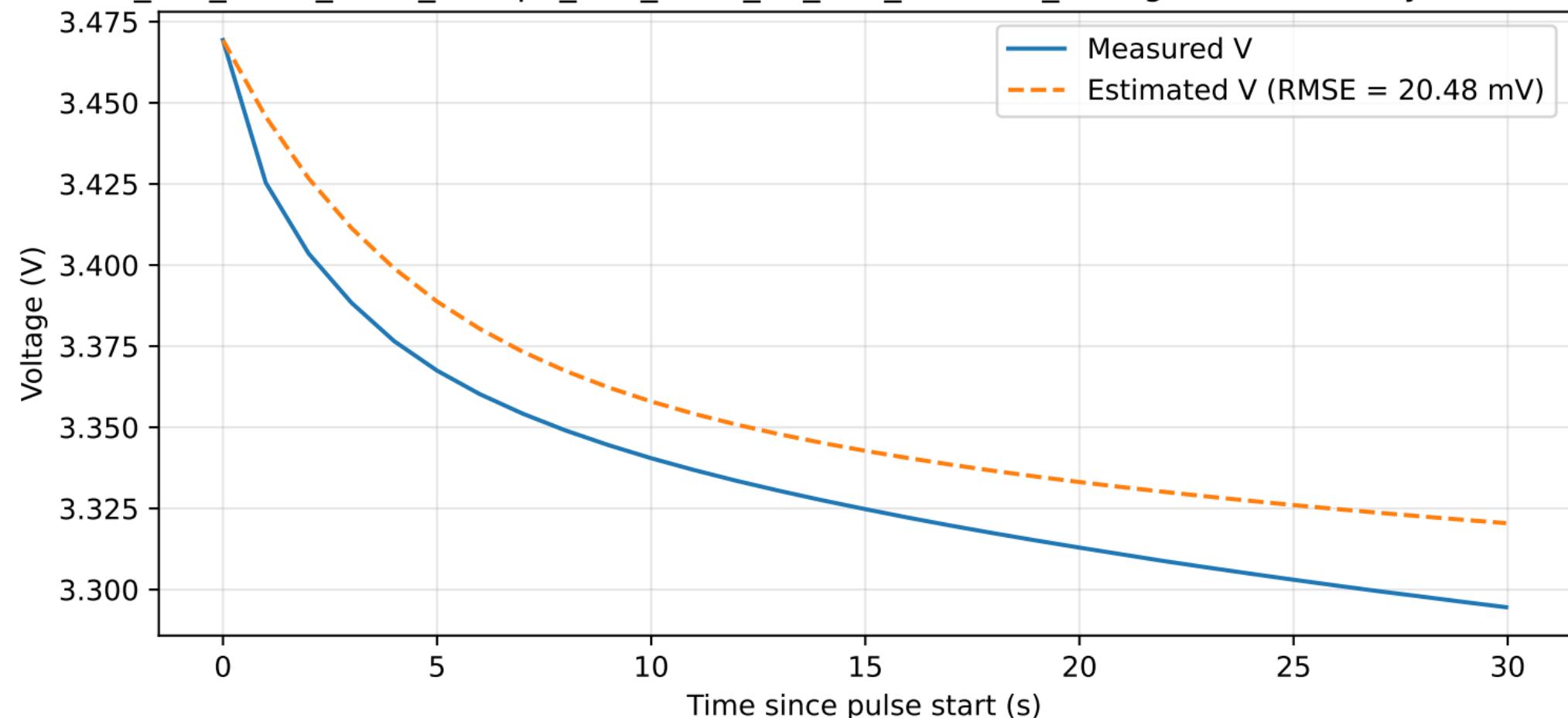
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



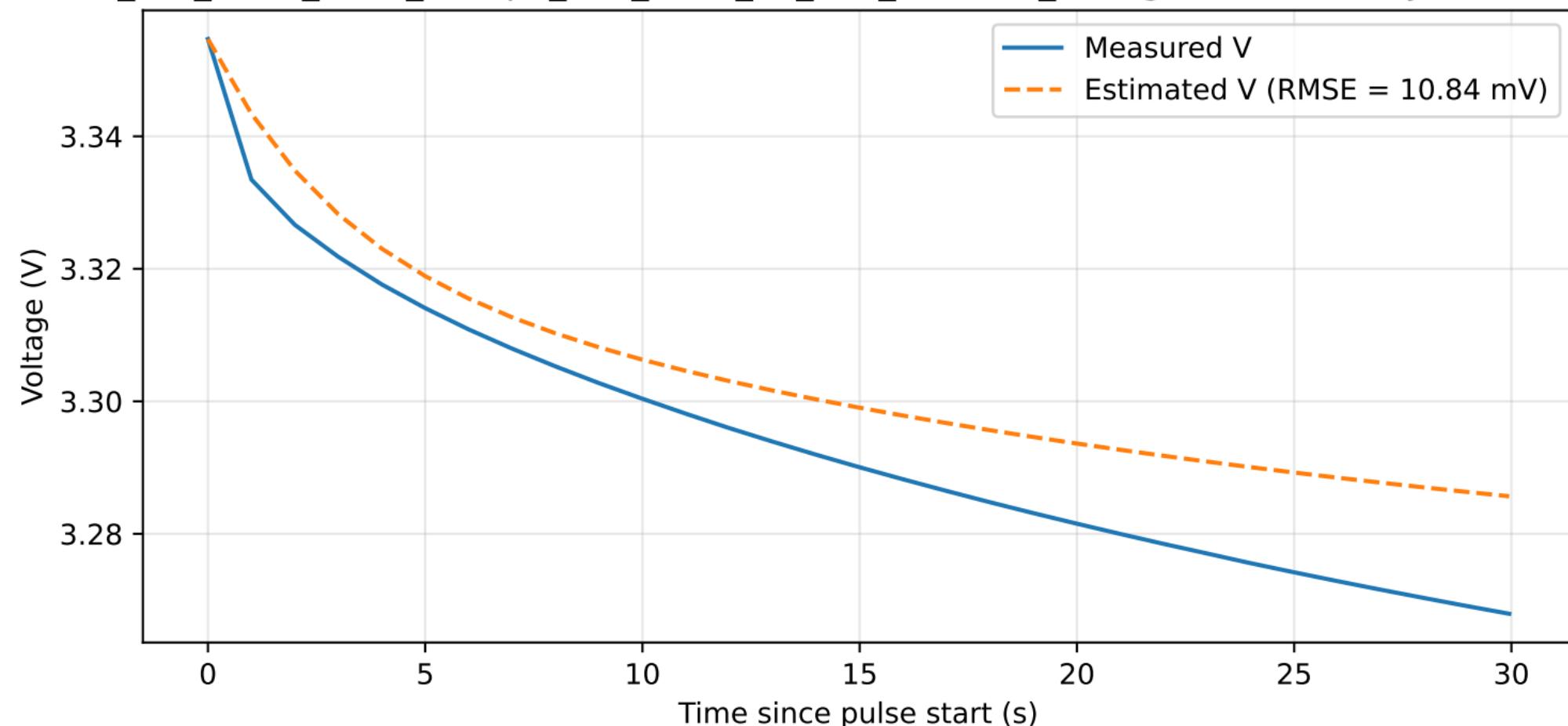
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0003\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



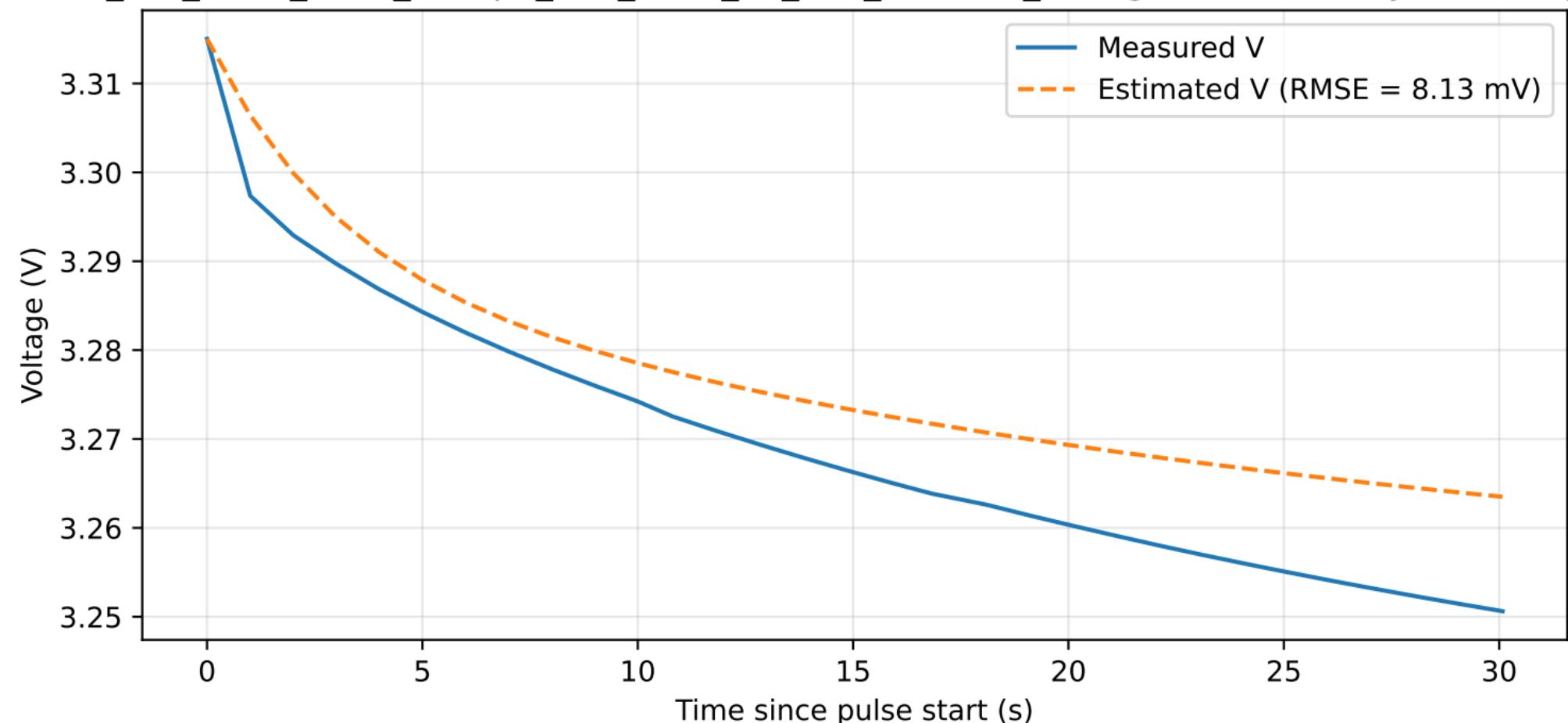
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



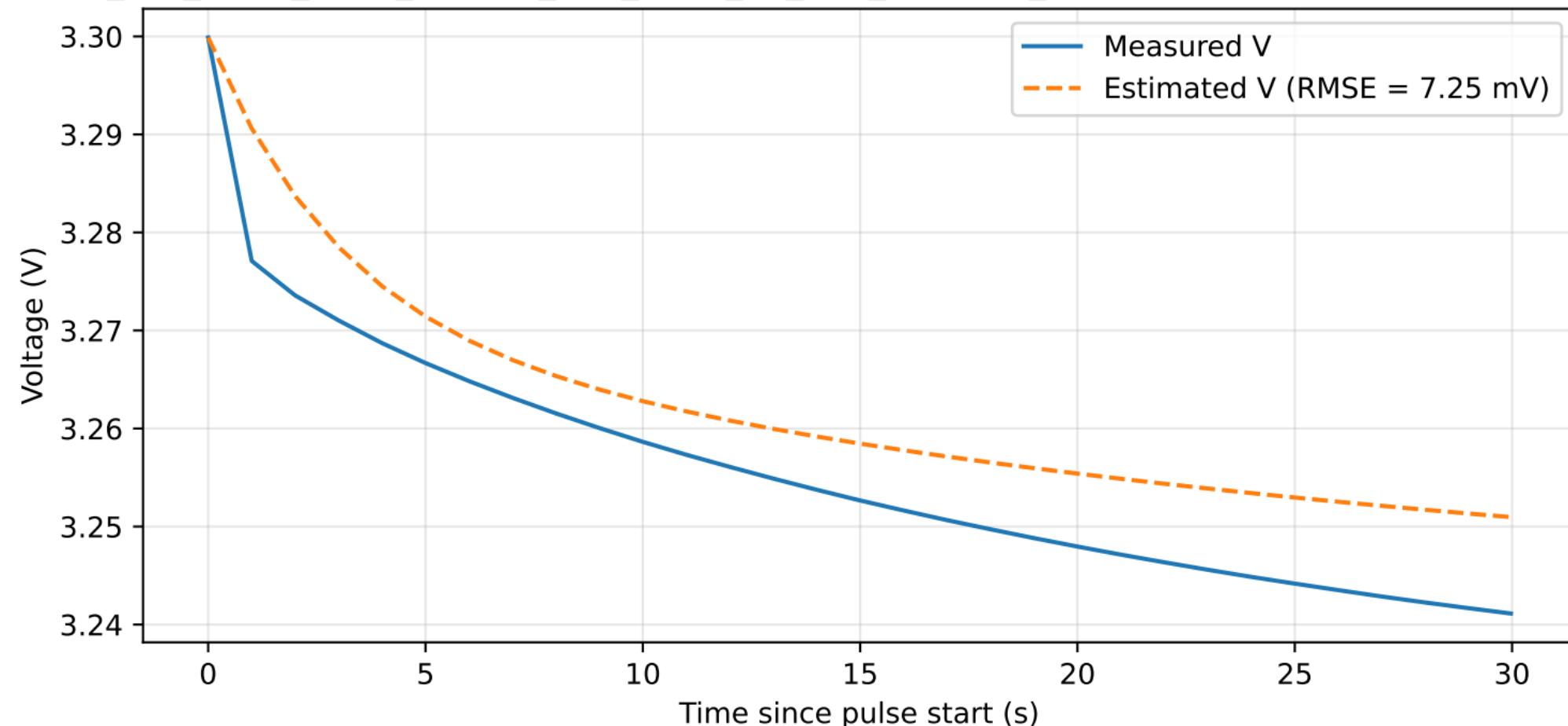
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



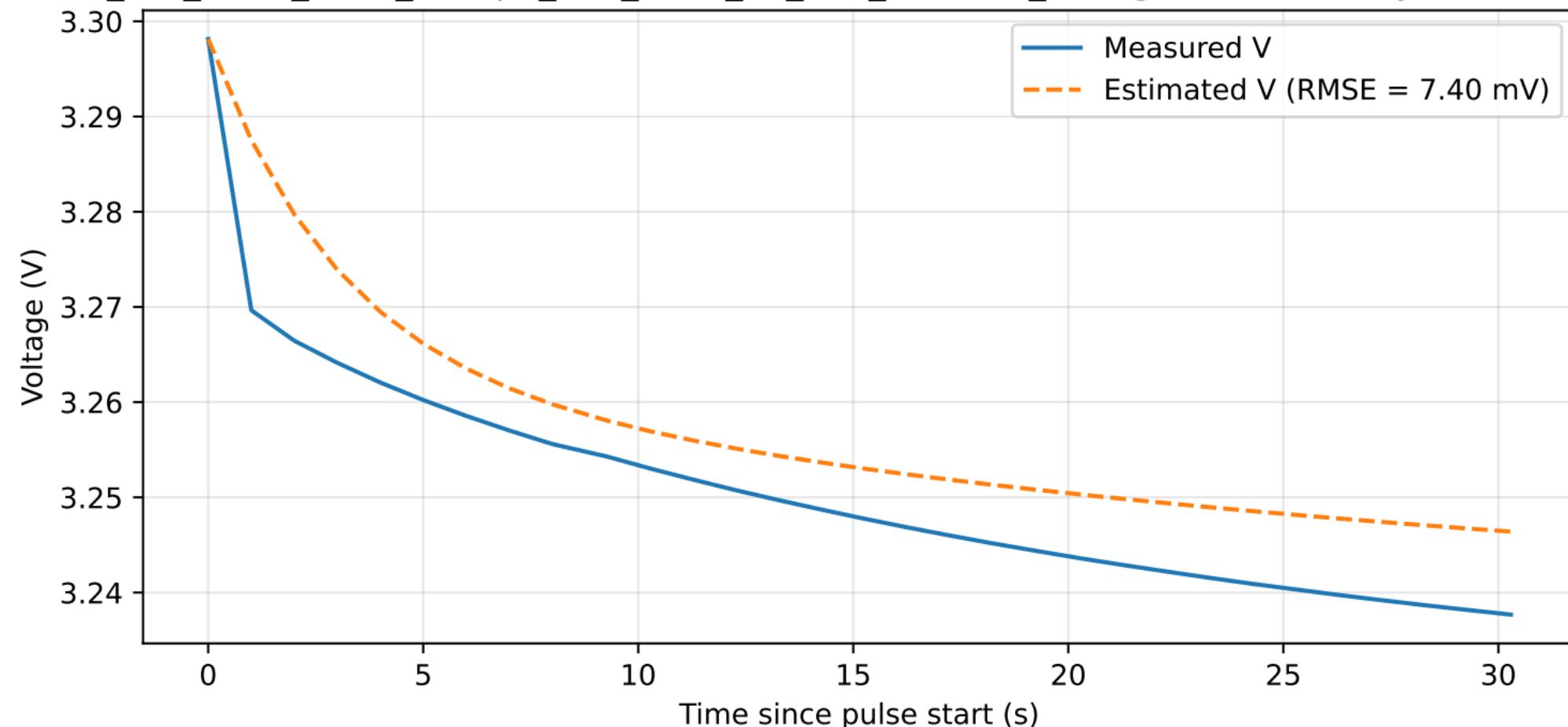
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



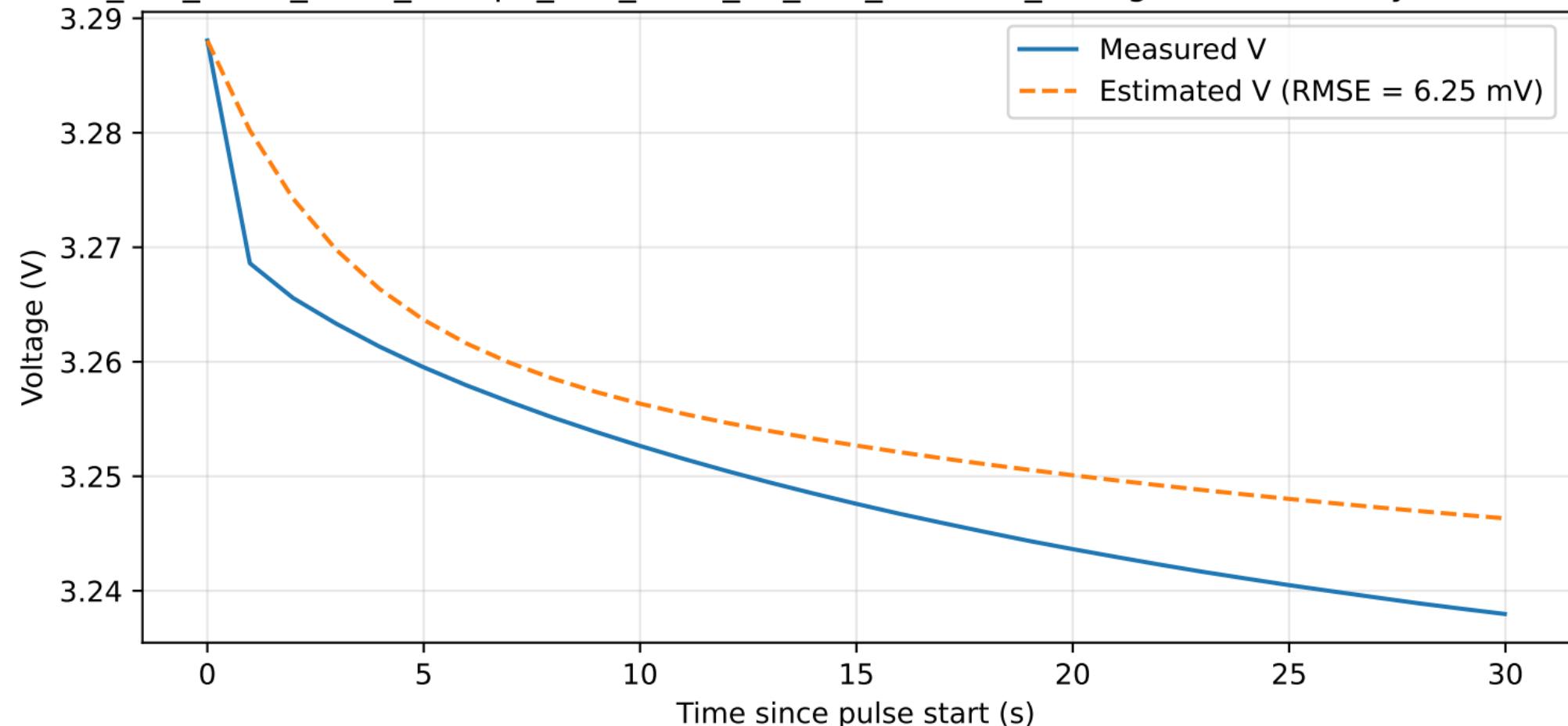
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



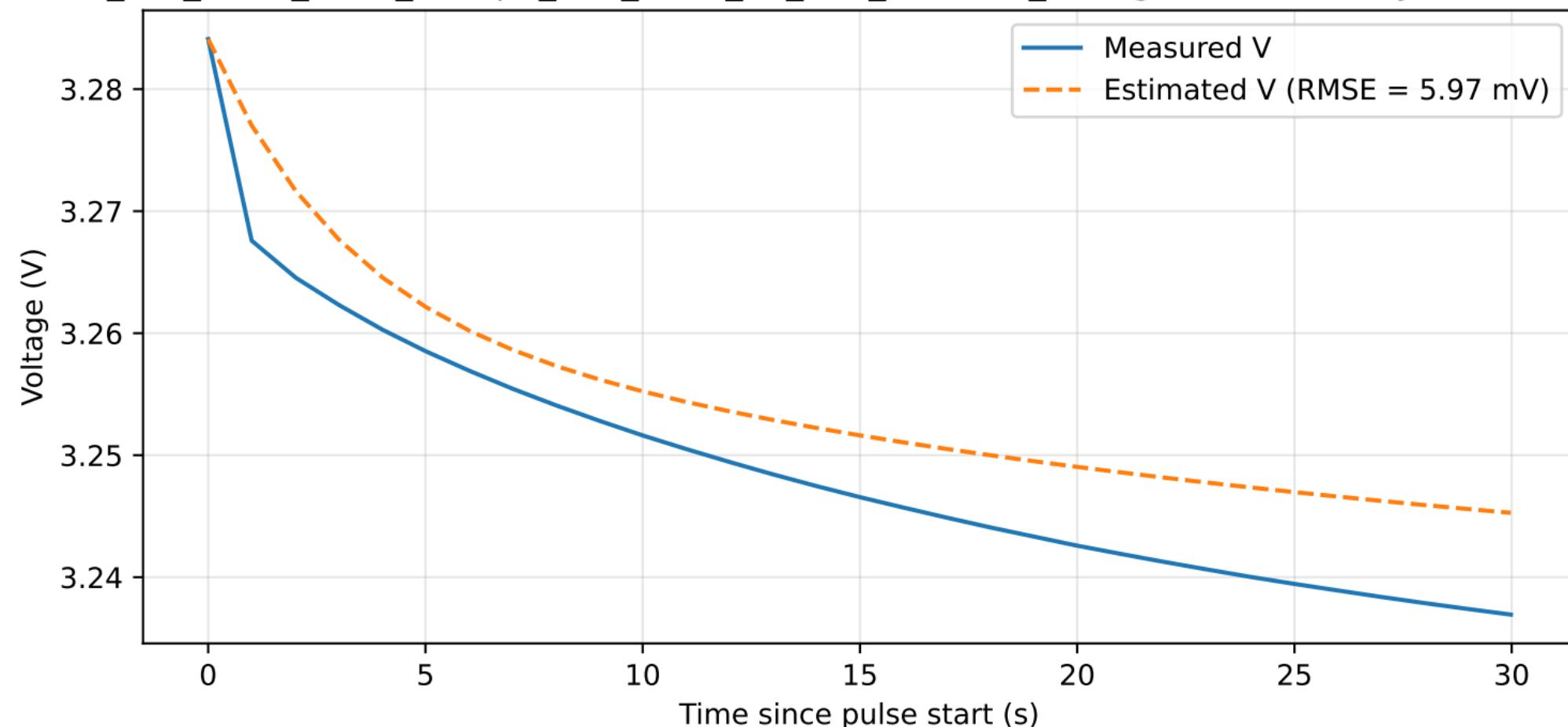
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



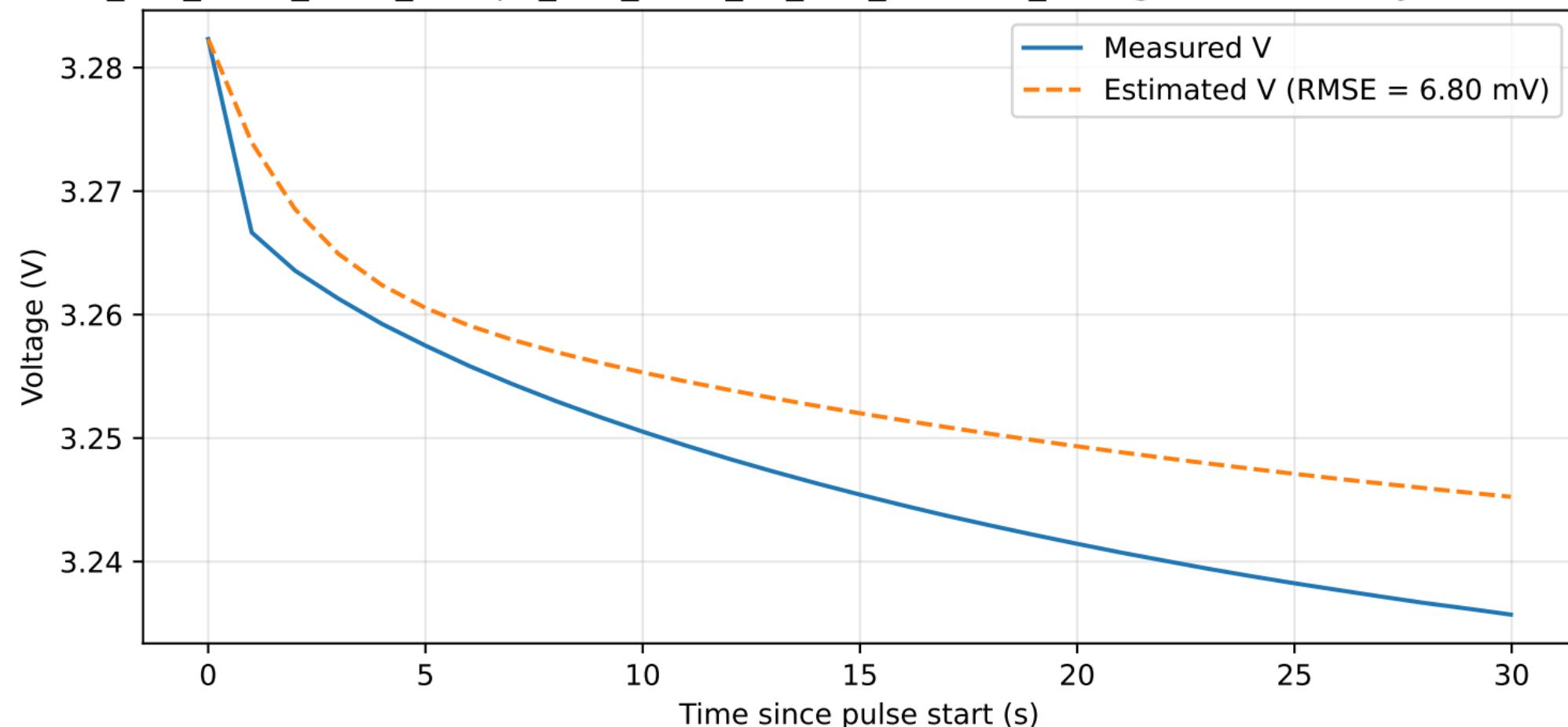
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



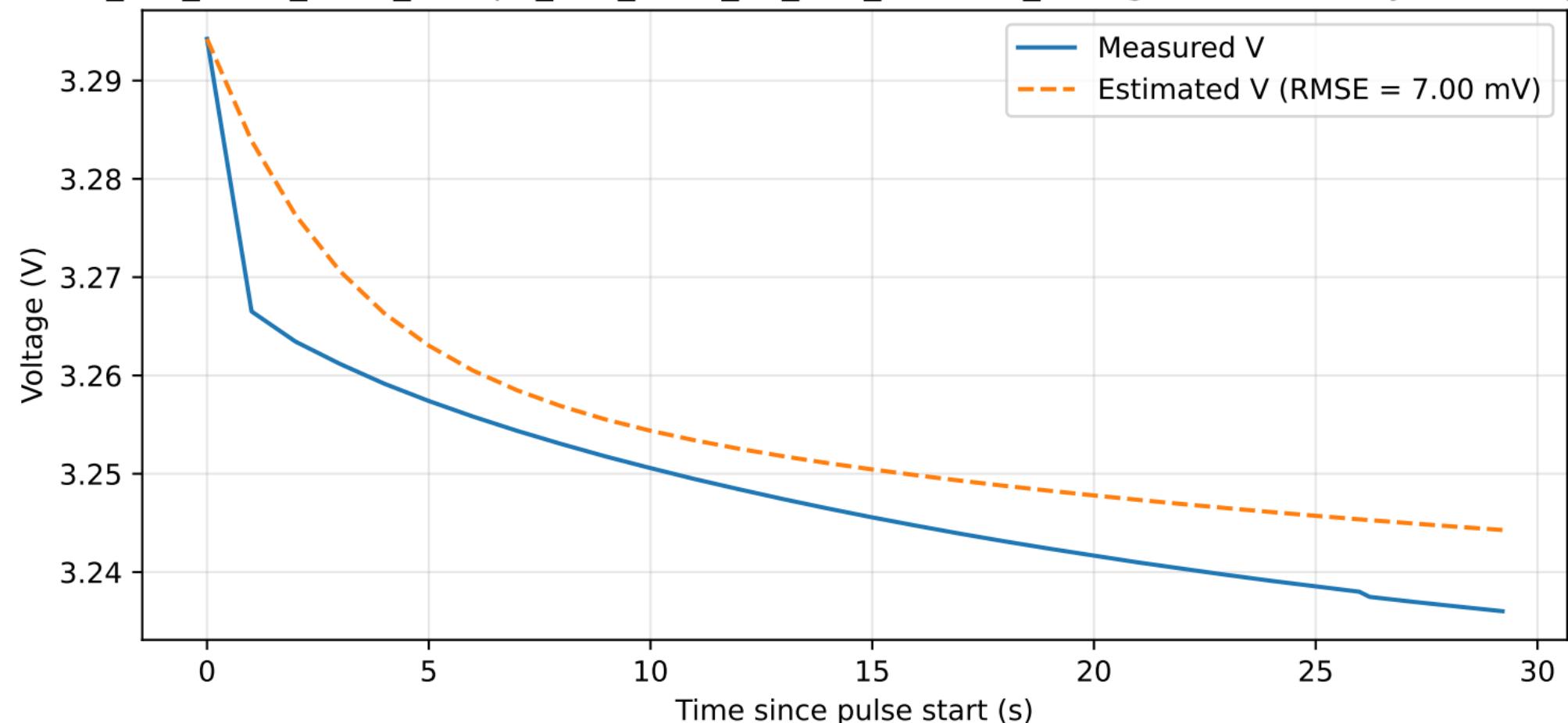
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



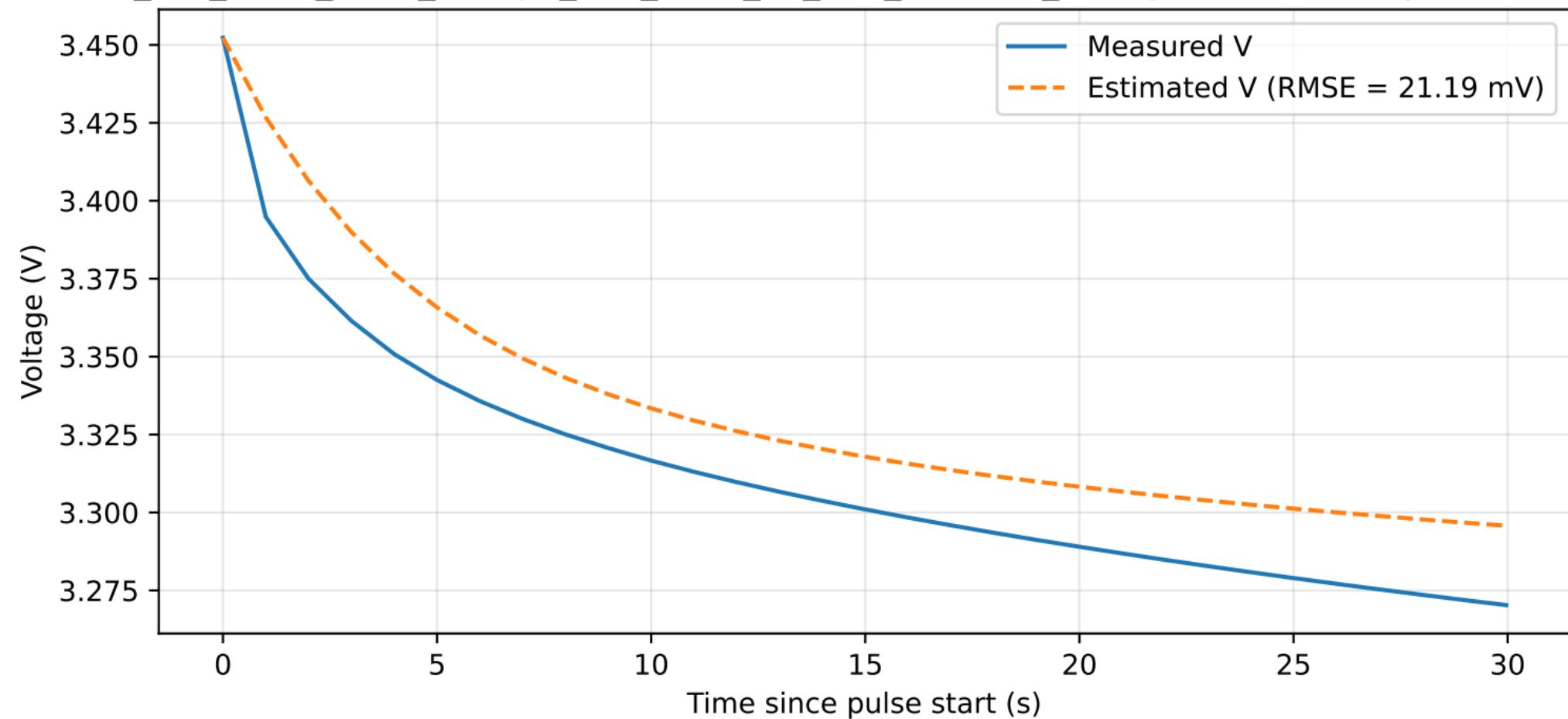
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



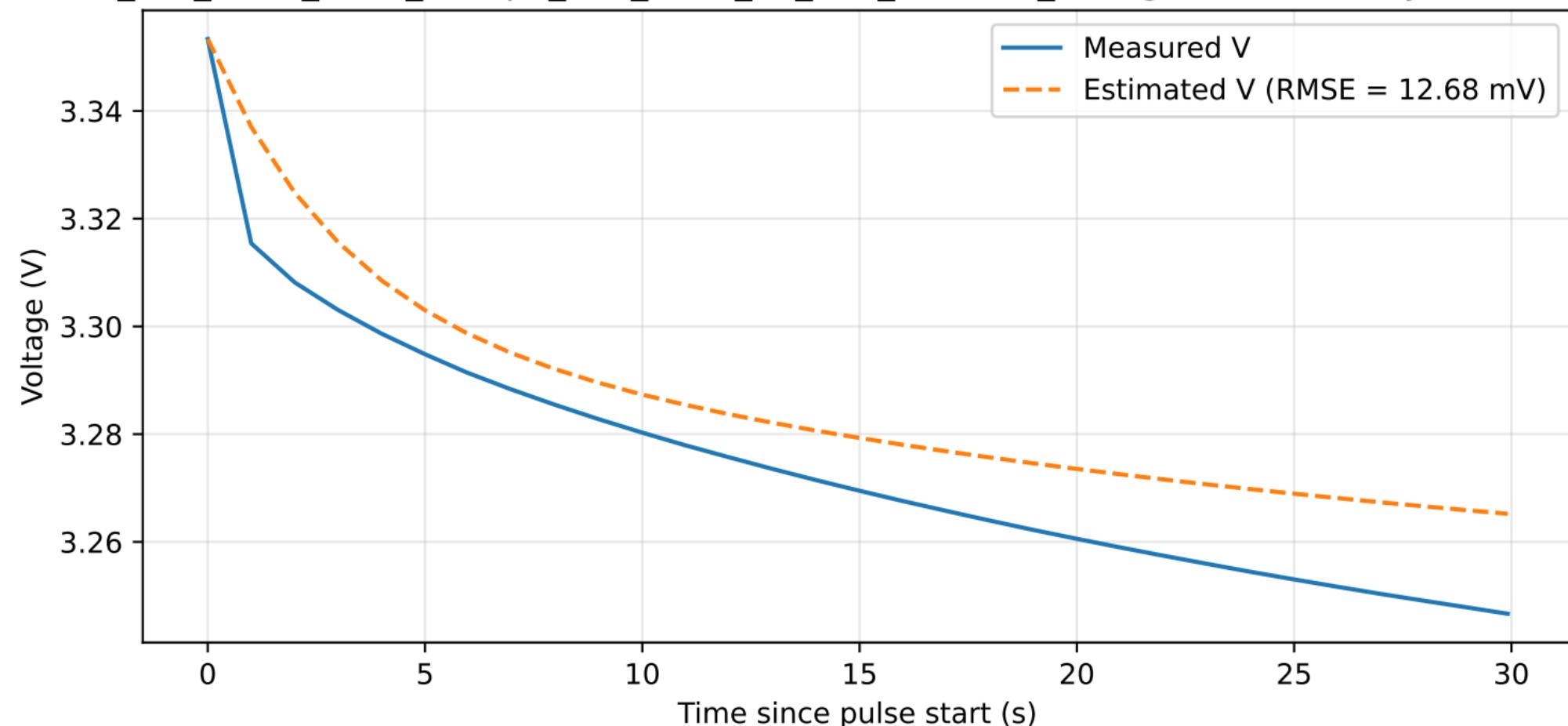
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0004\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



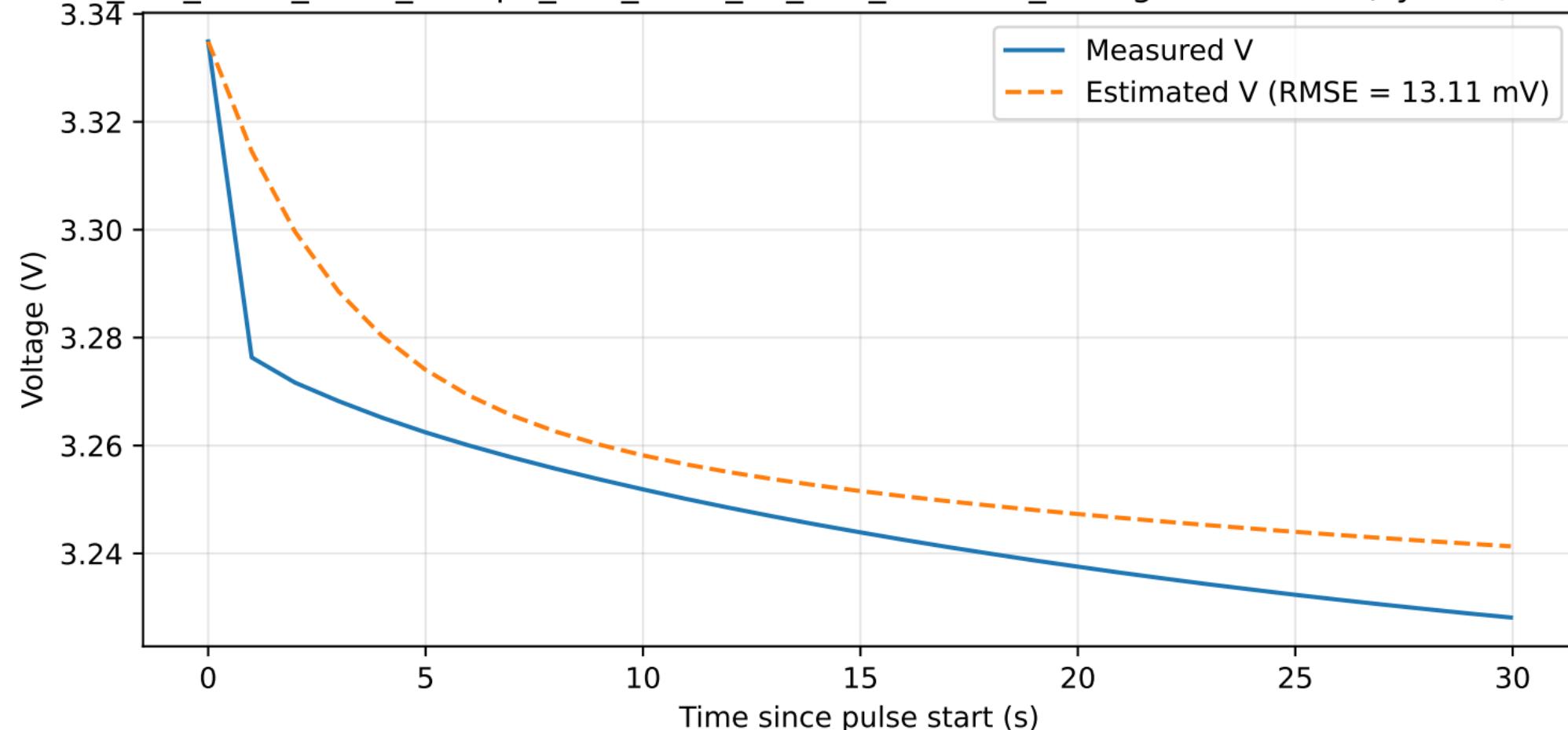
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



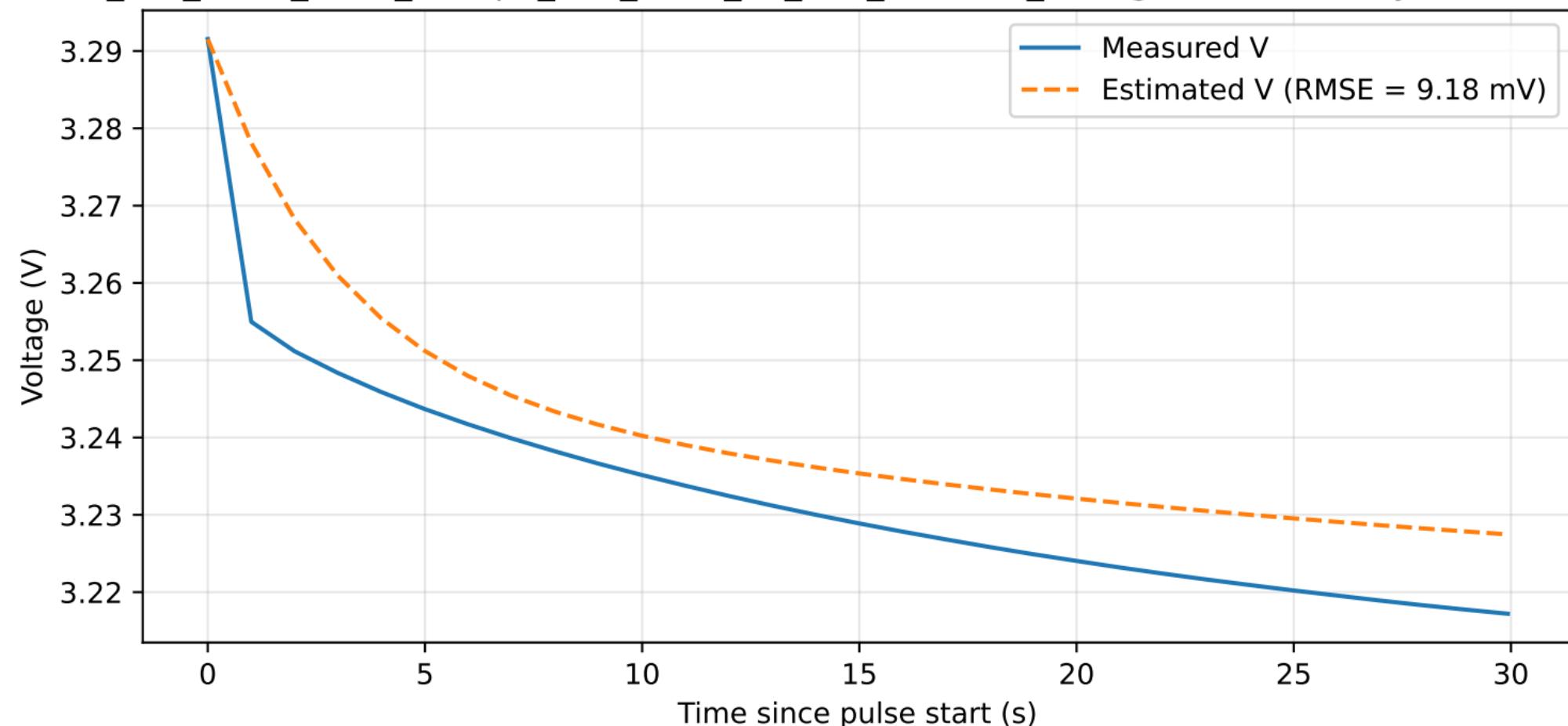
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



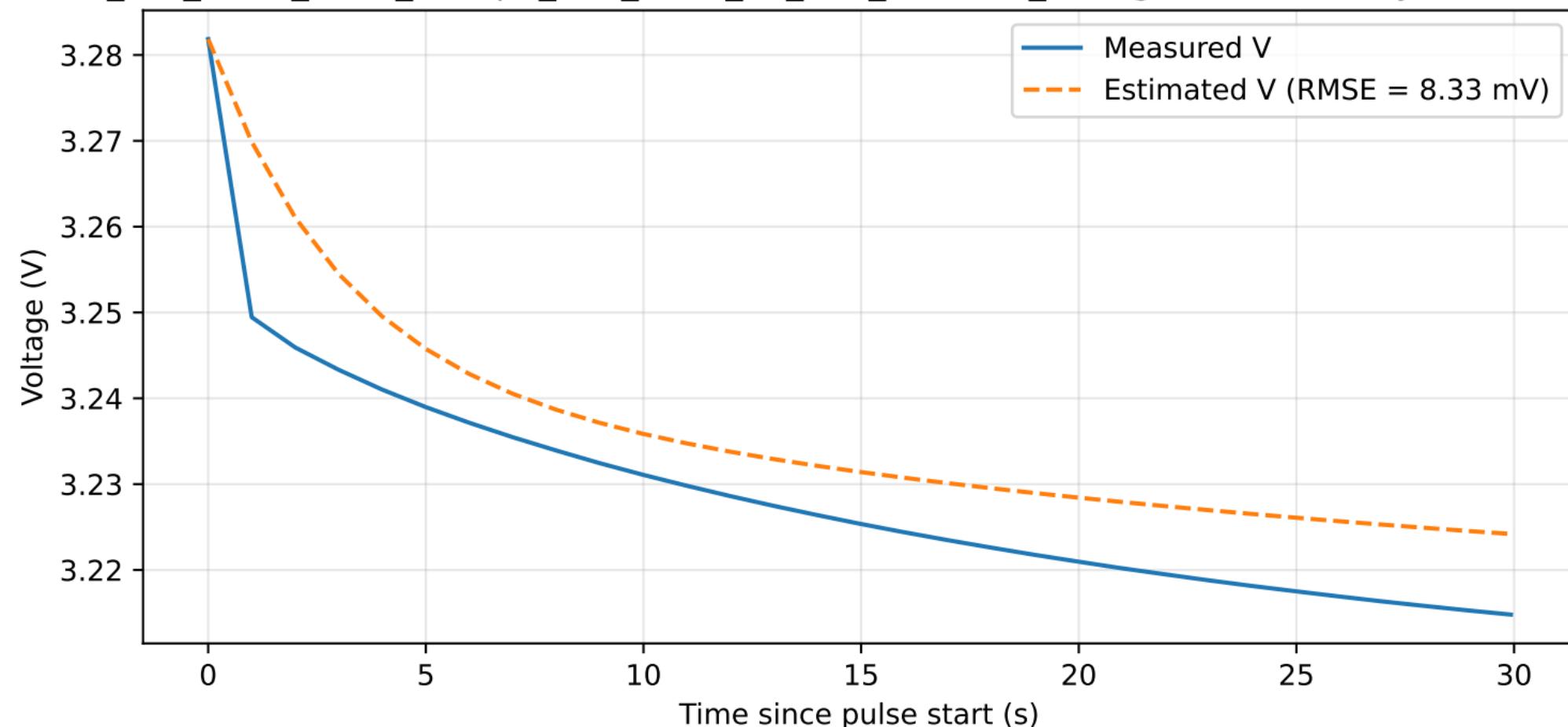
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



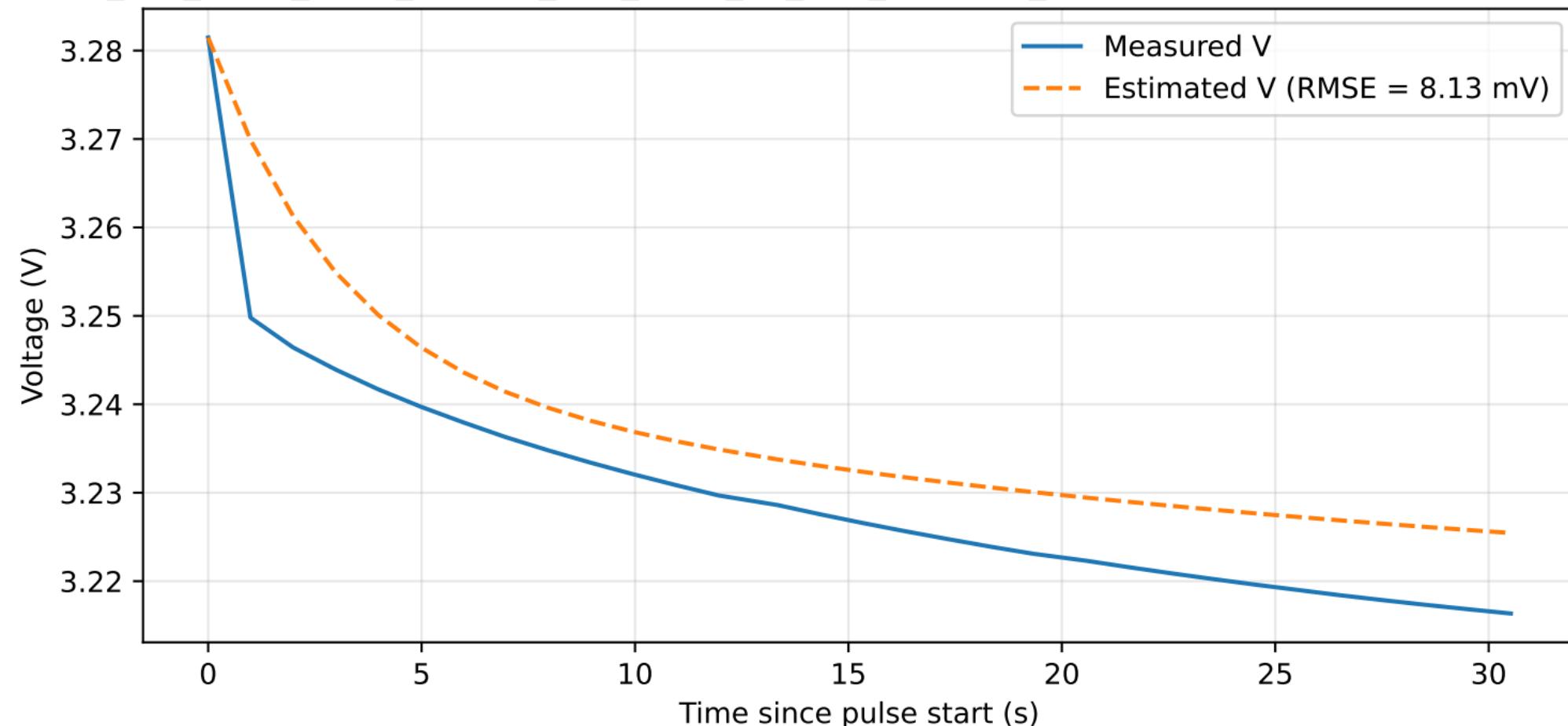
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



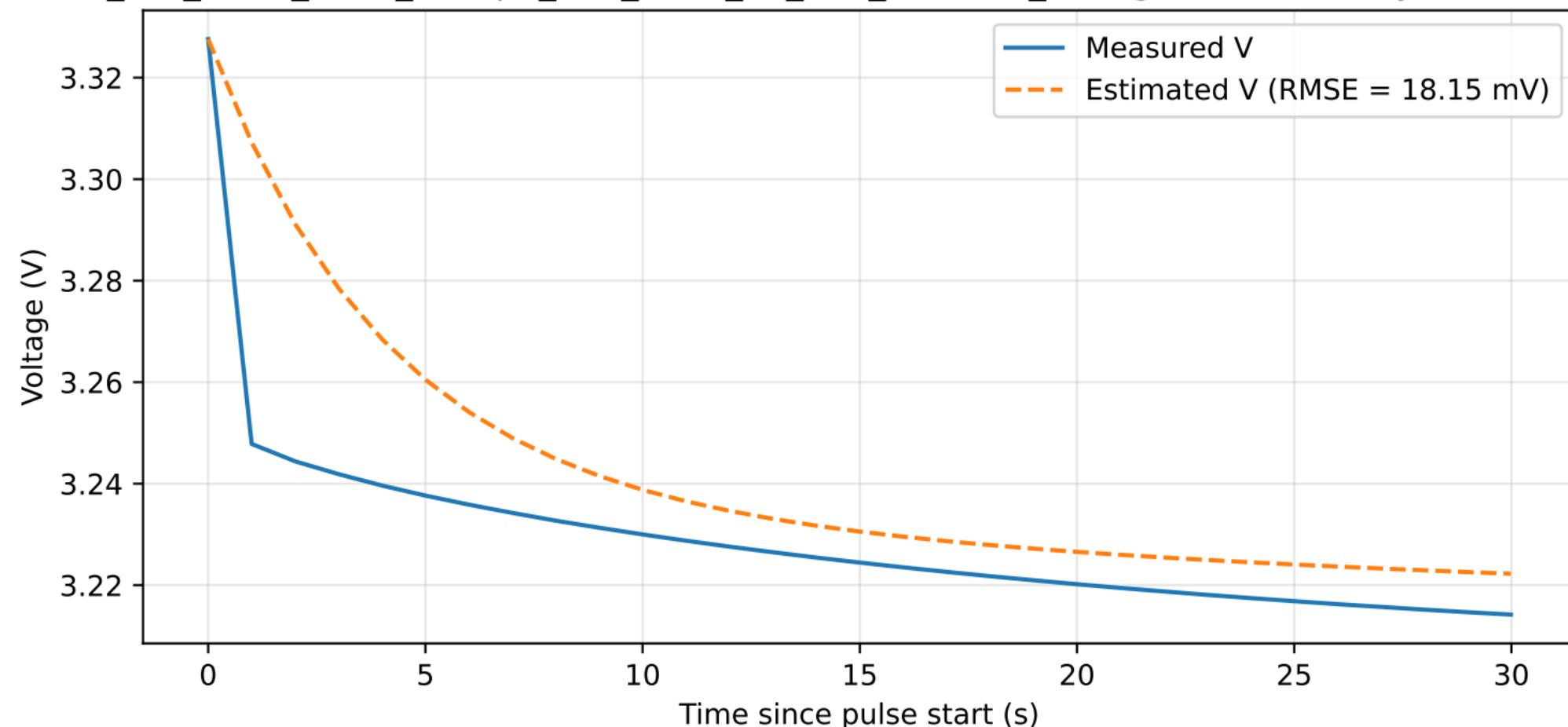
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



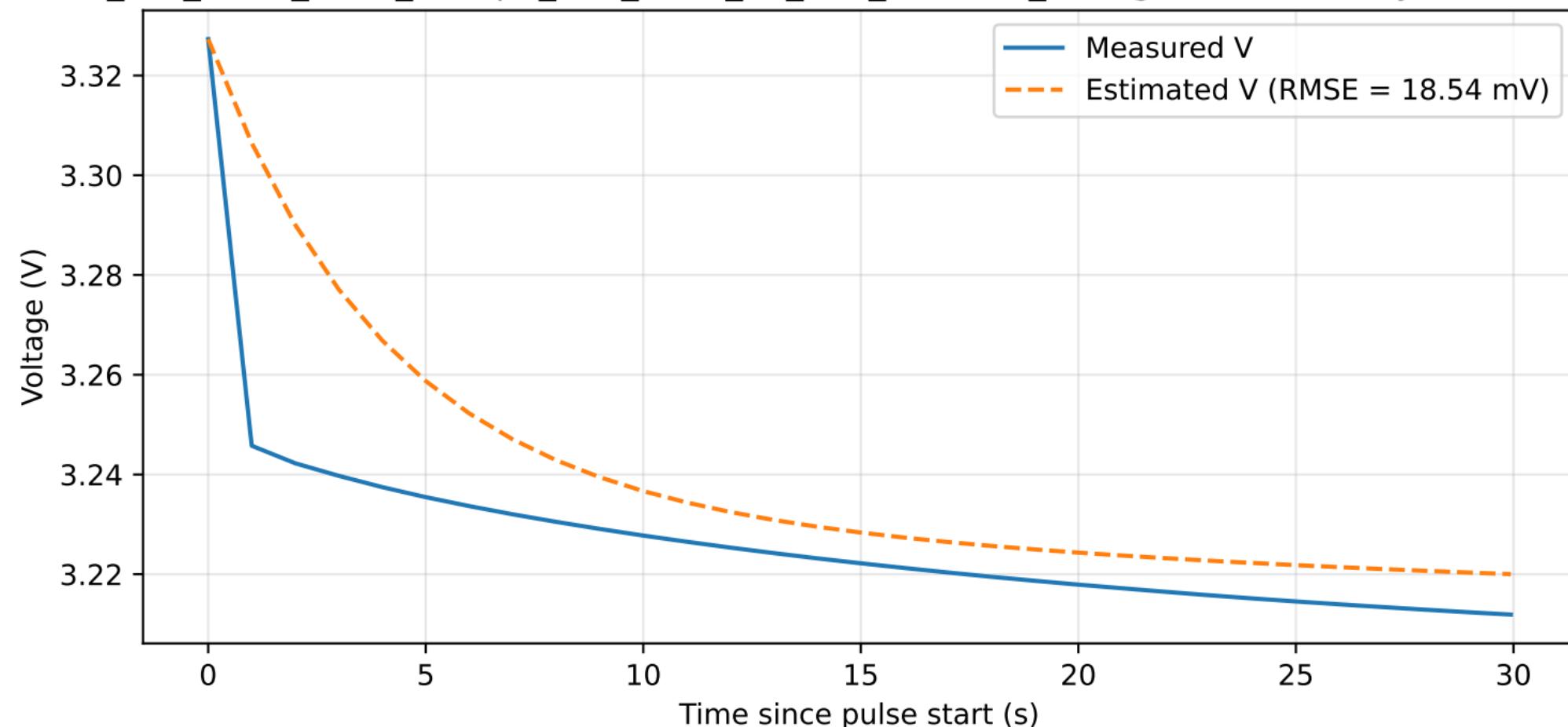
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



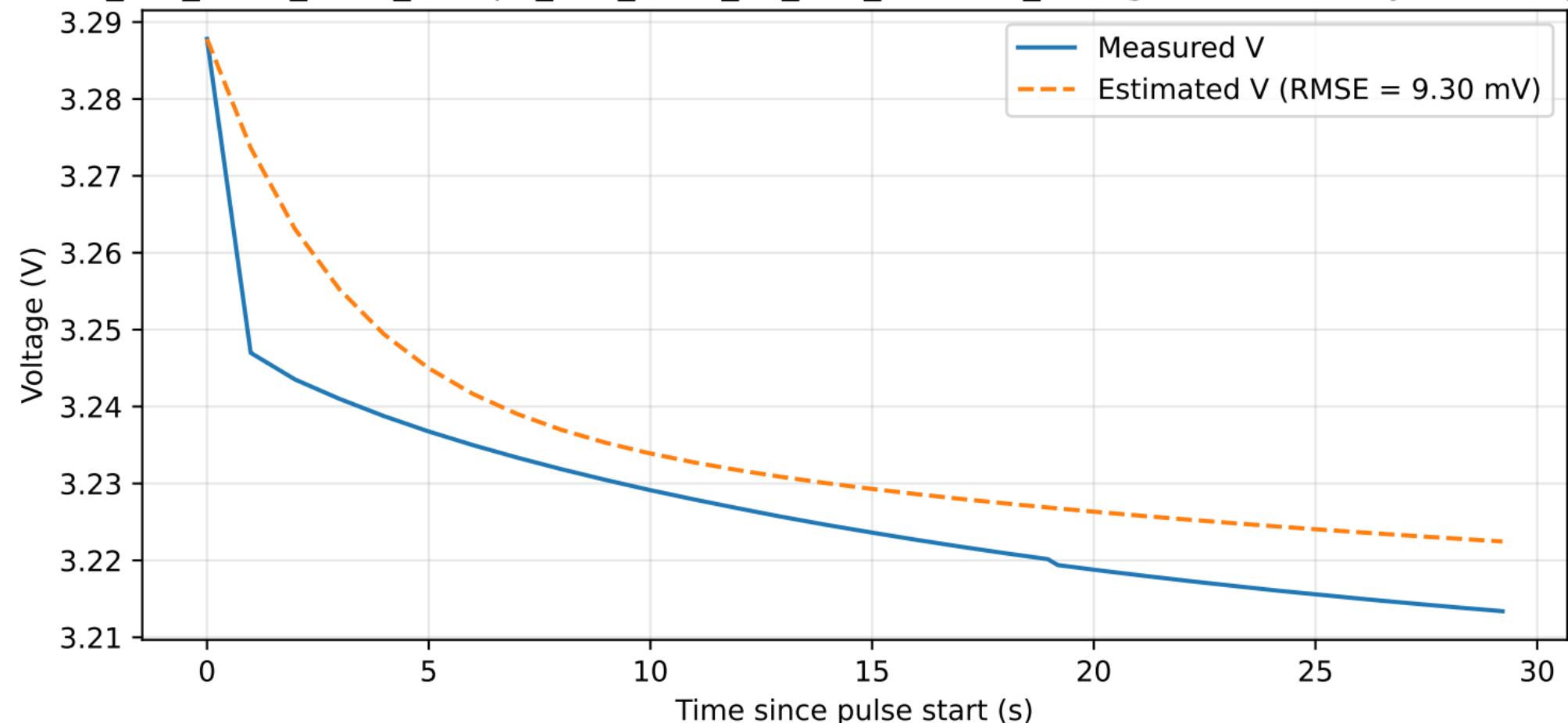
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



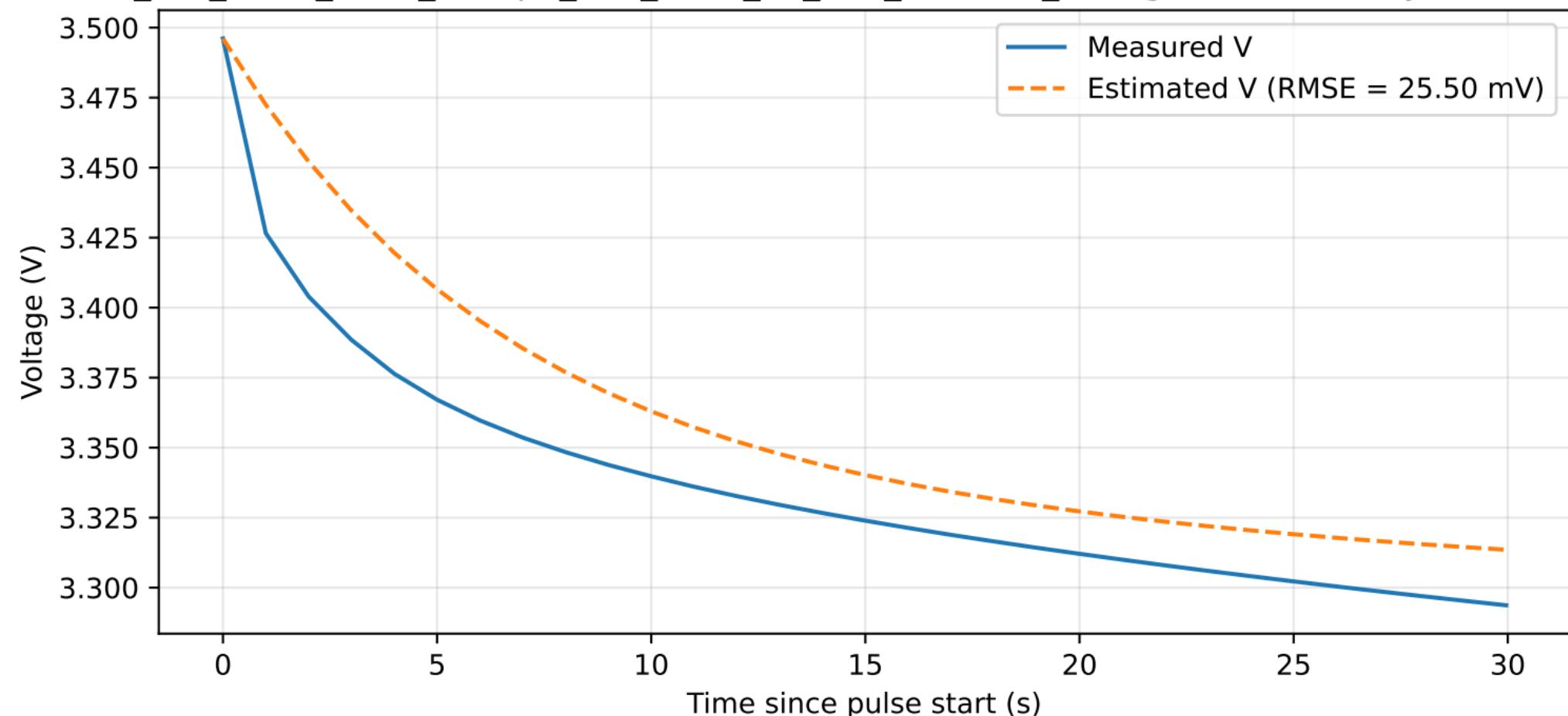
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



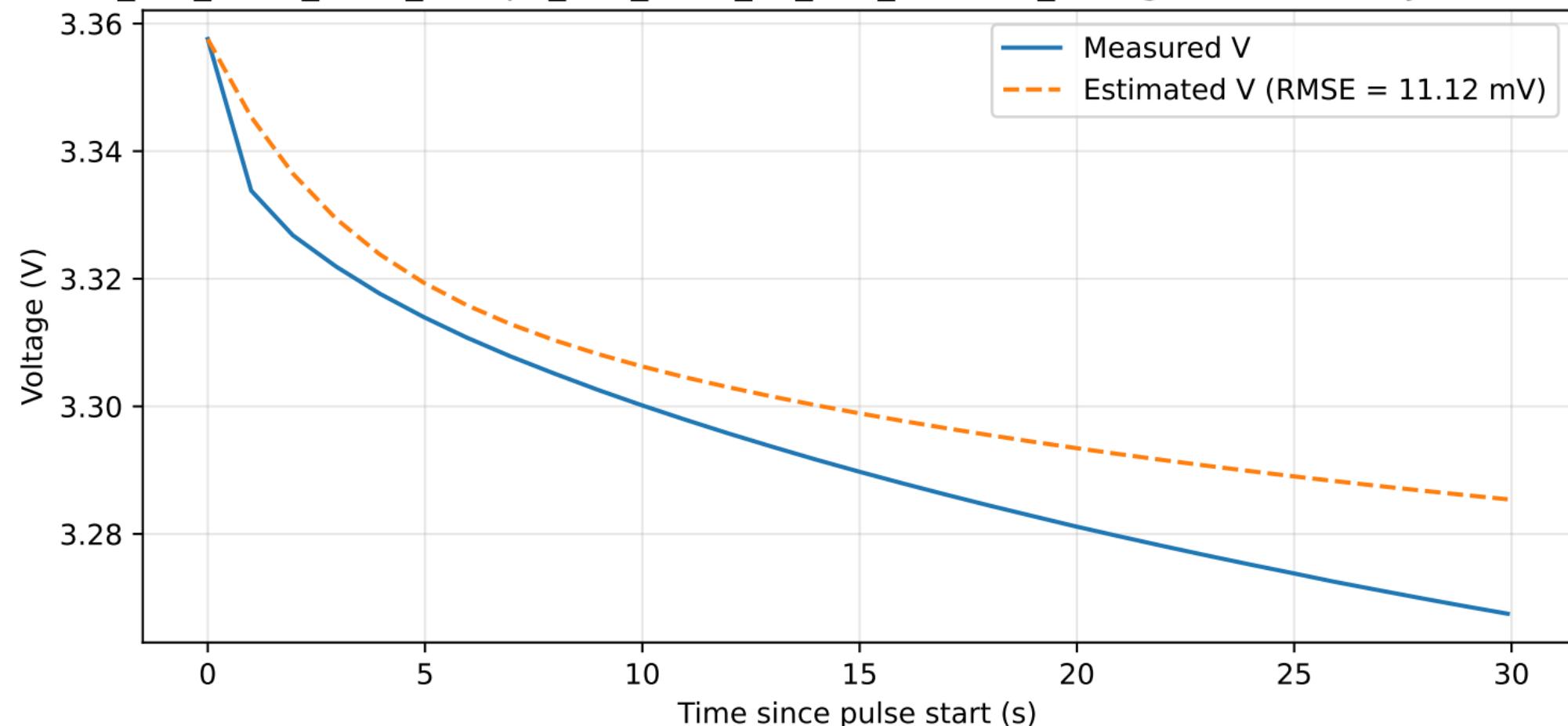
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0007\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



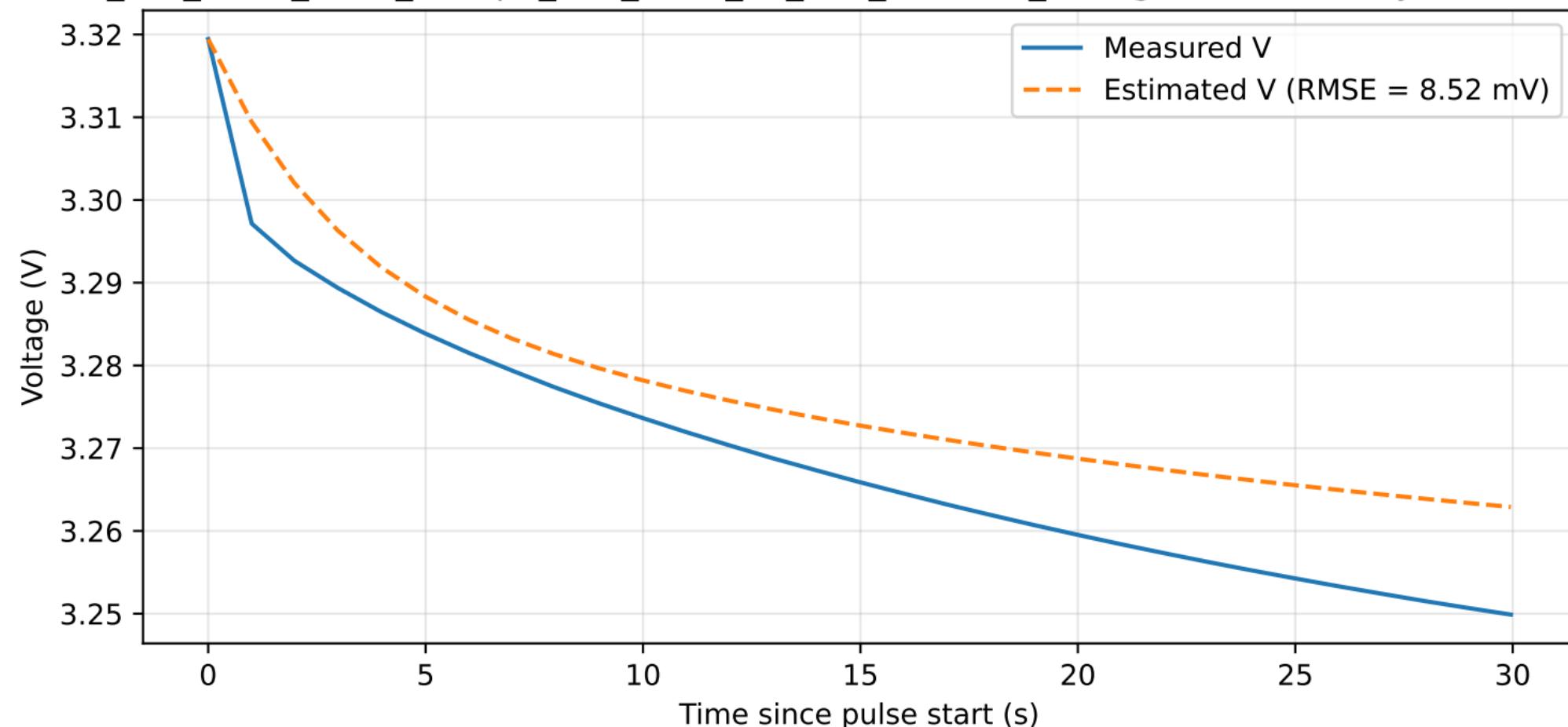
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



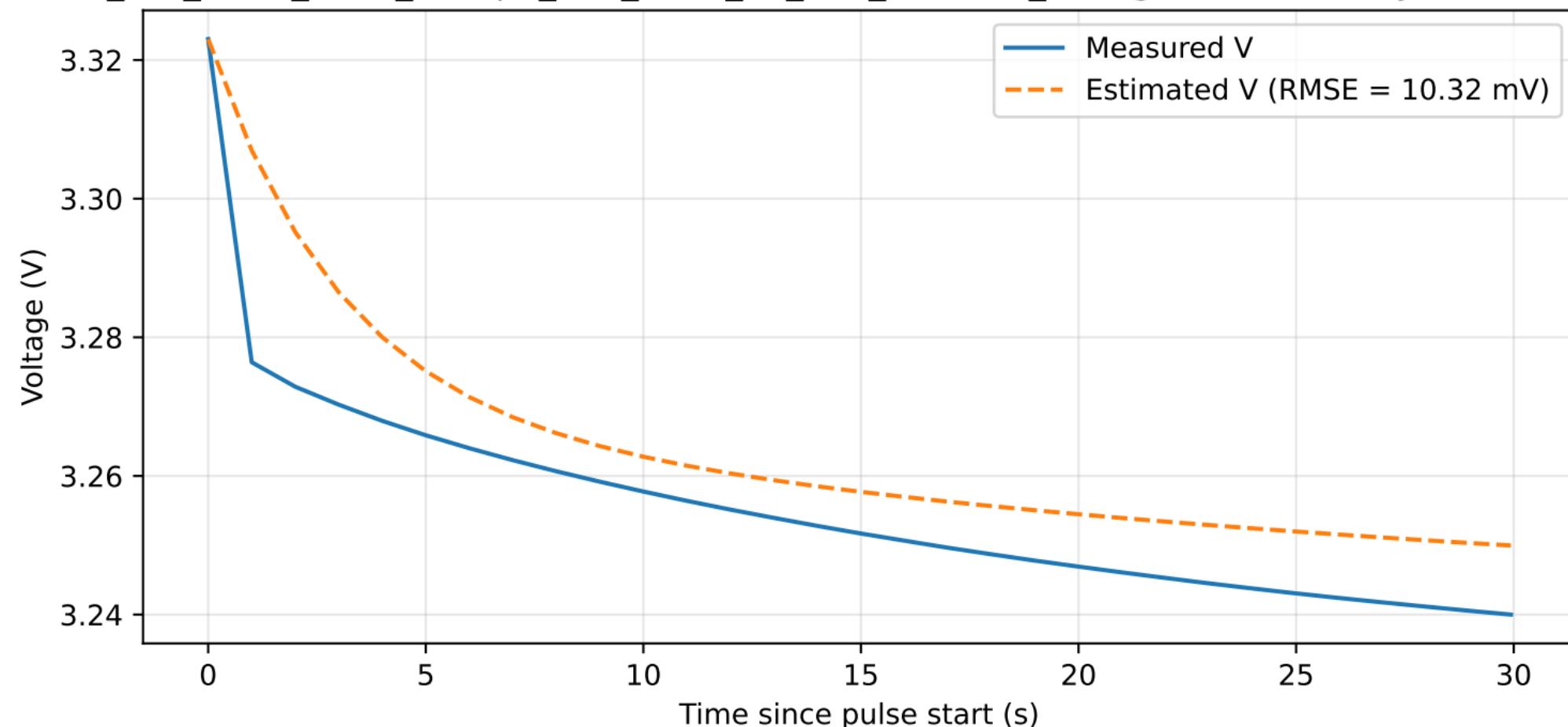
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



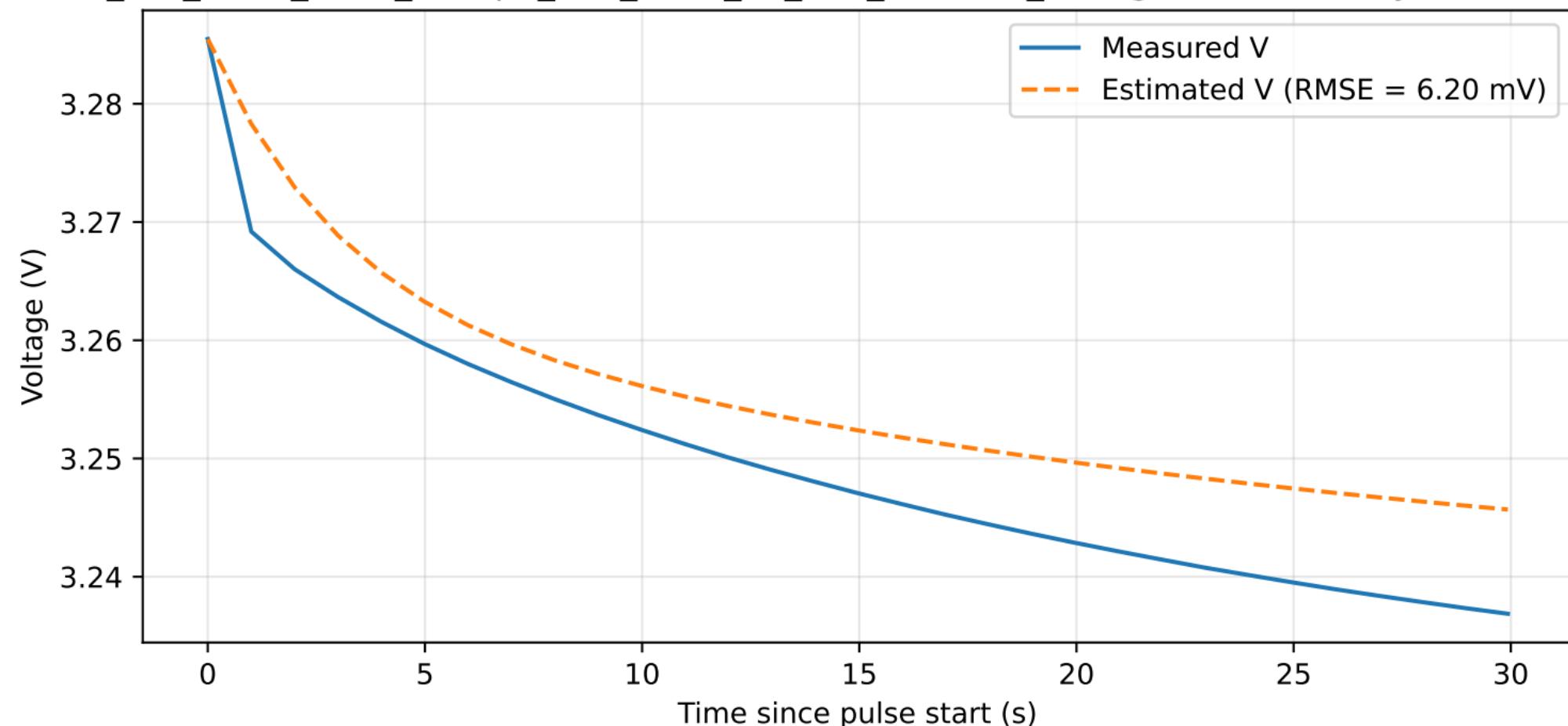
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



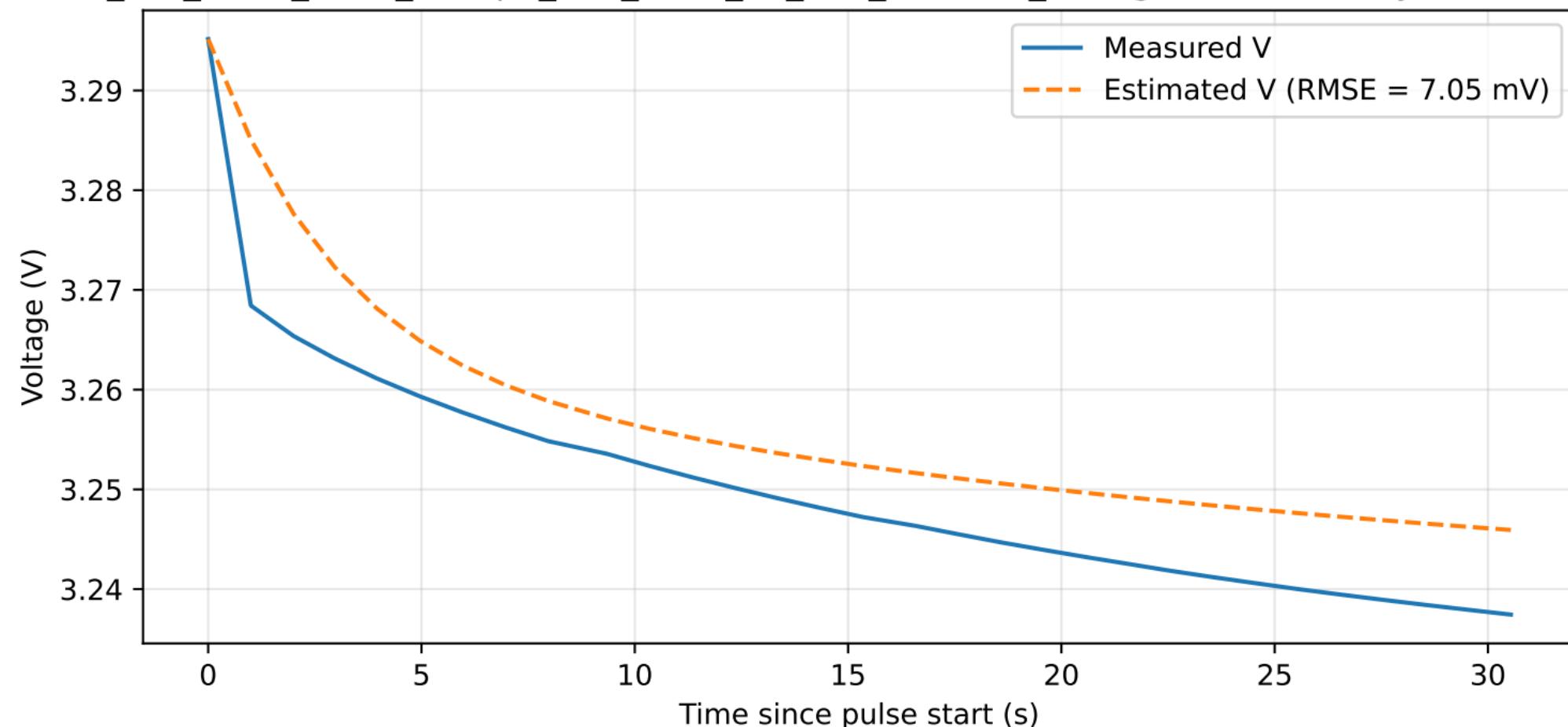
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



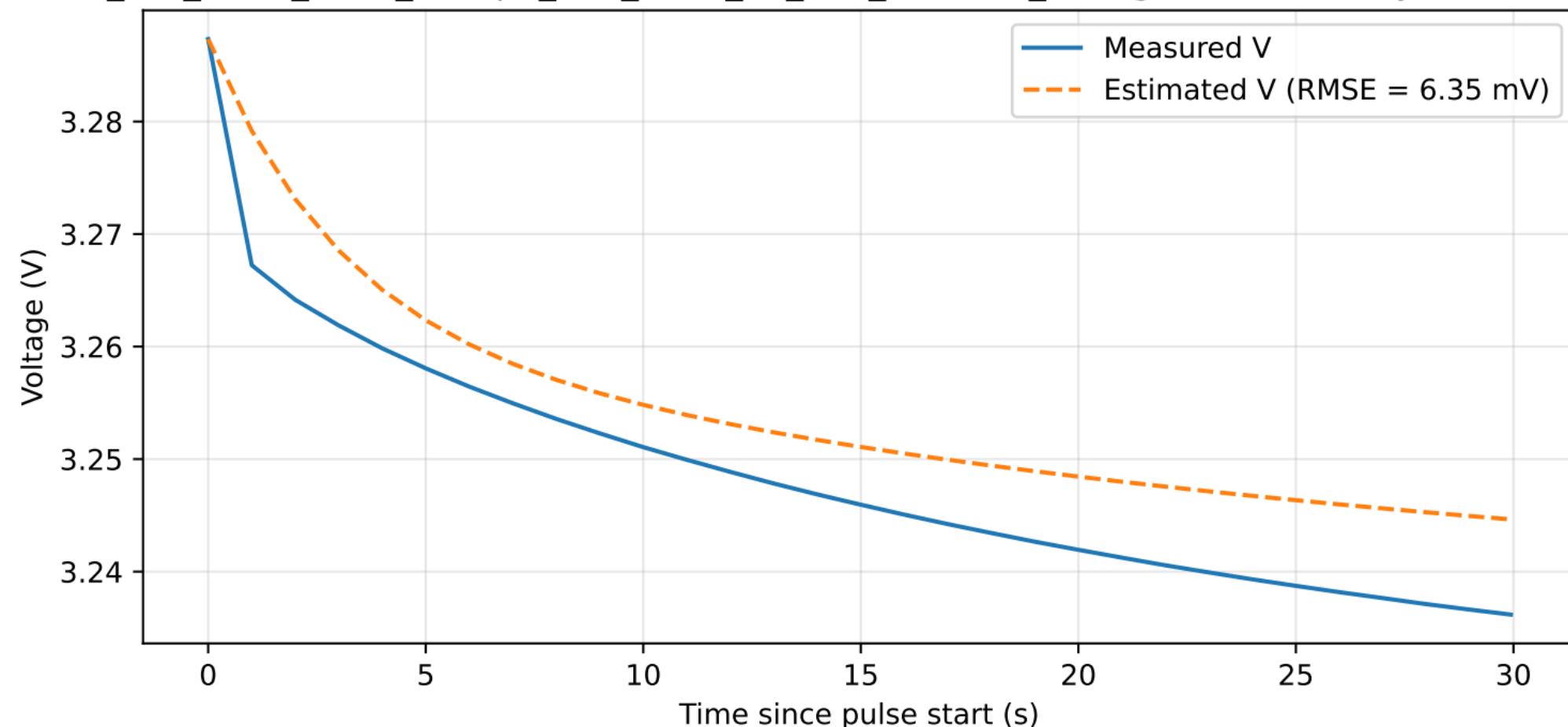
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



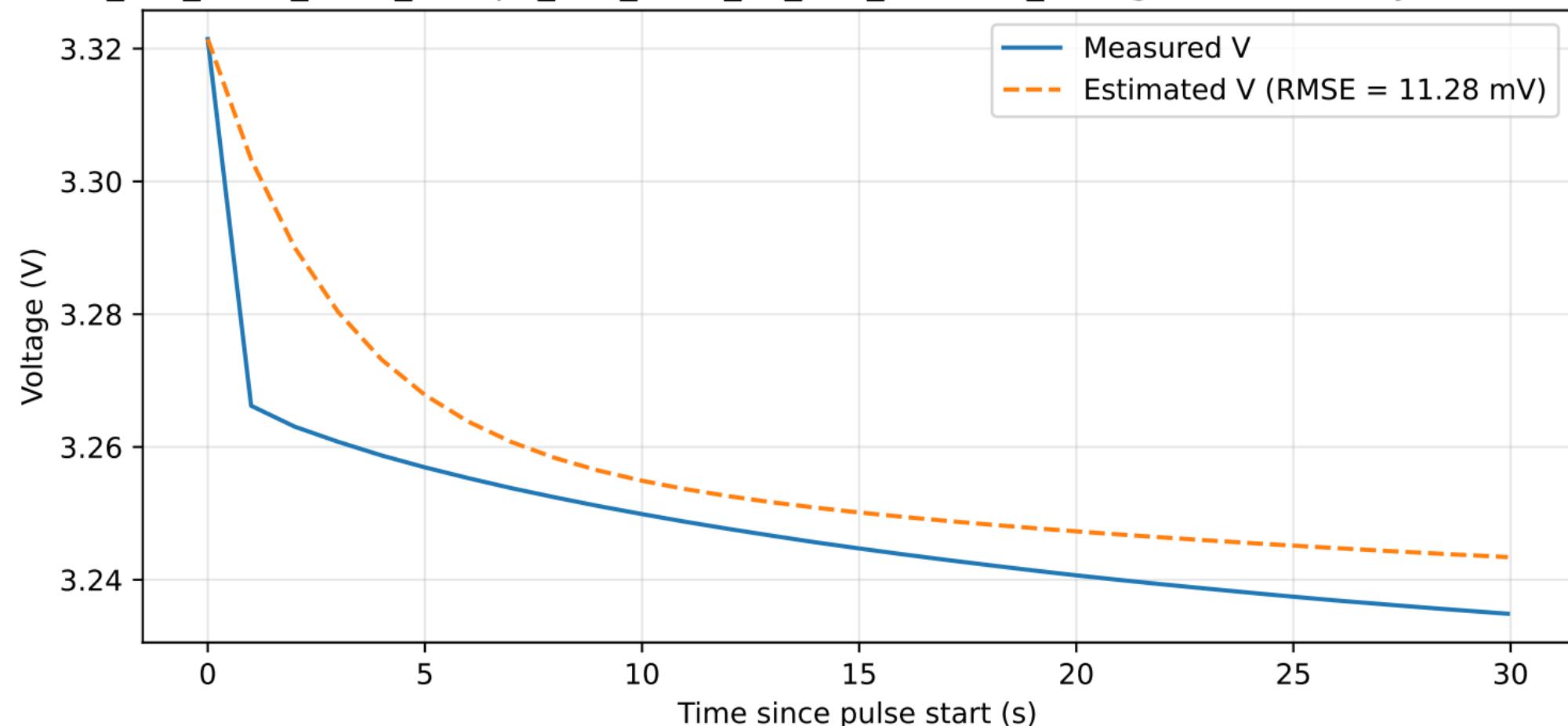
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



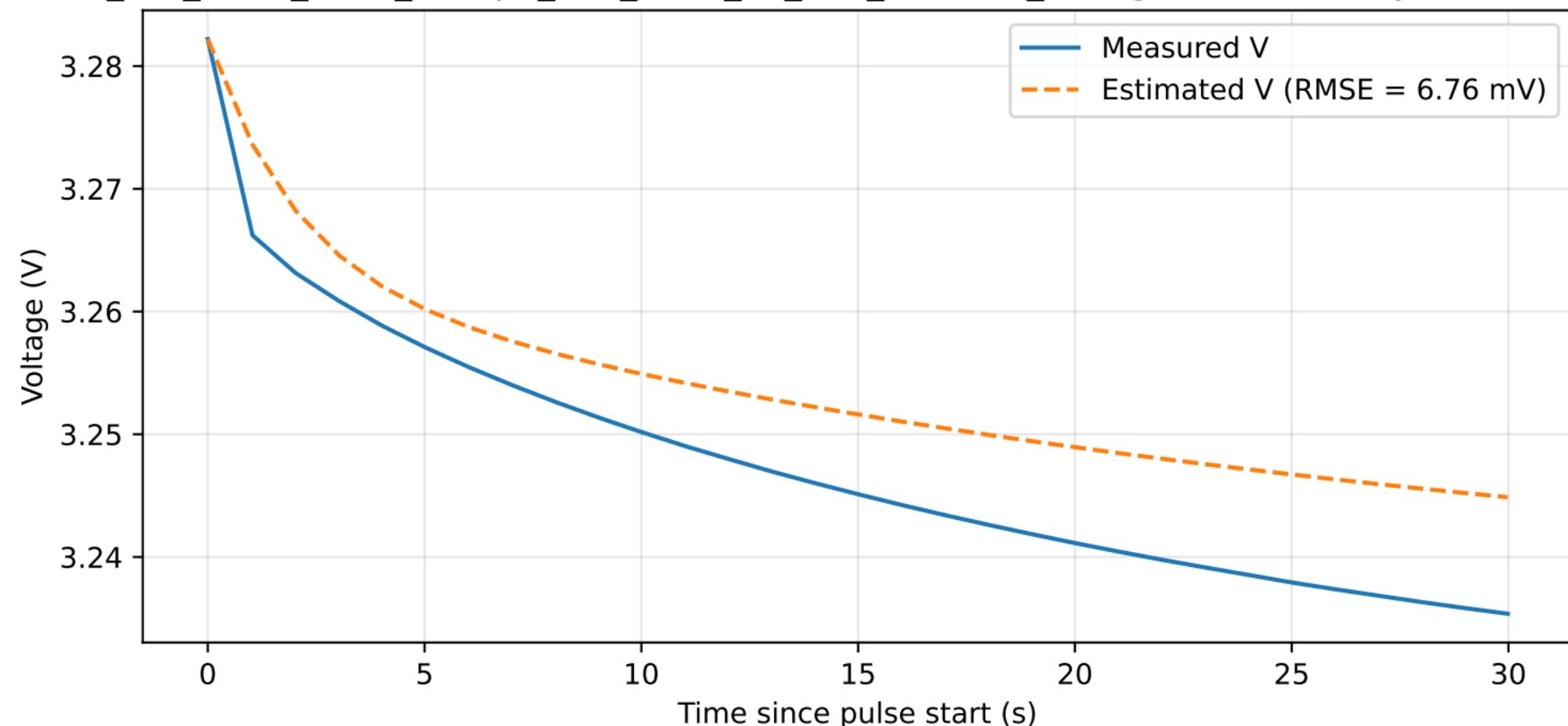
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



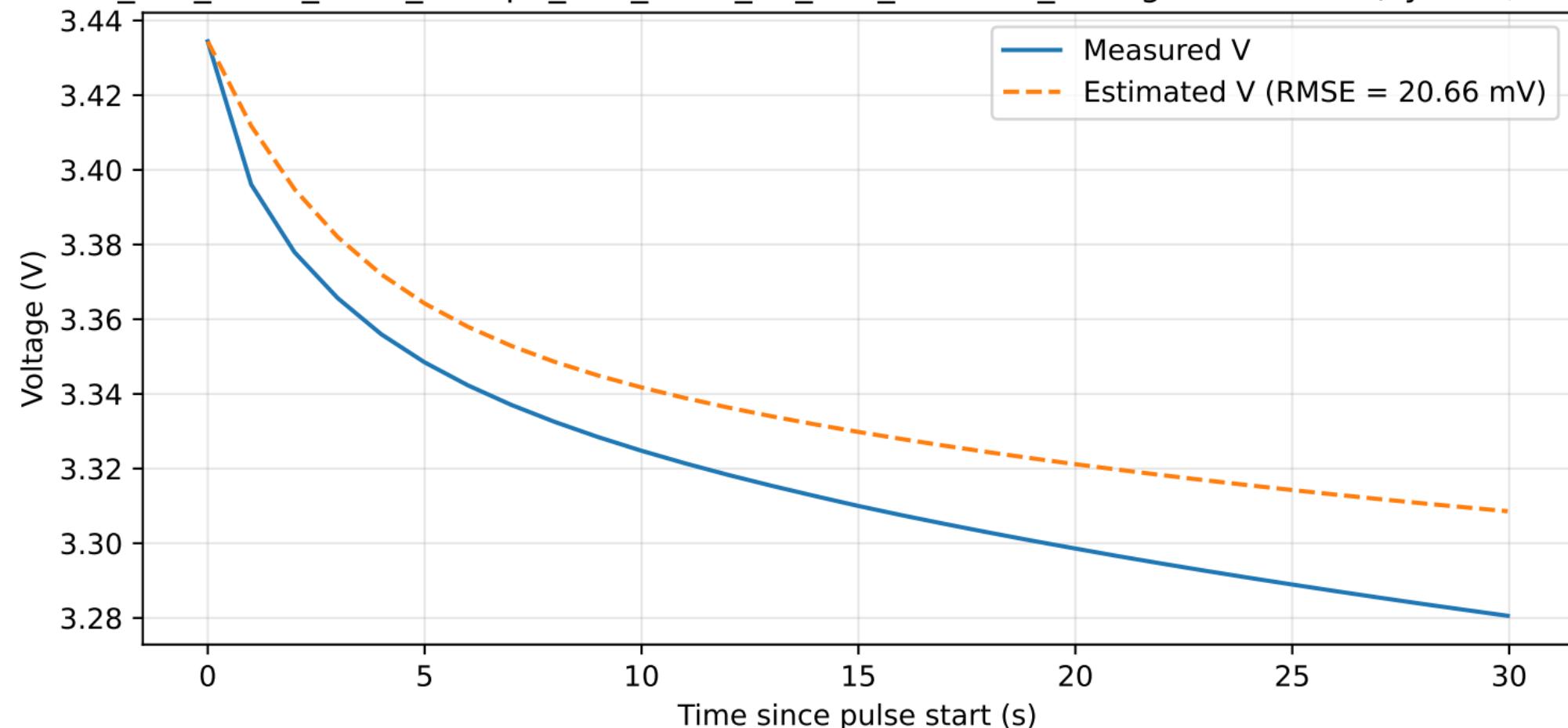
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



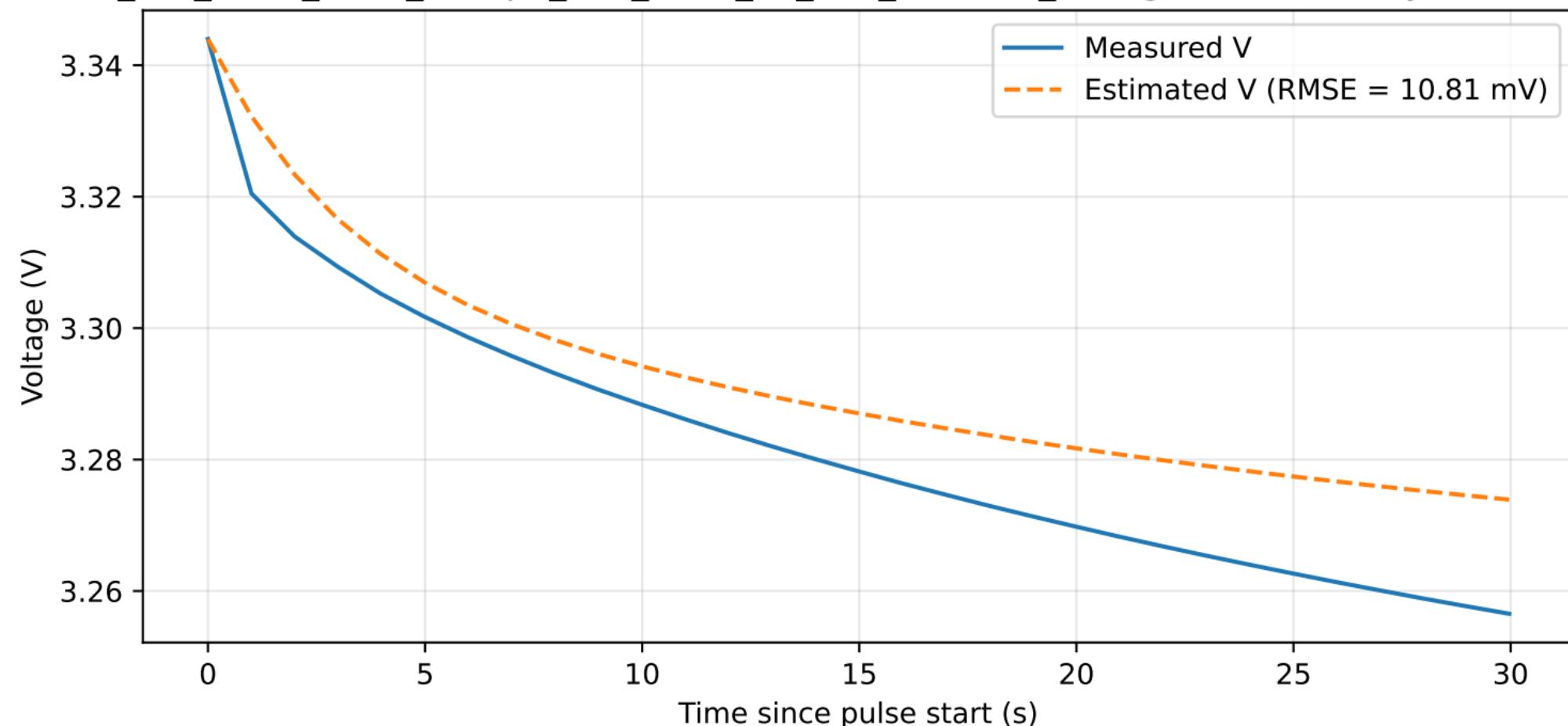
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0011\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



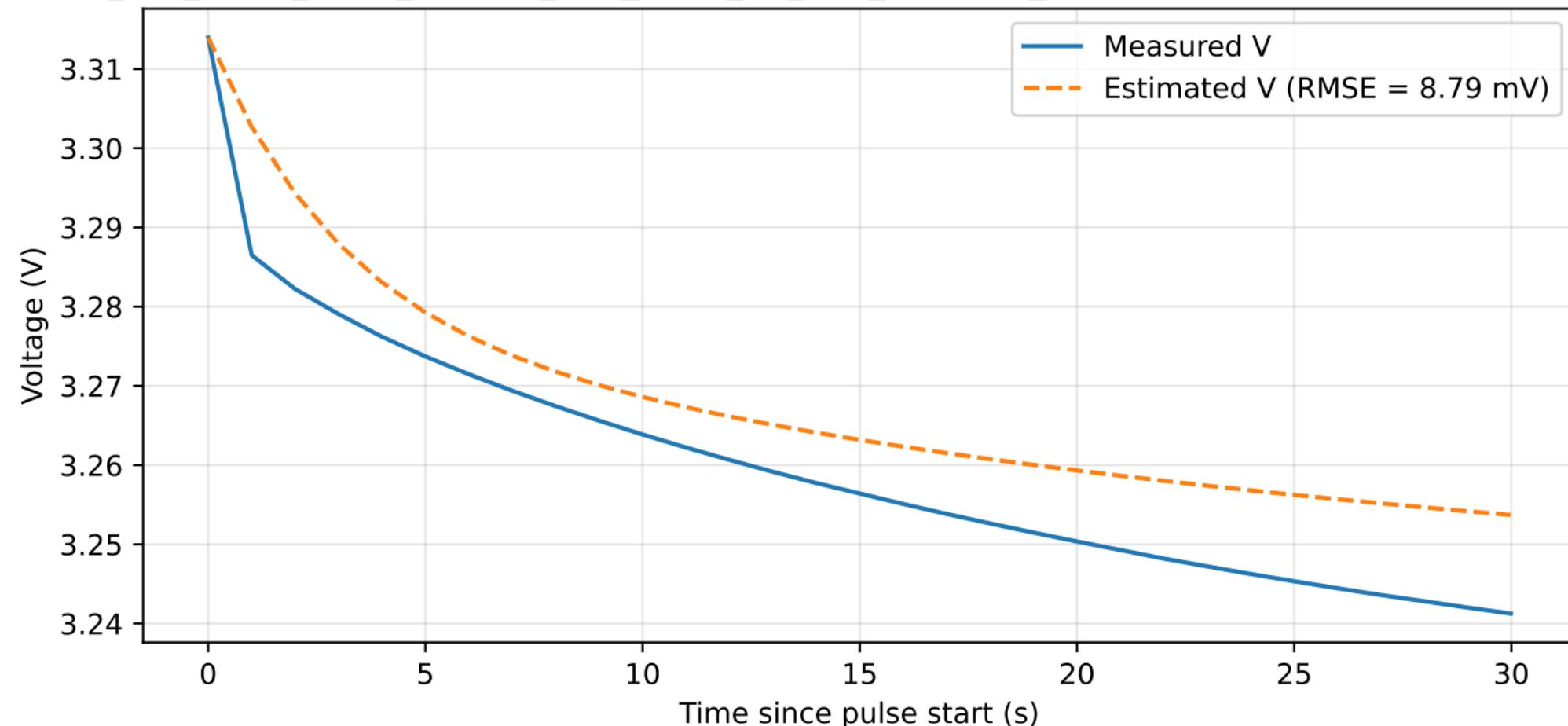
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



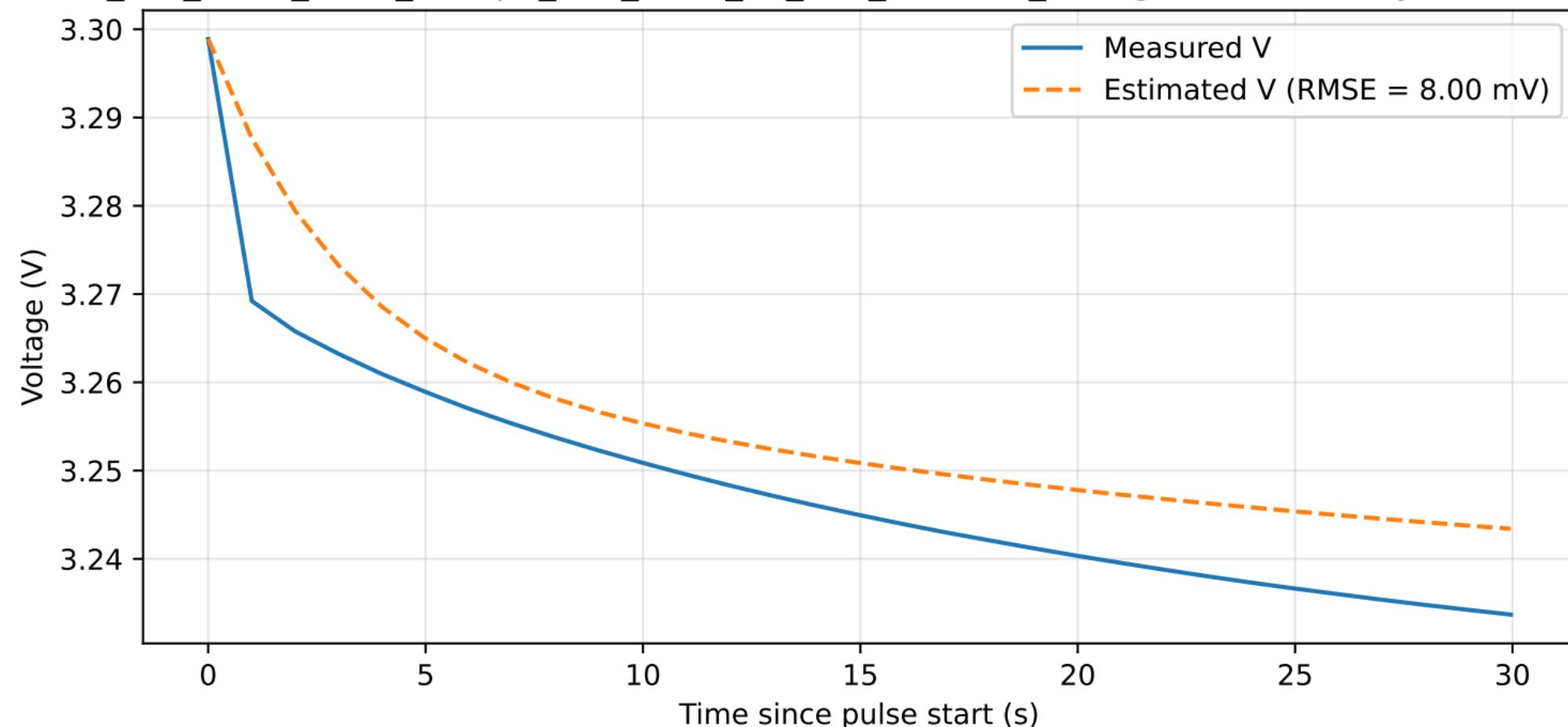
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



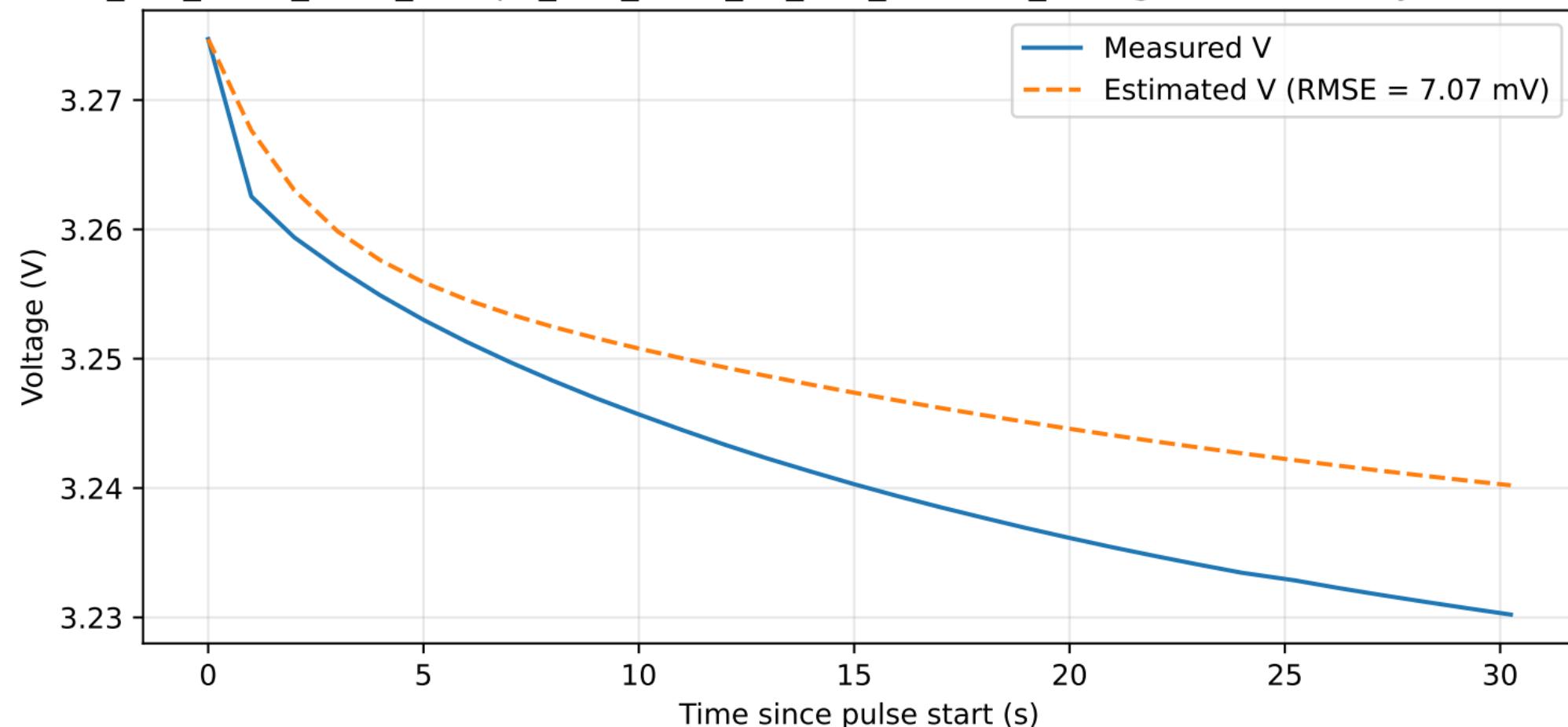
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



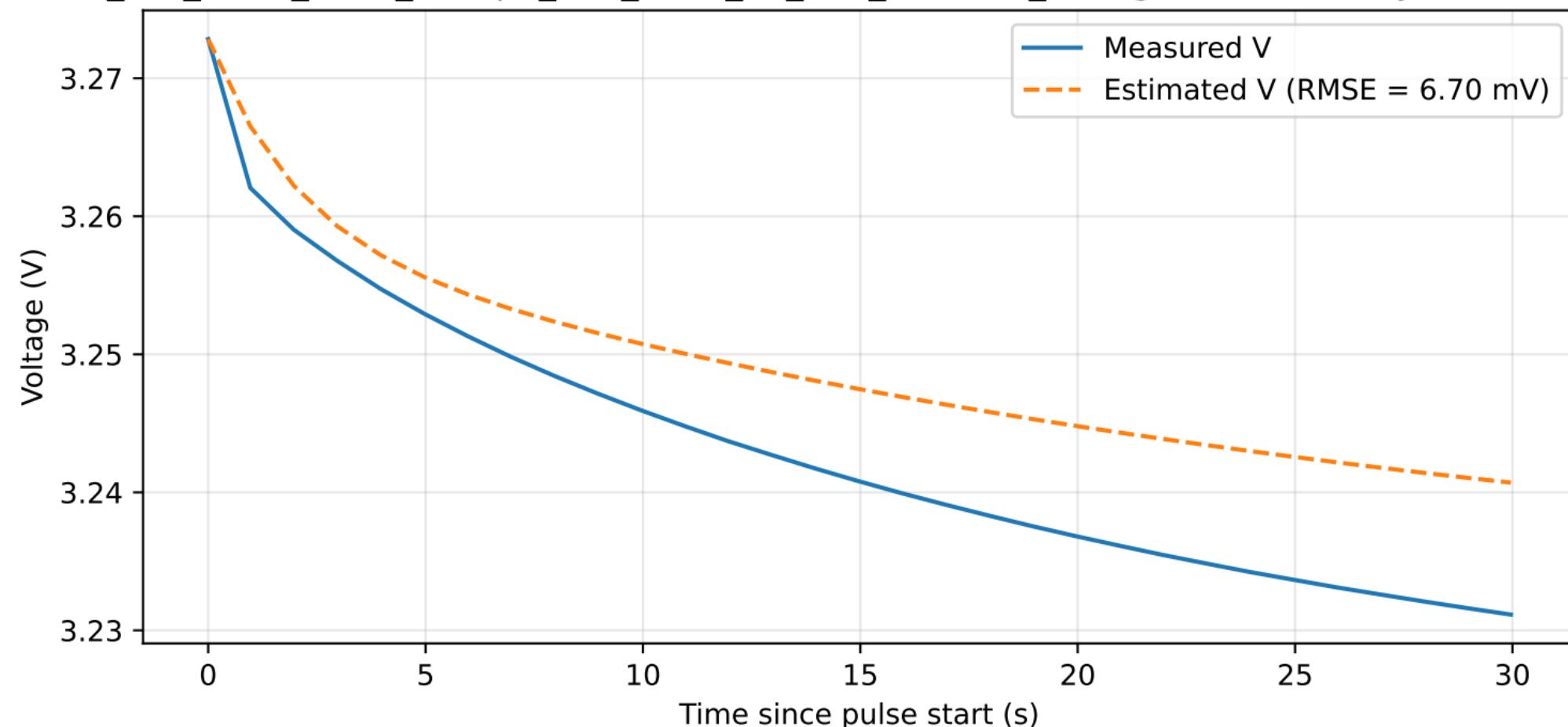
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



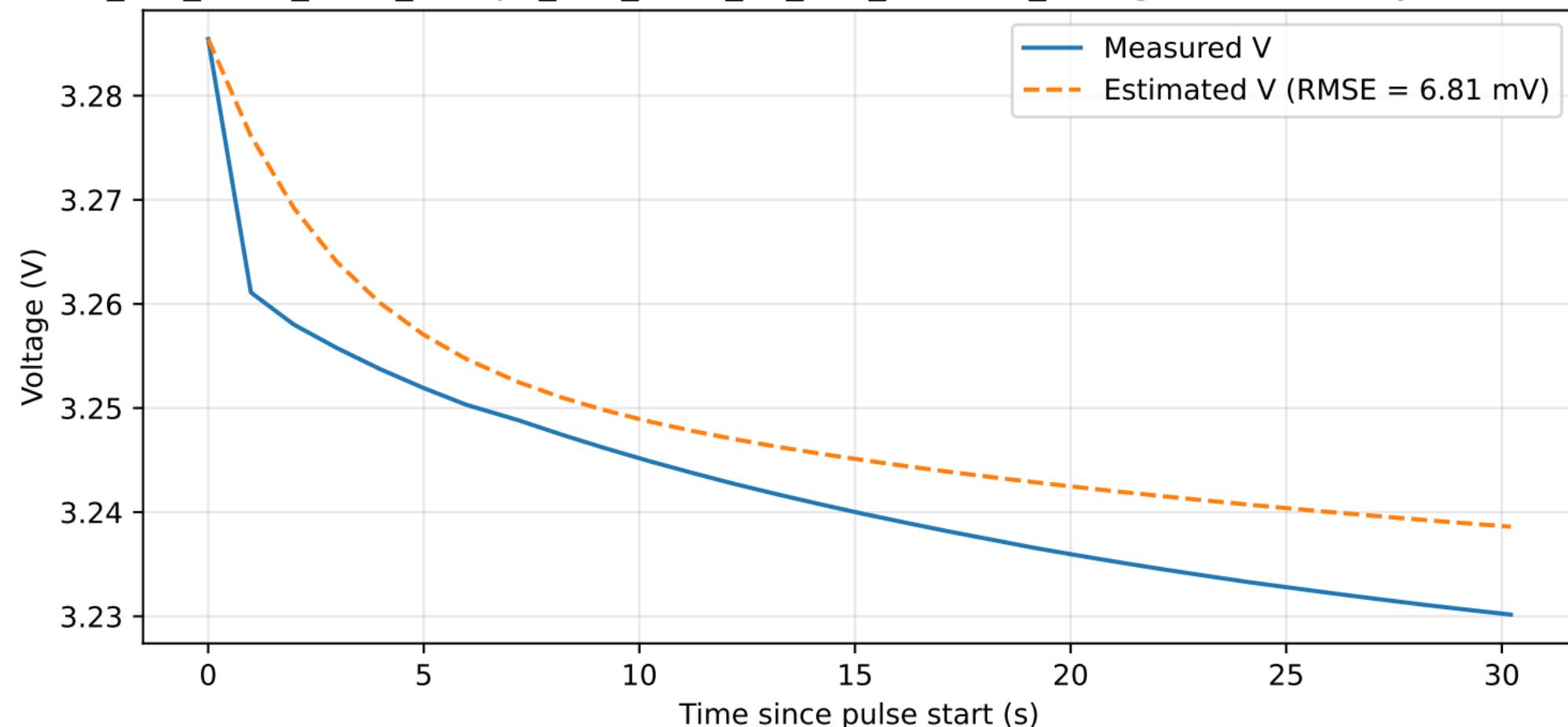
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



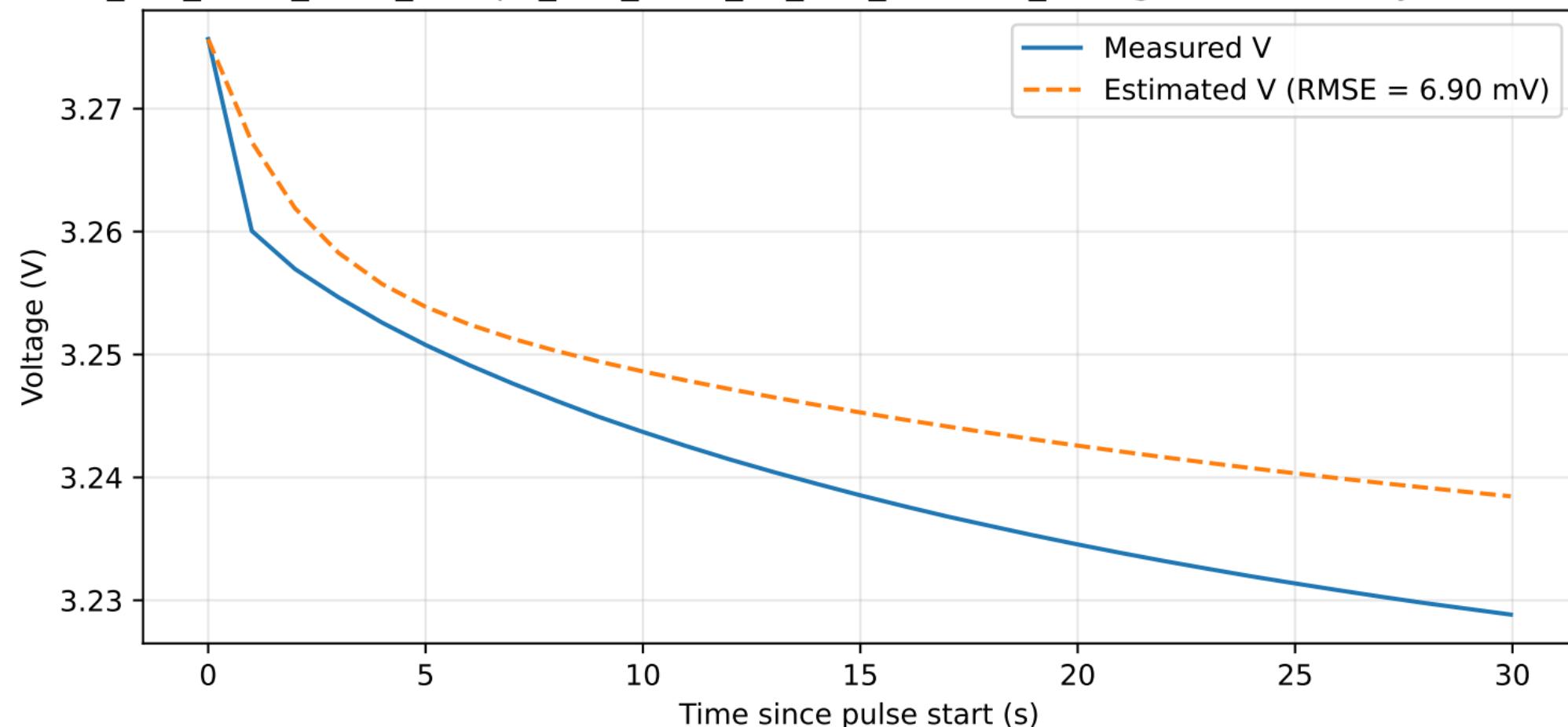
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



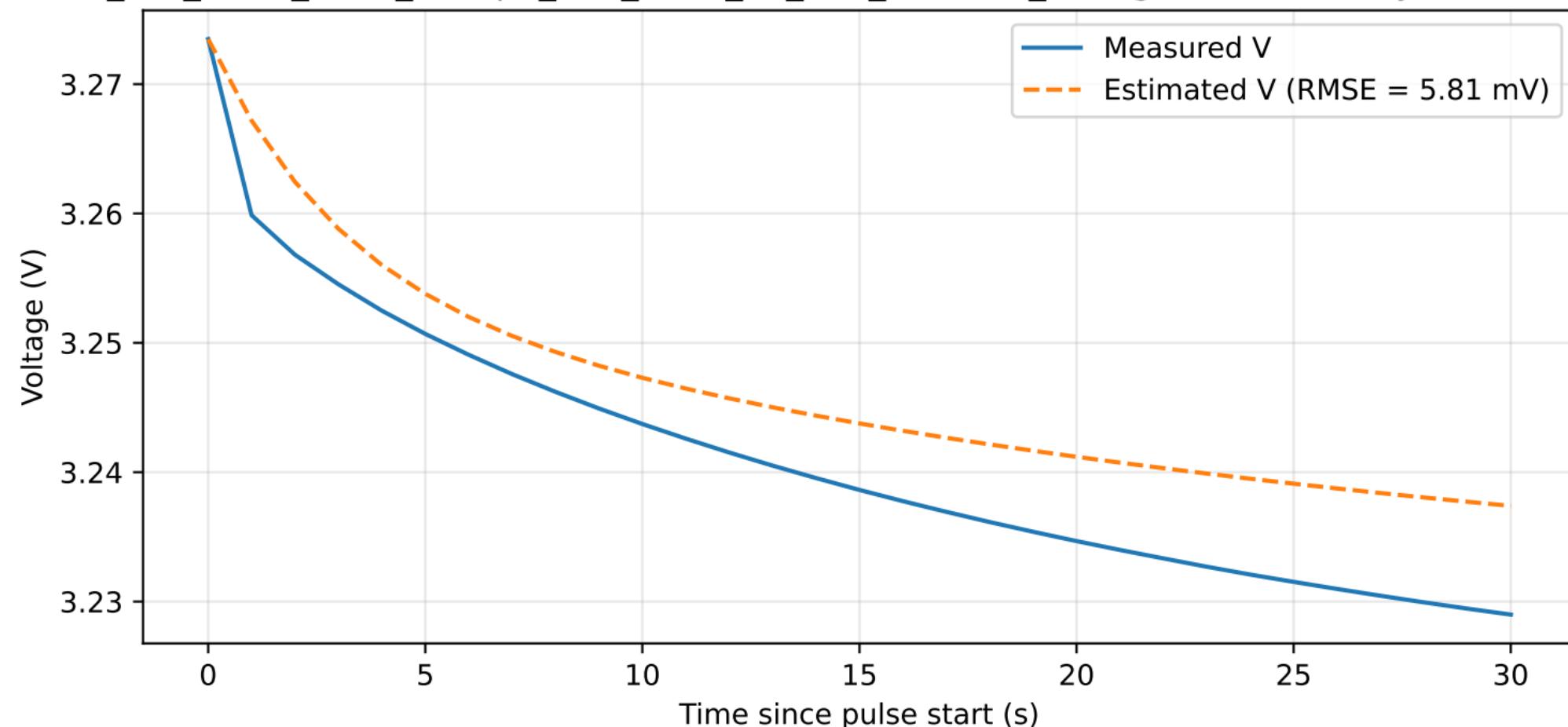
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



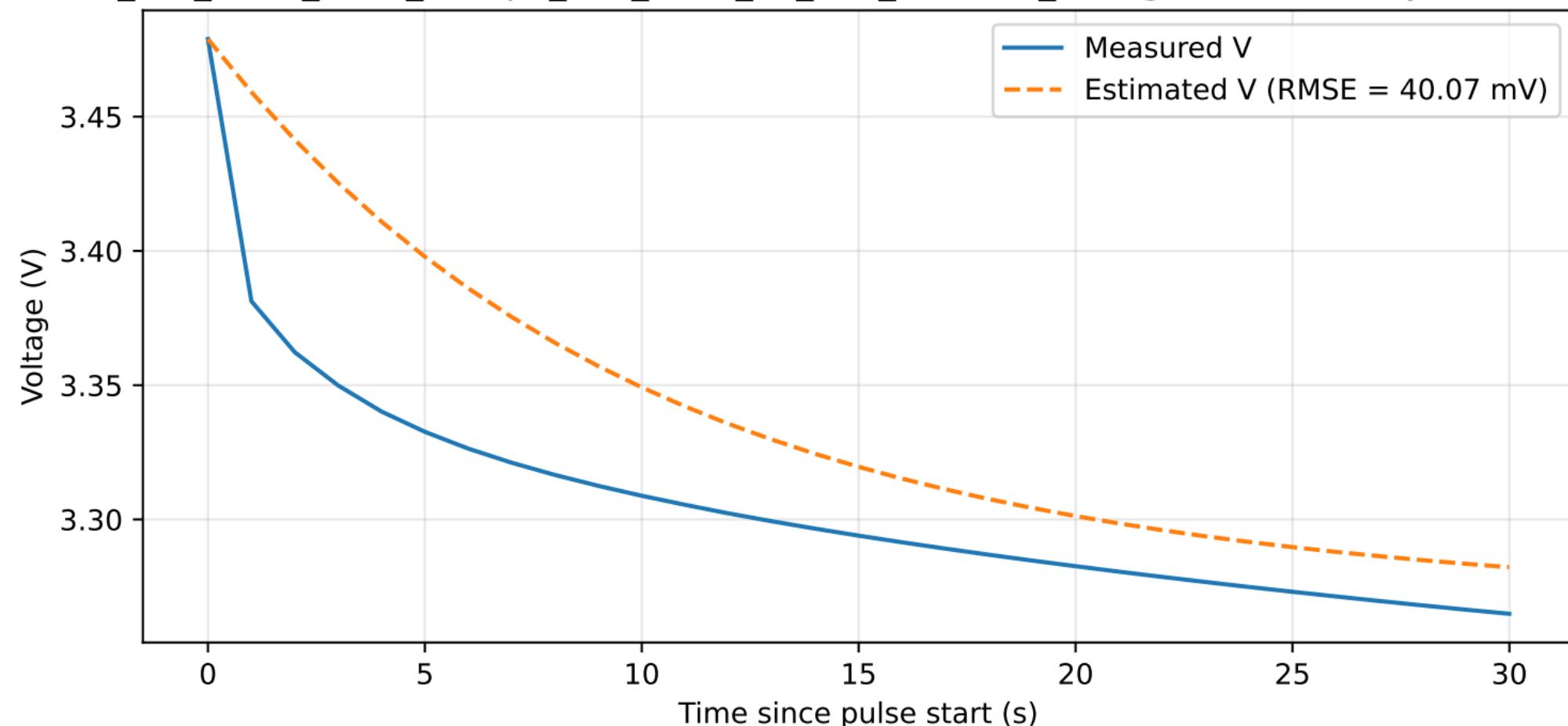
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



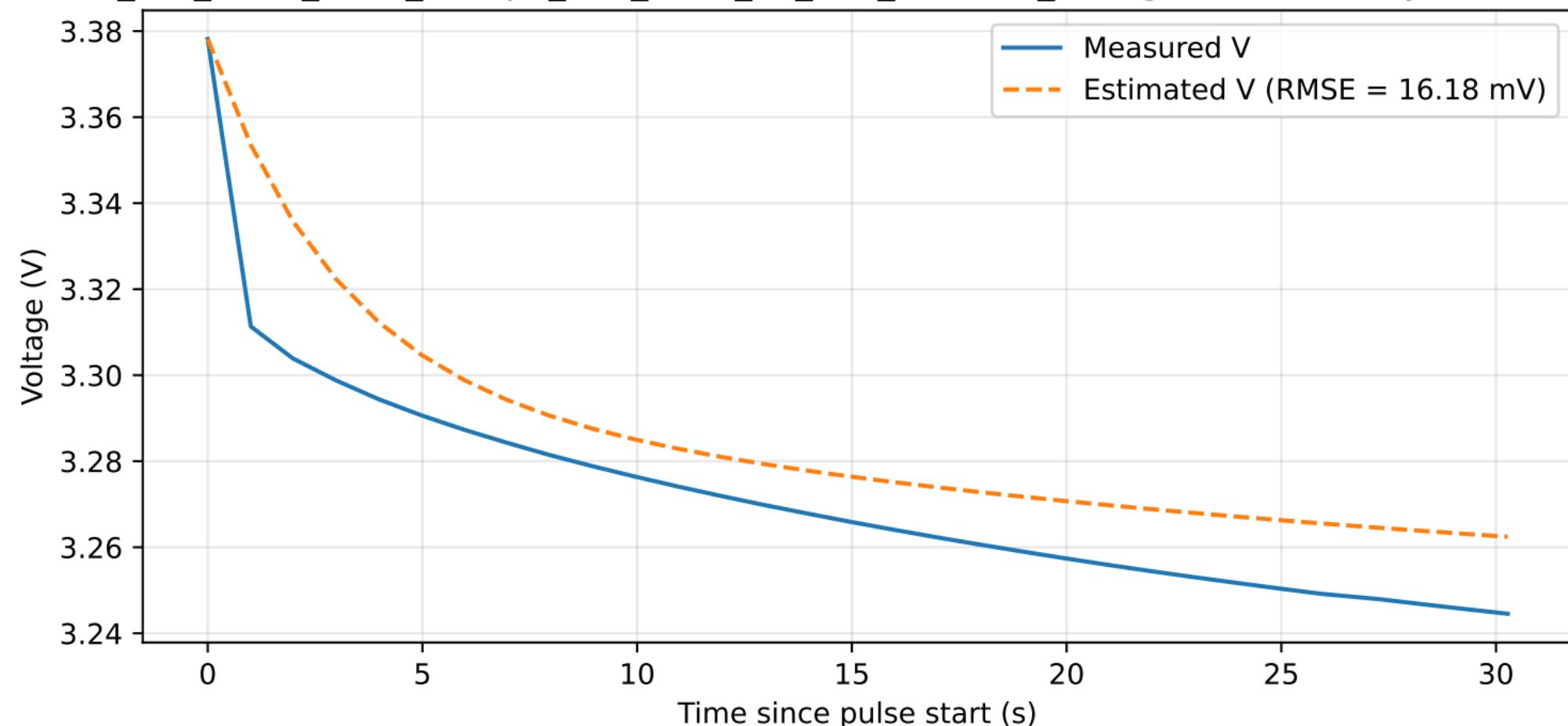
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0025\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



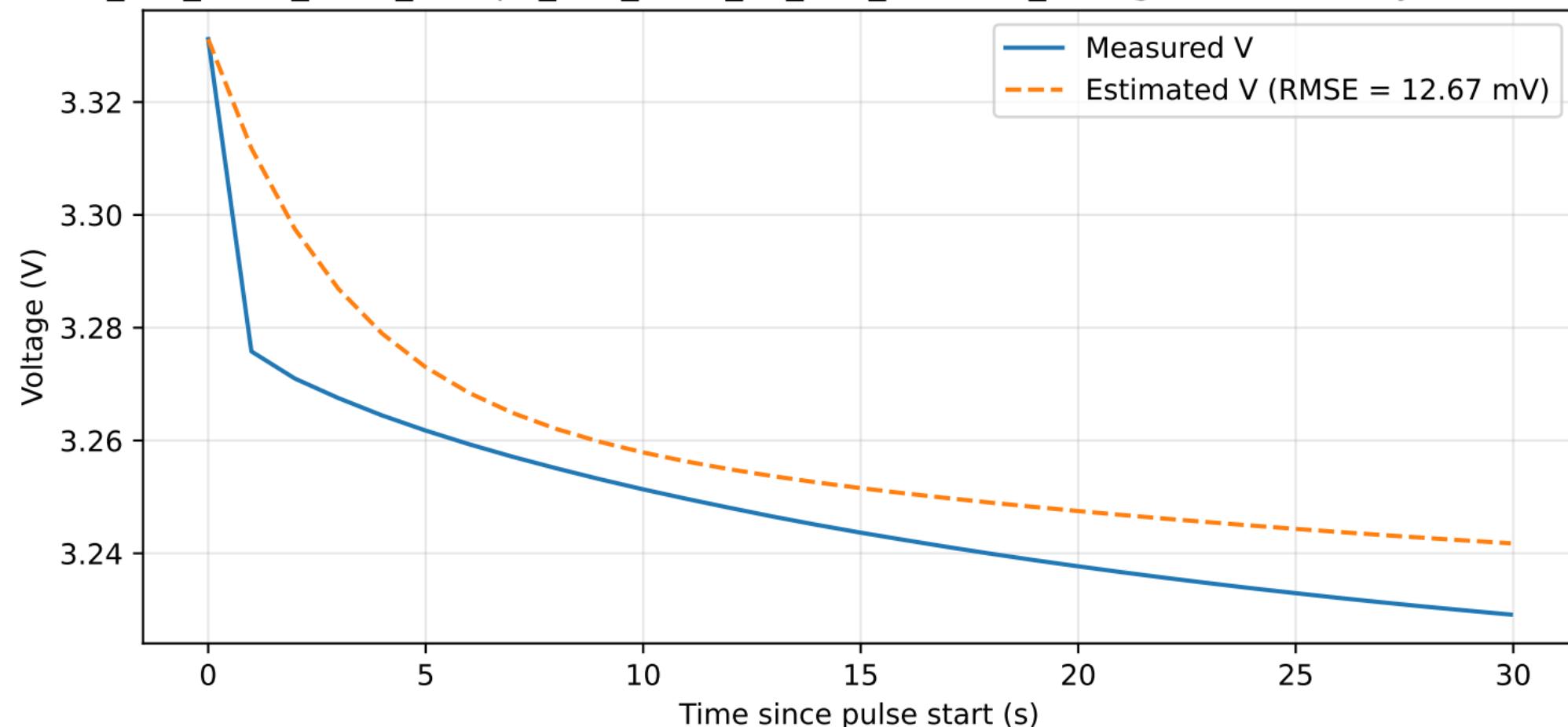
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



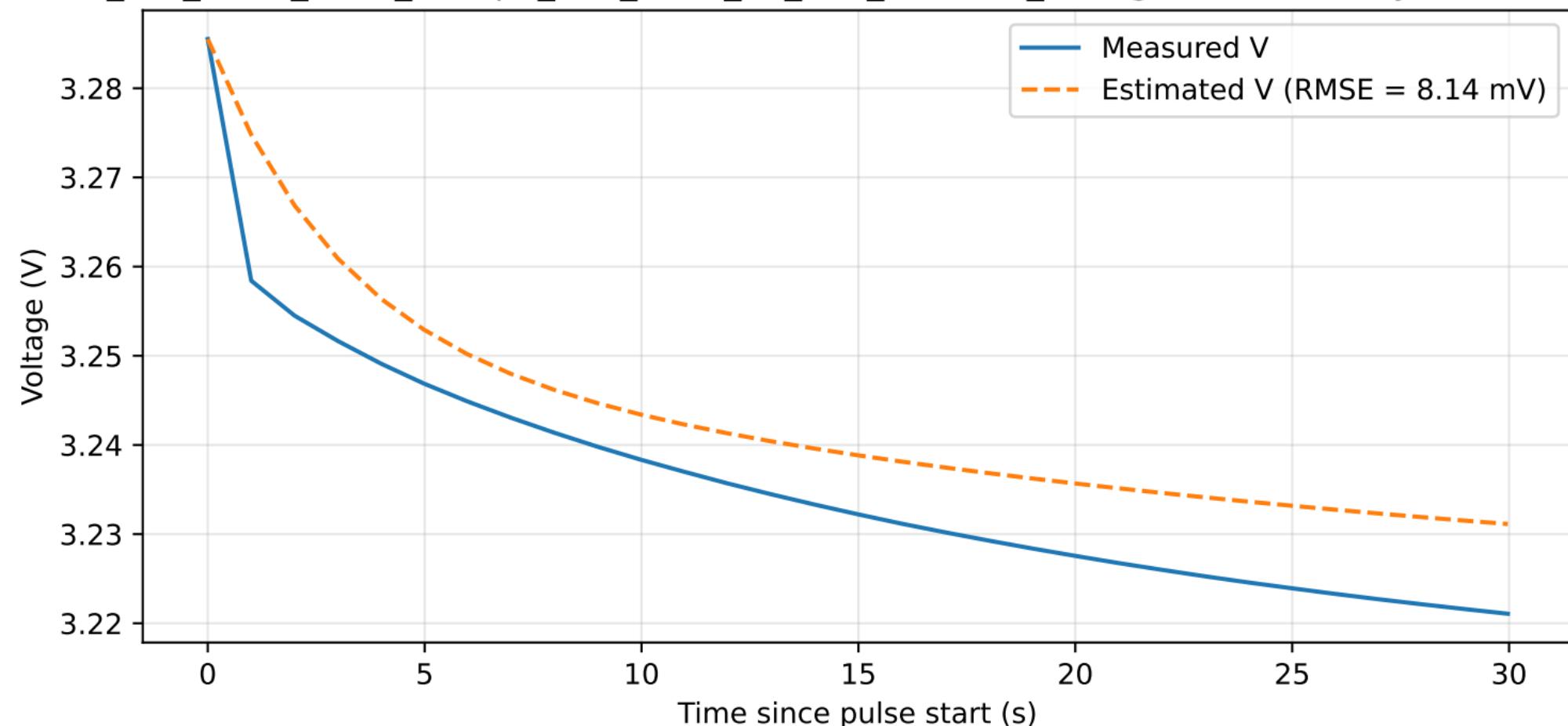
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



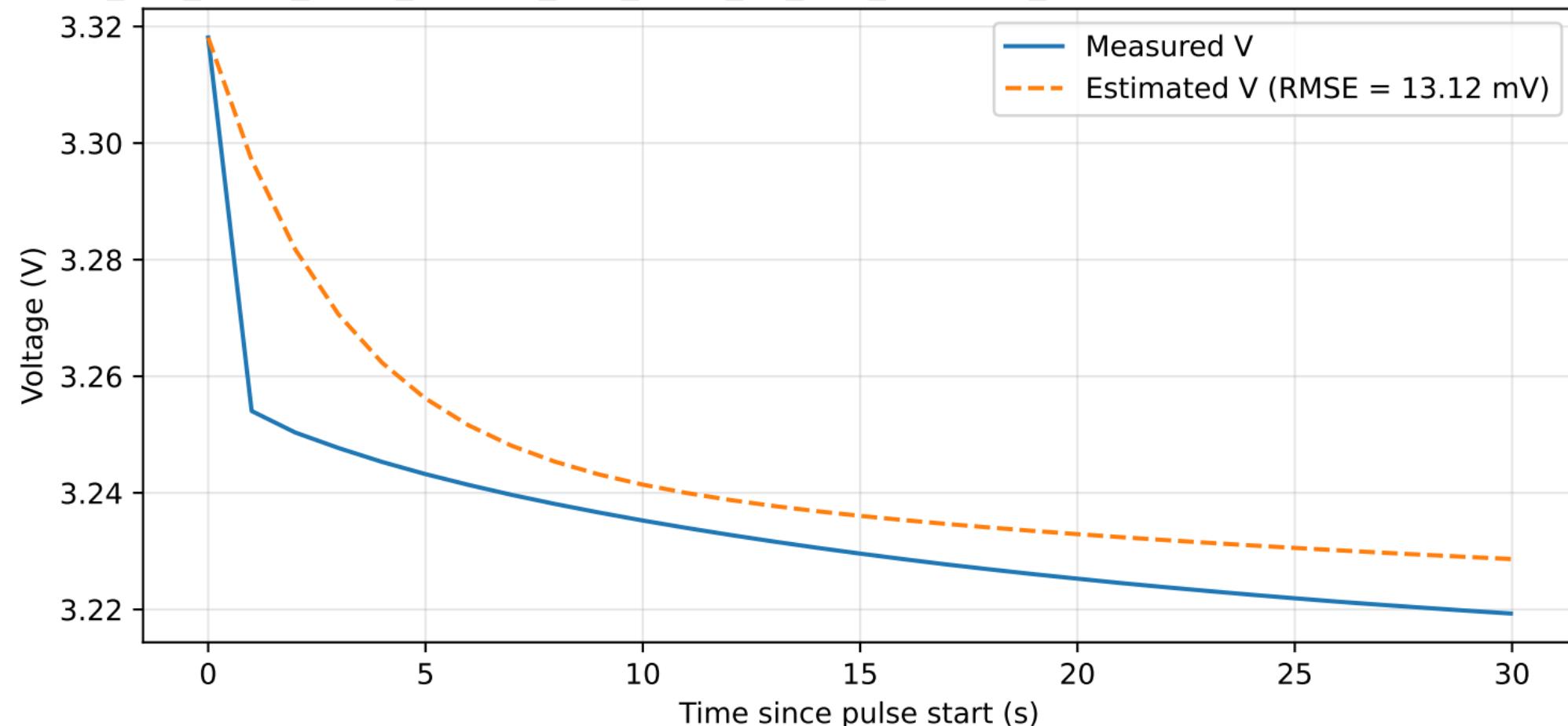
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



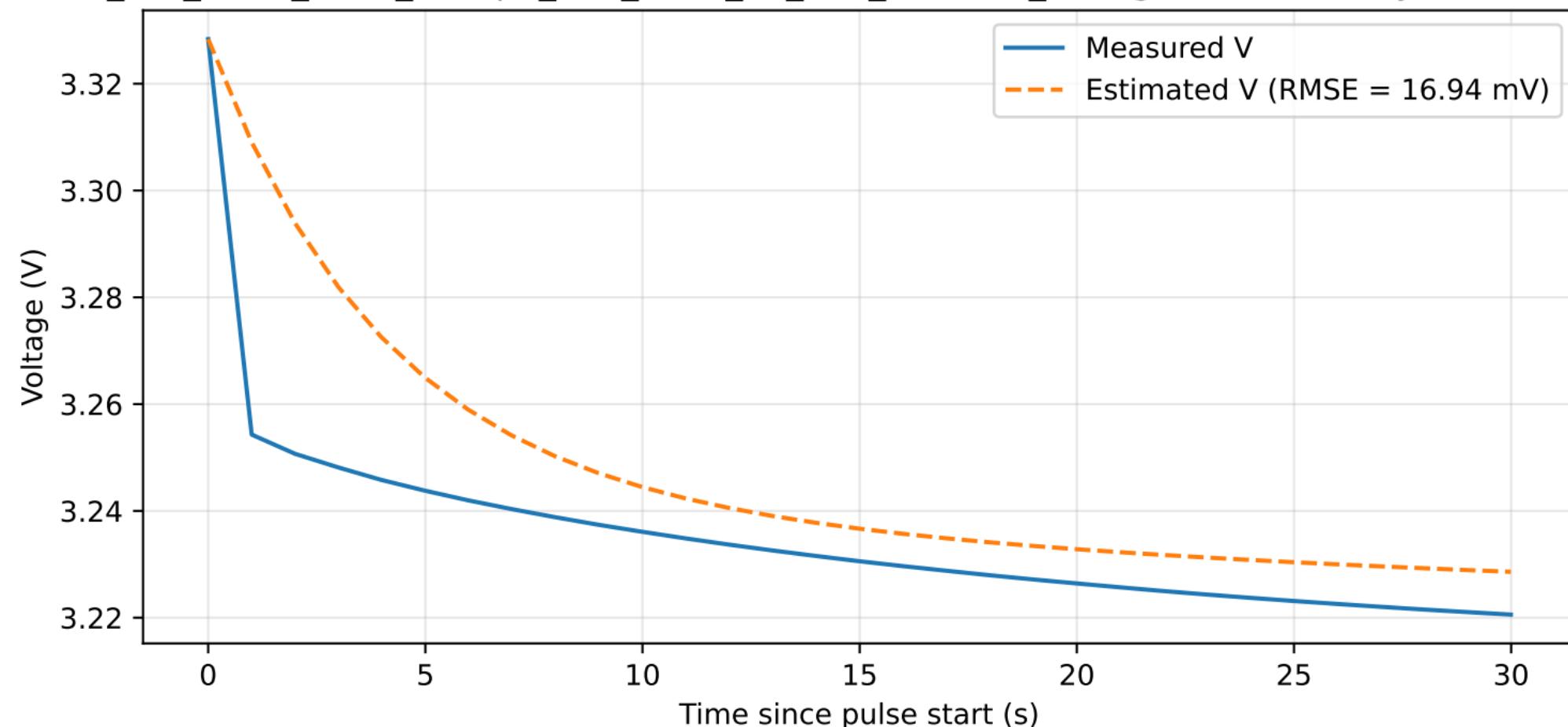
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



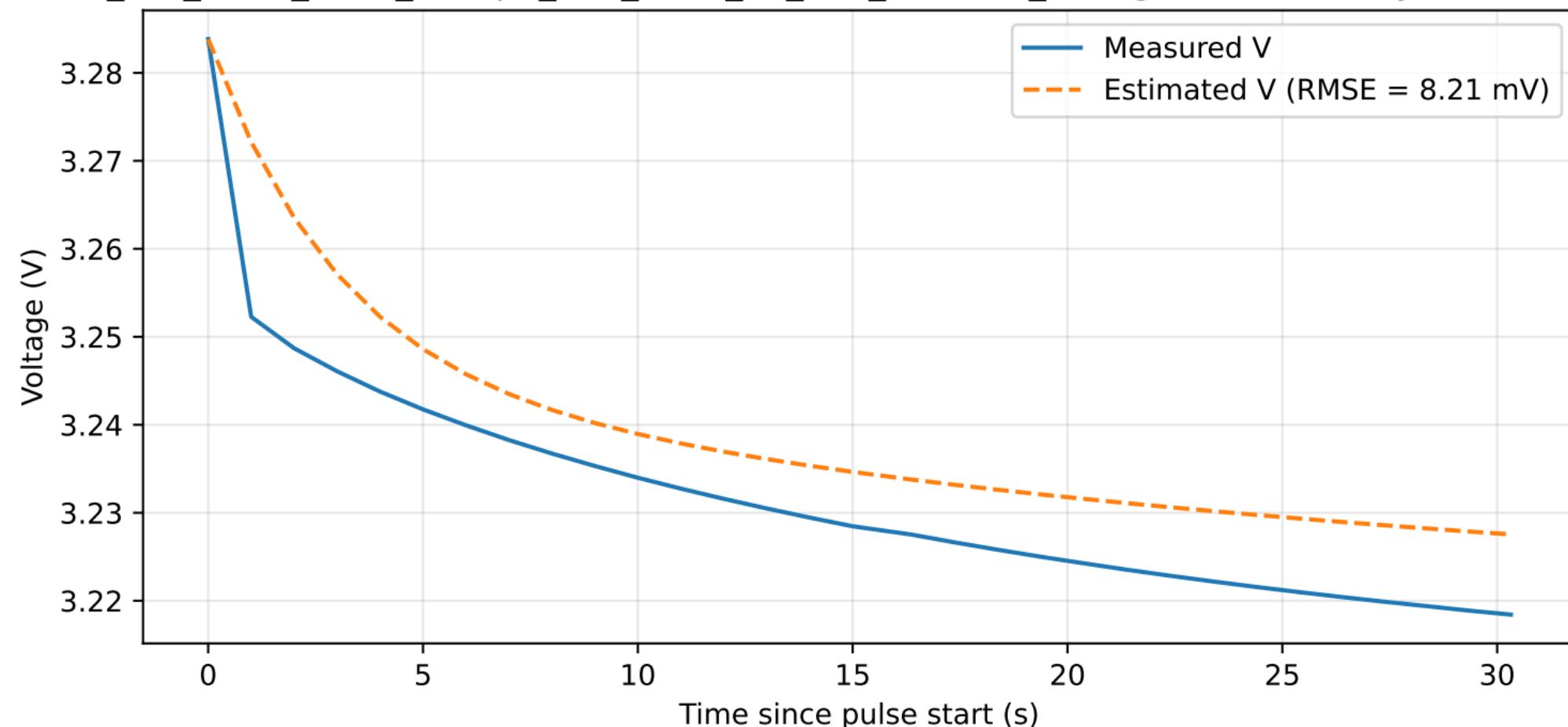
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



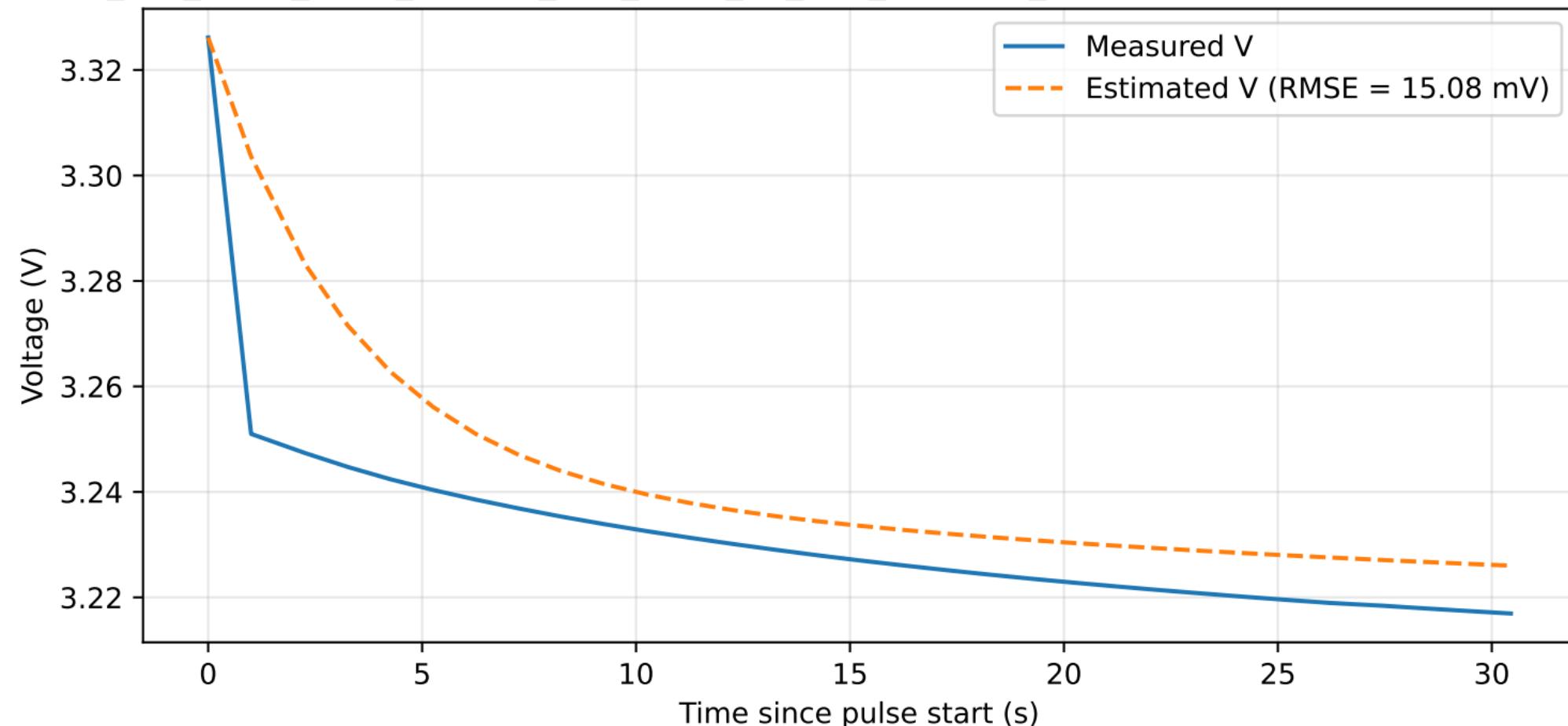
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



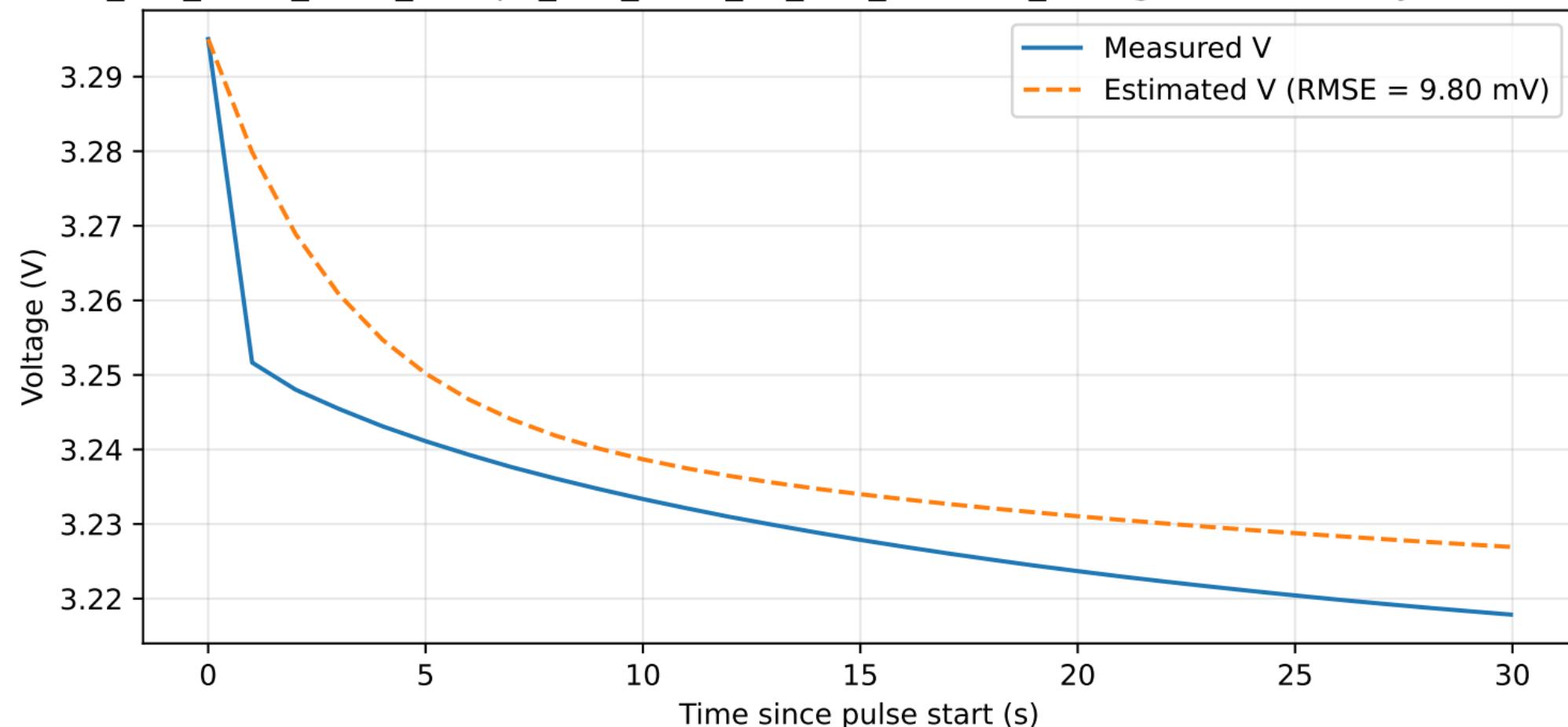
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



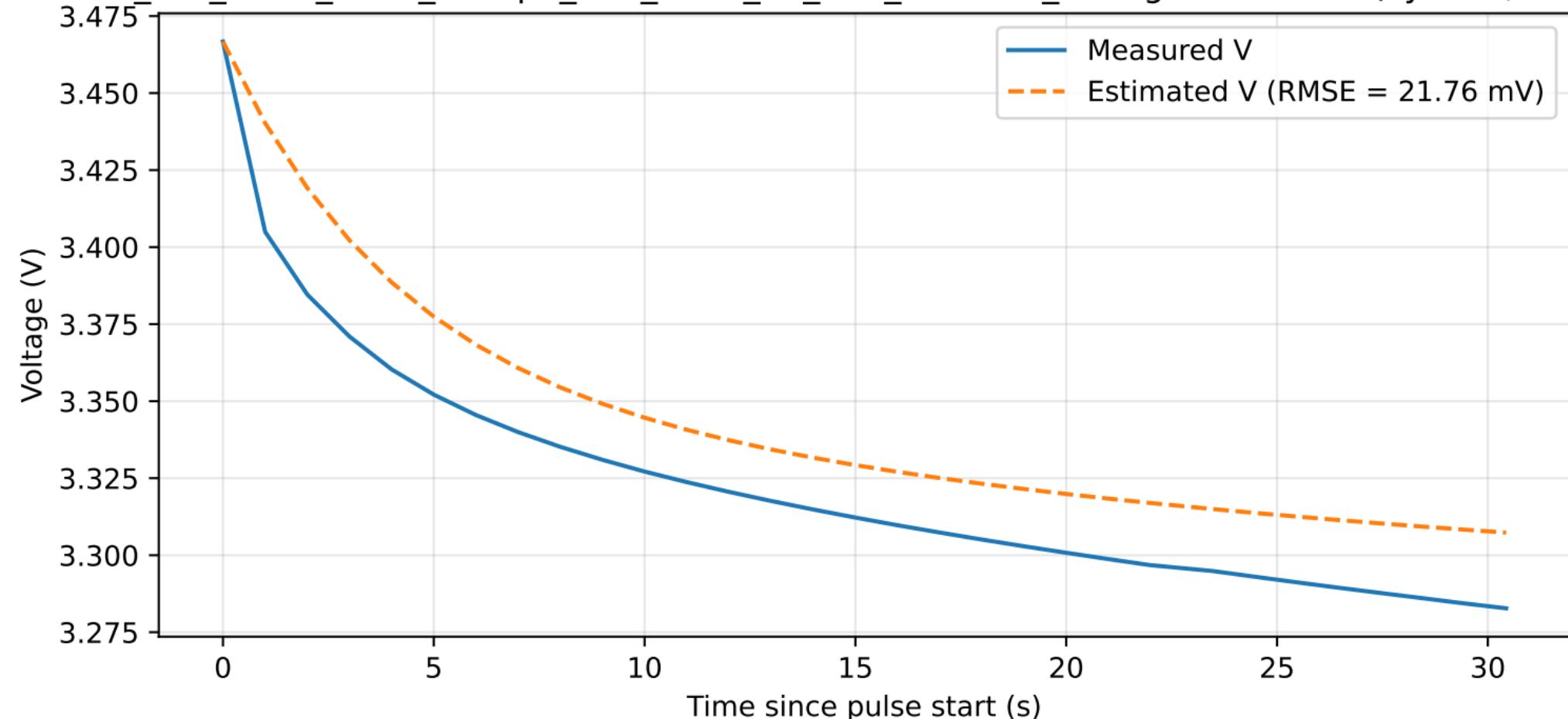
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



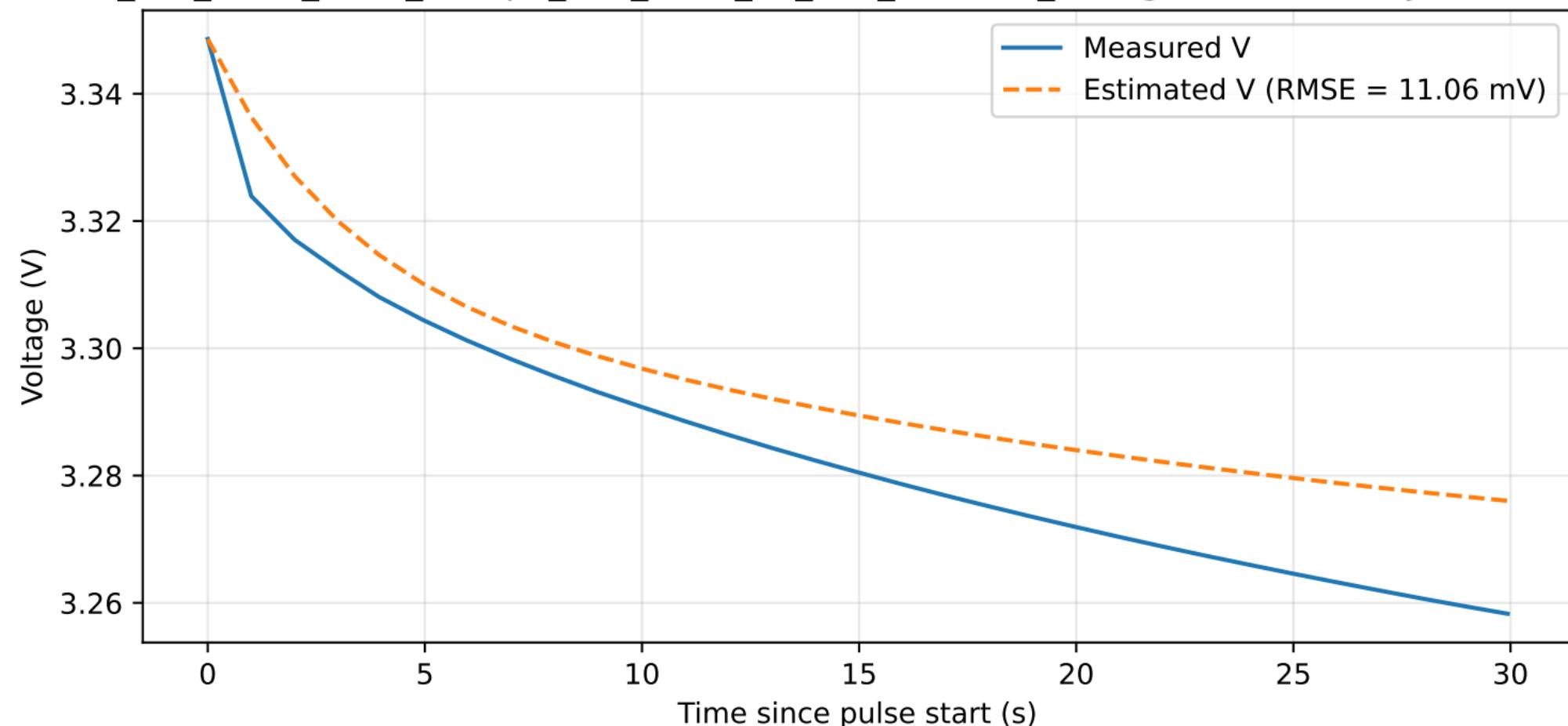
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0034\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



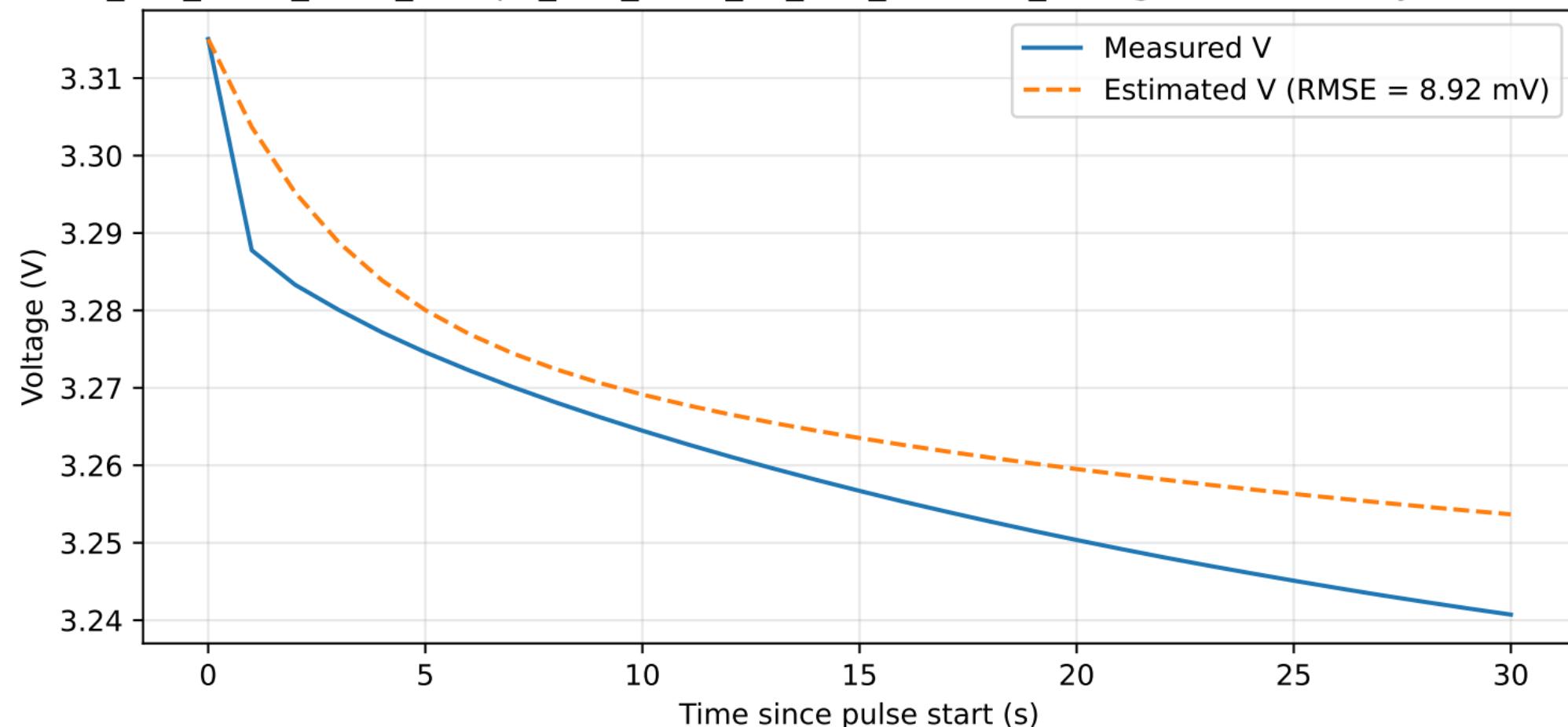
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



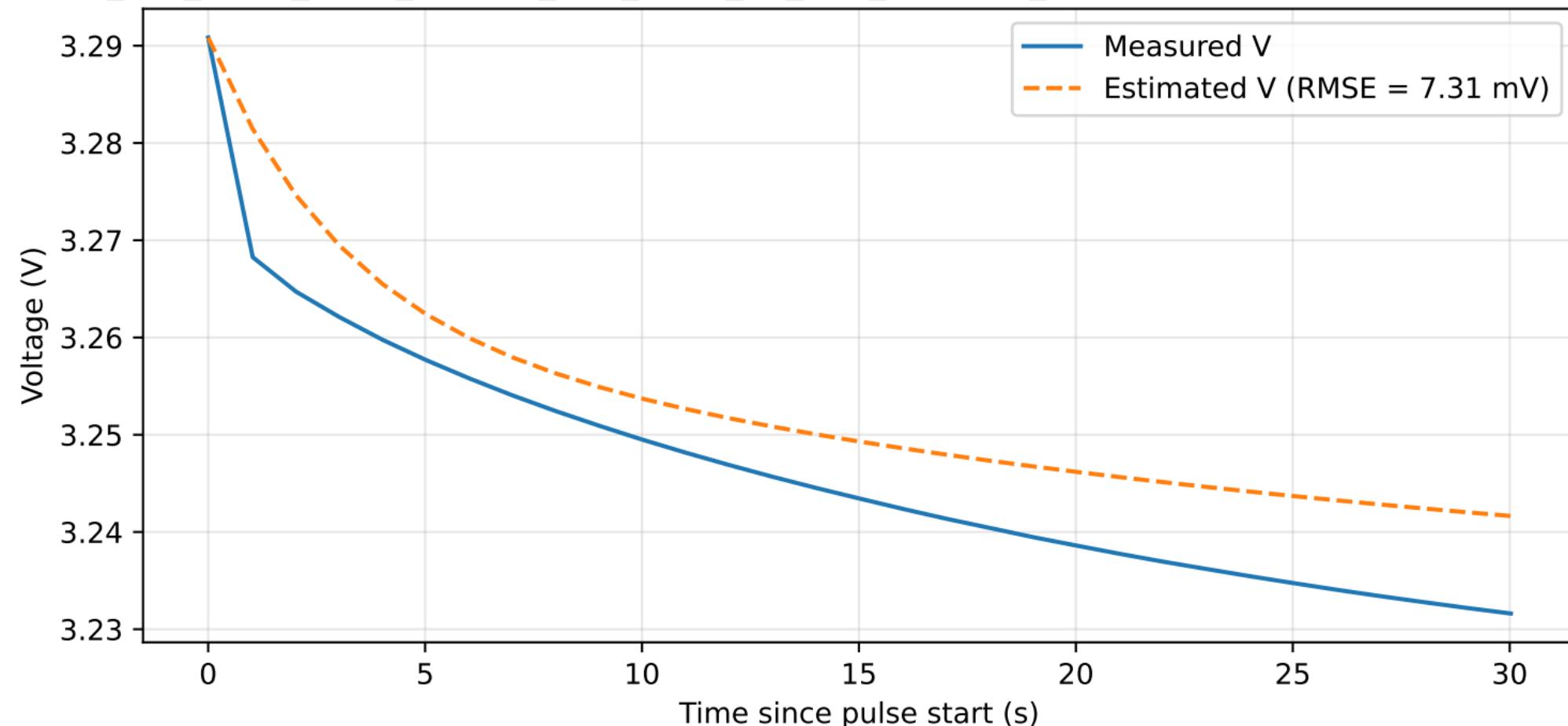
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



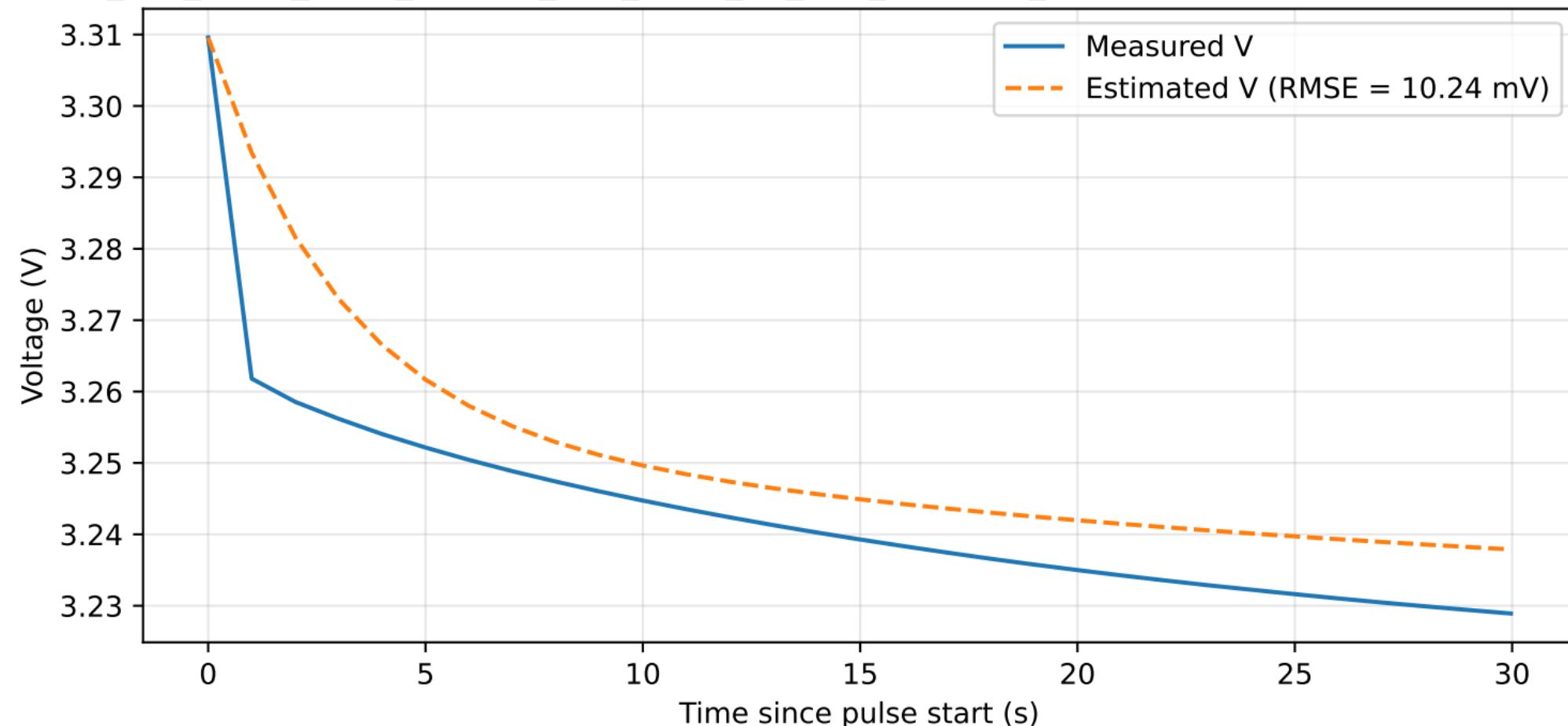
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



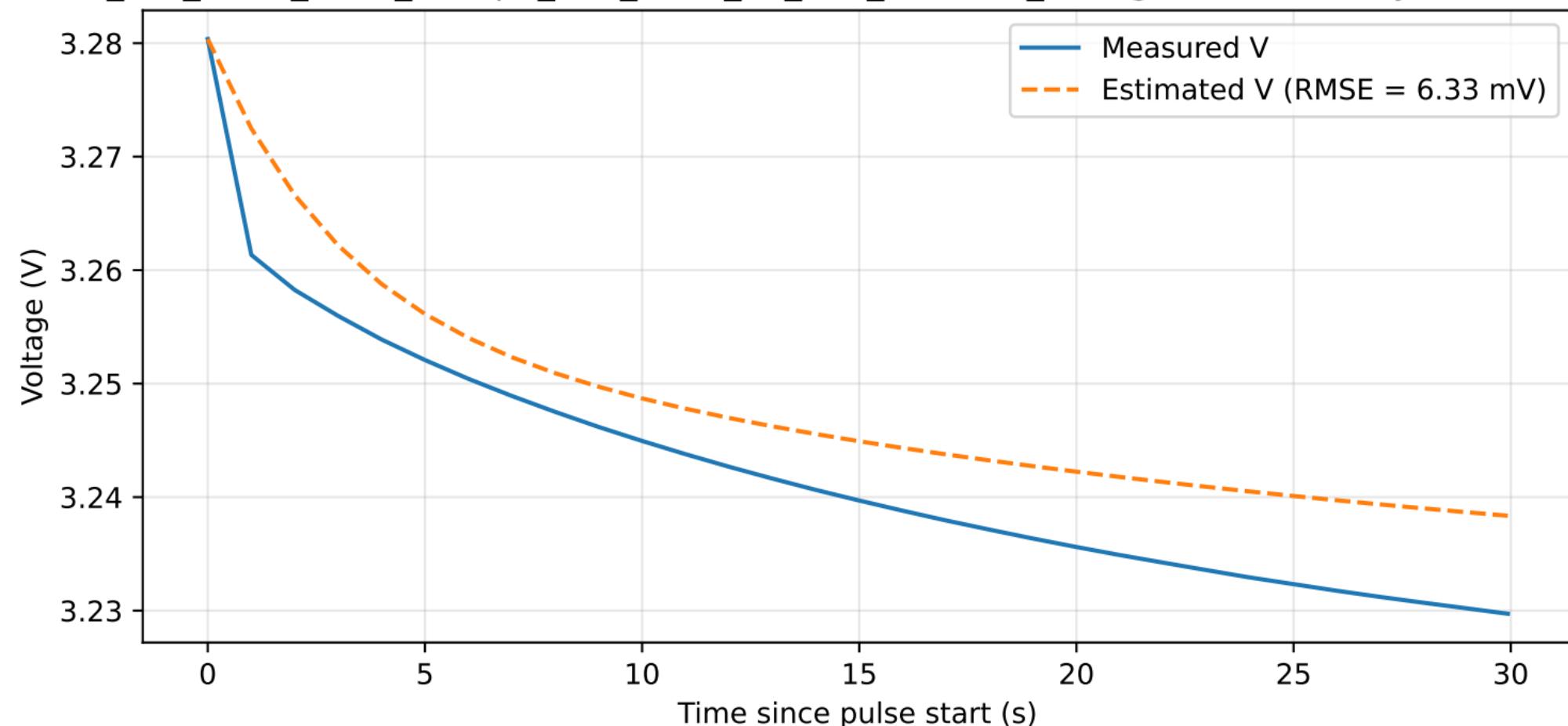
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



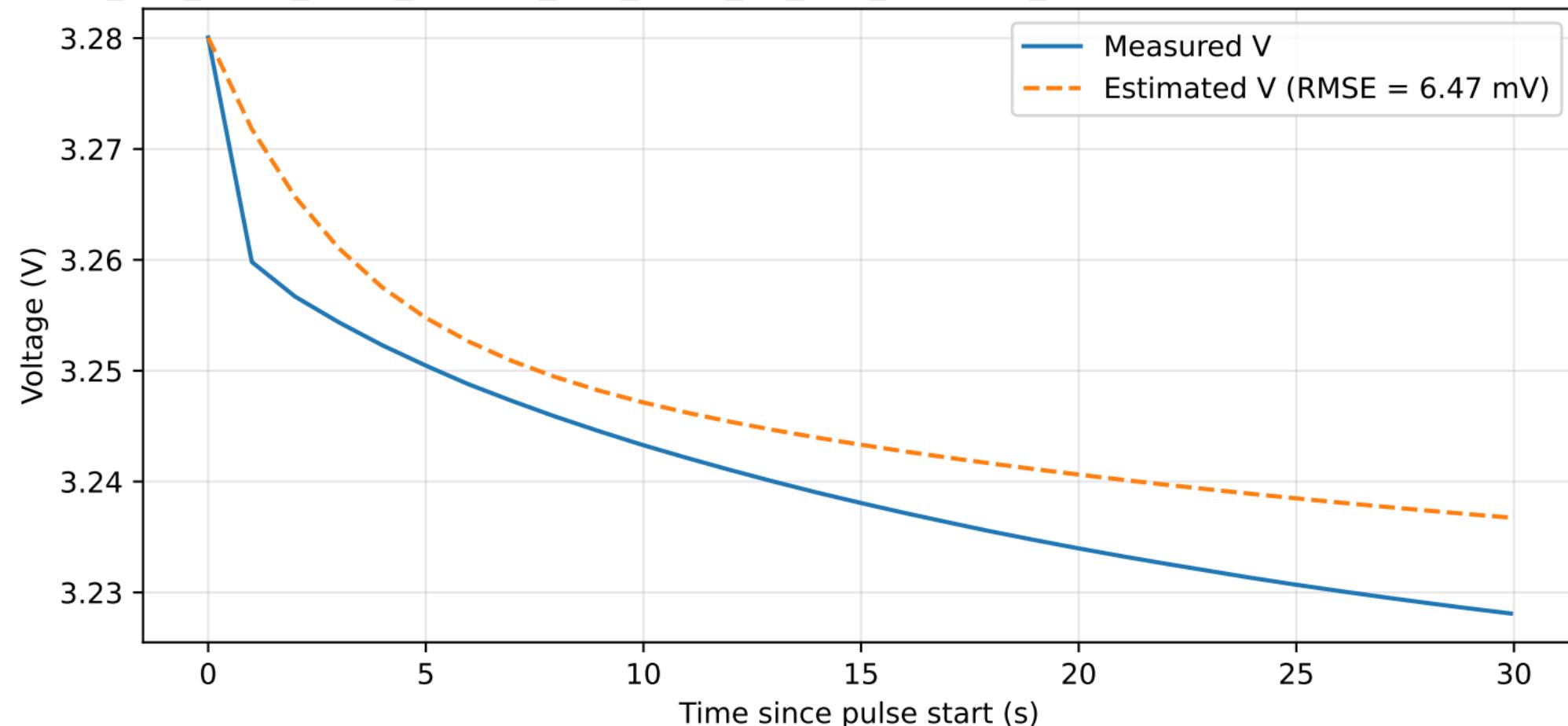
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



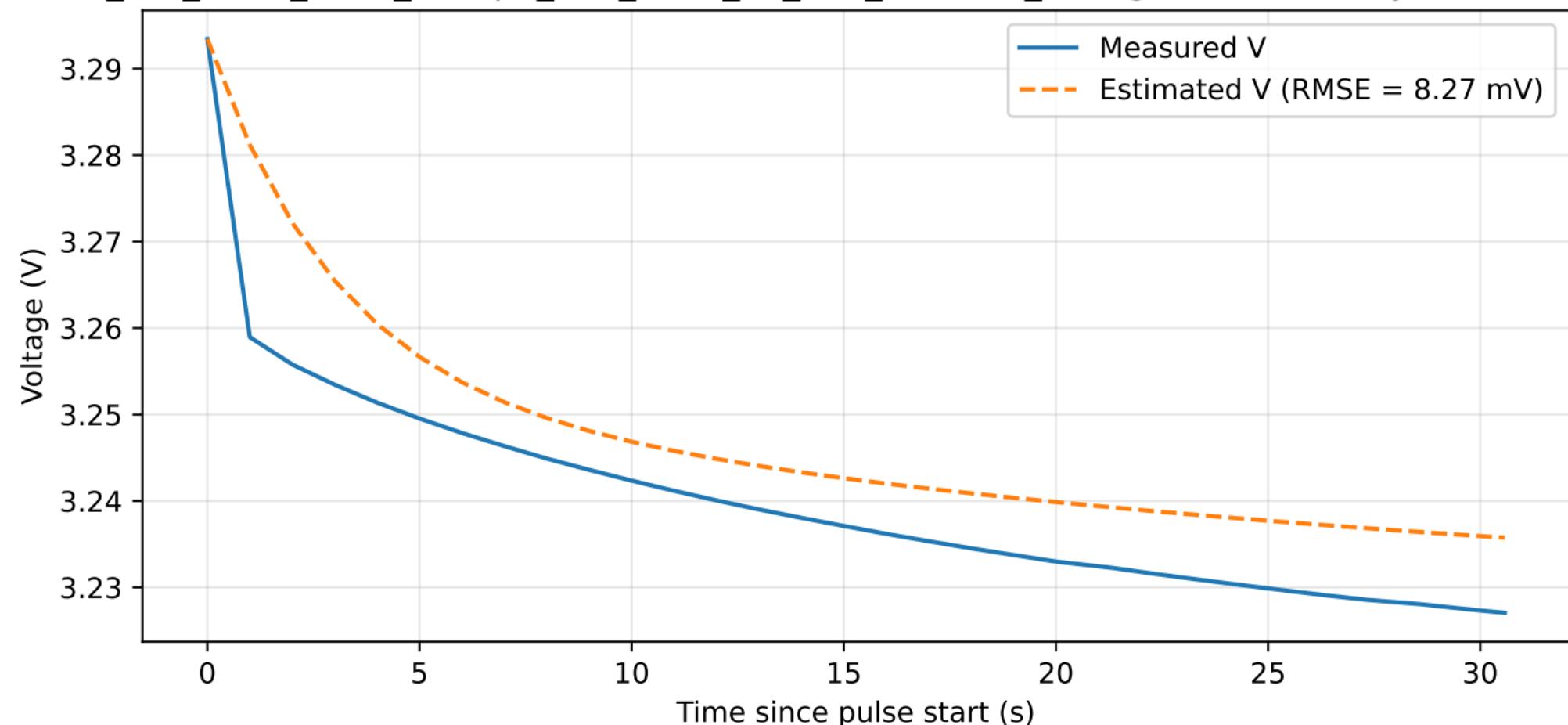
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



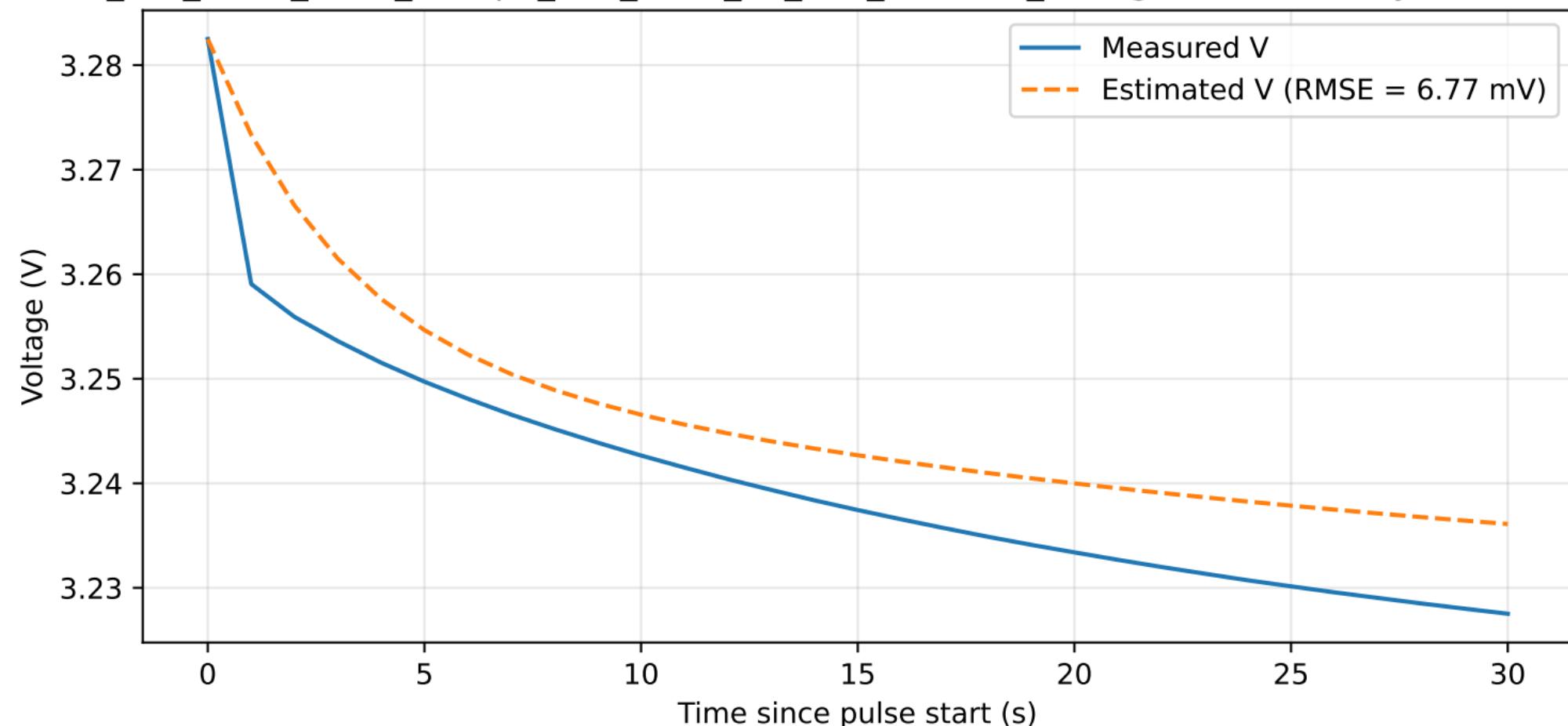
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



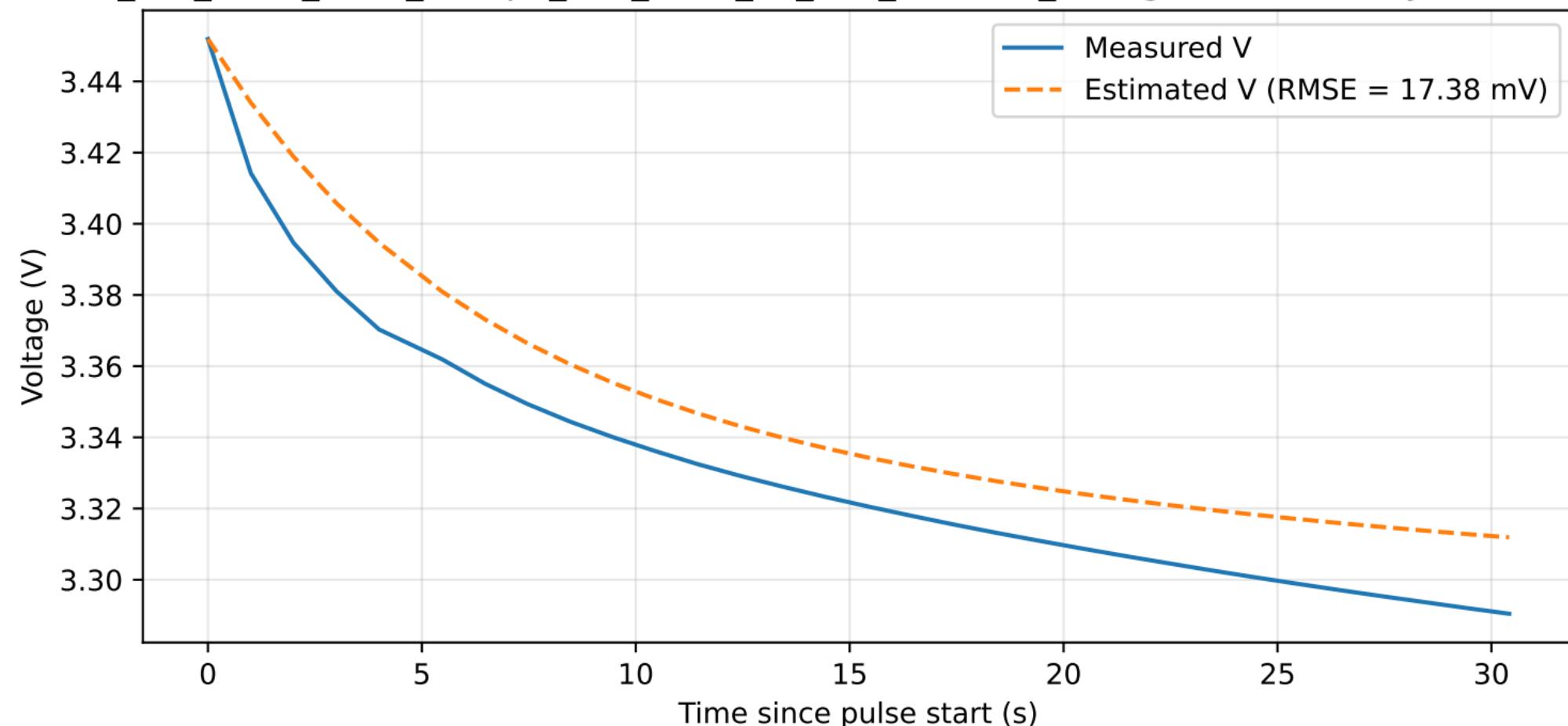
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



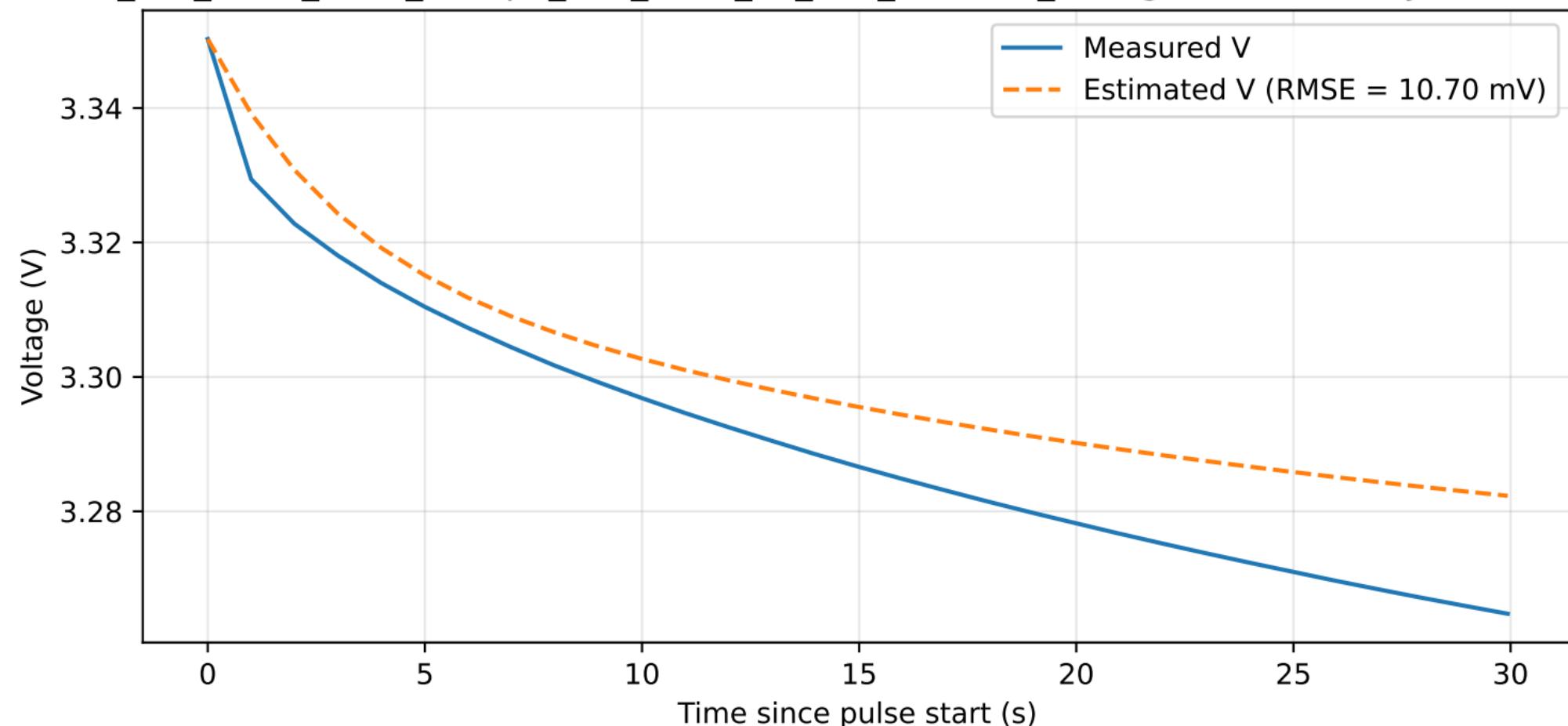
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0040\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



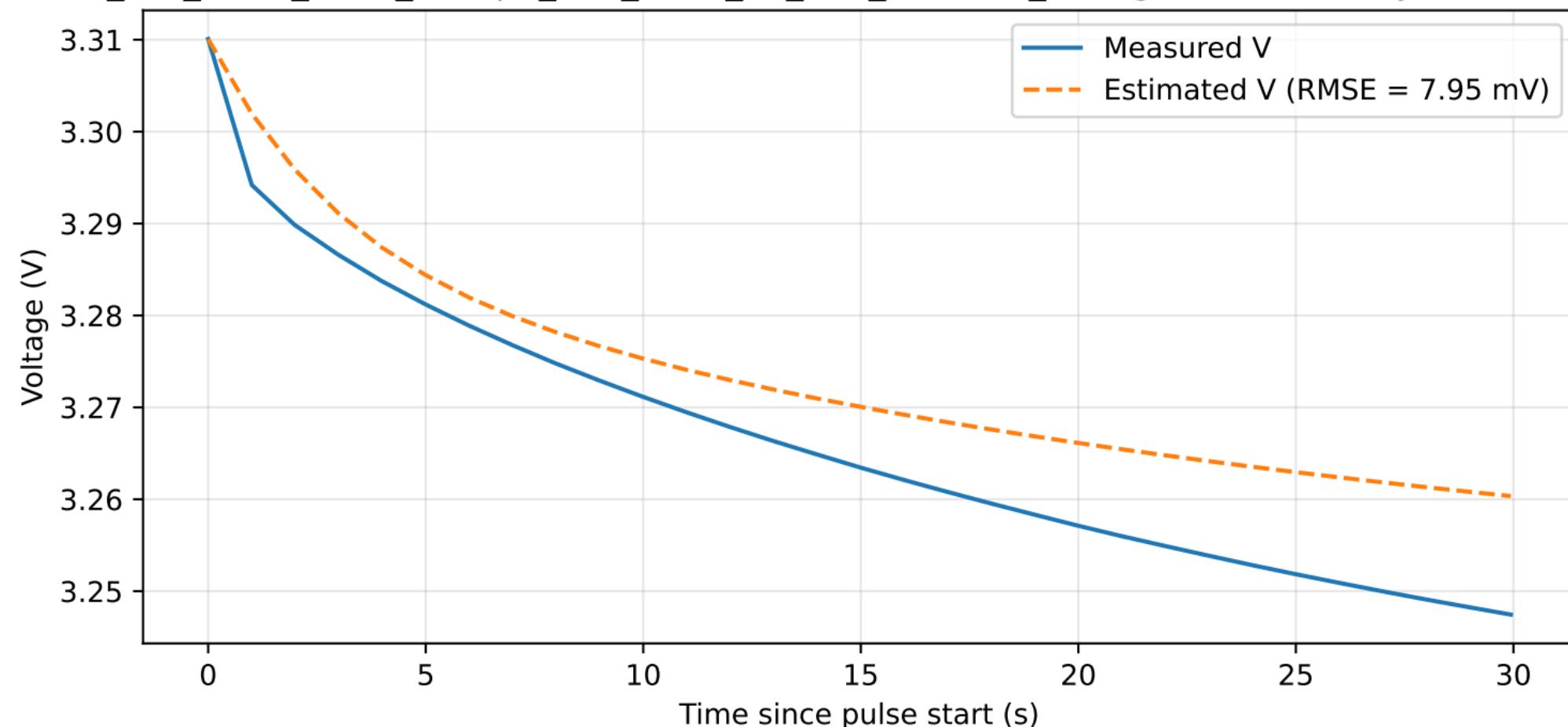
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



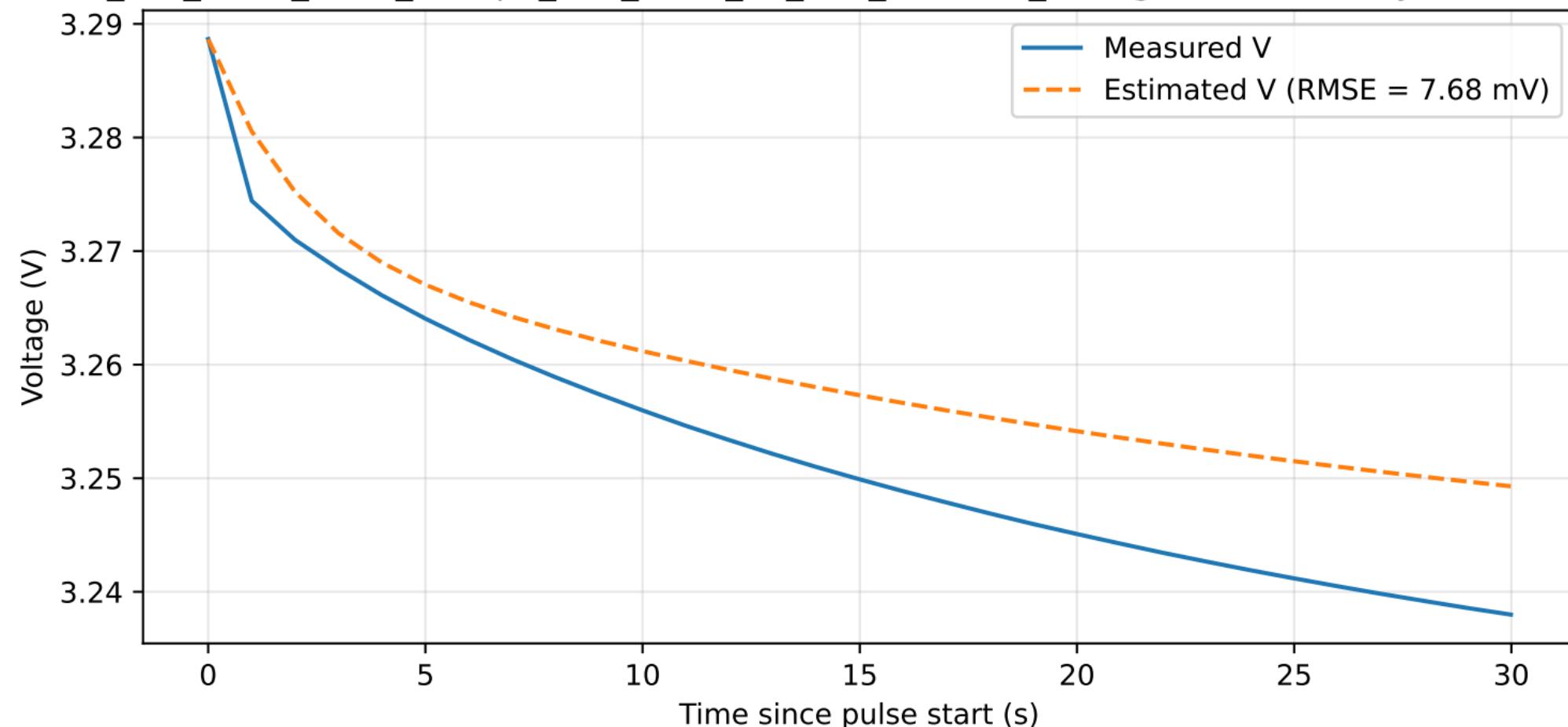
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



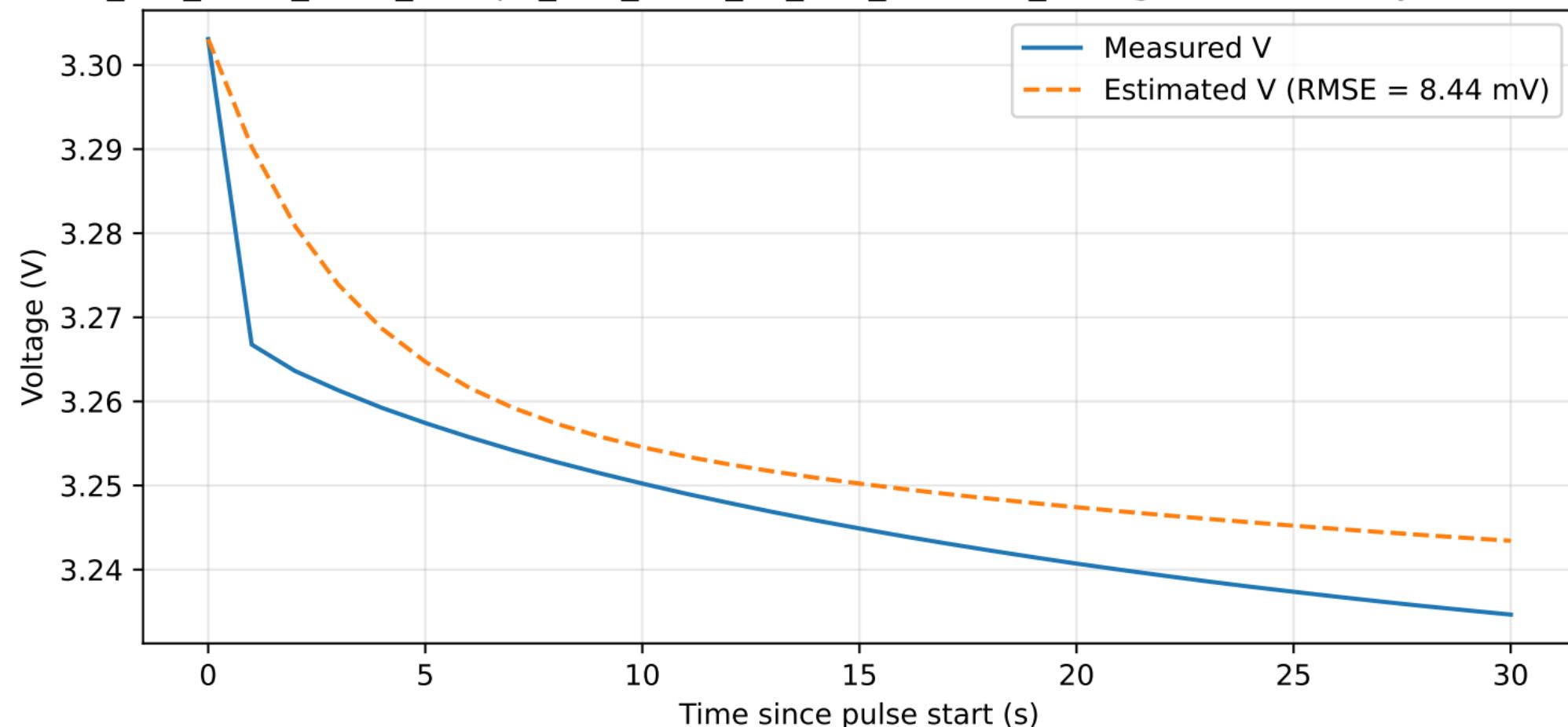
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



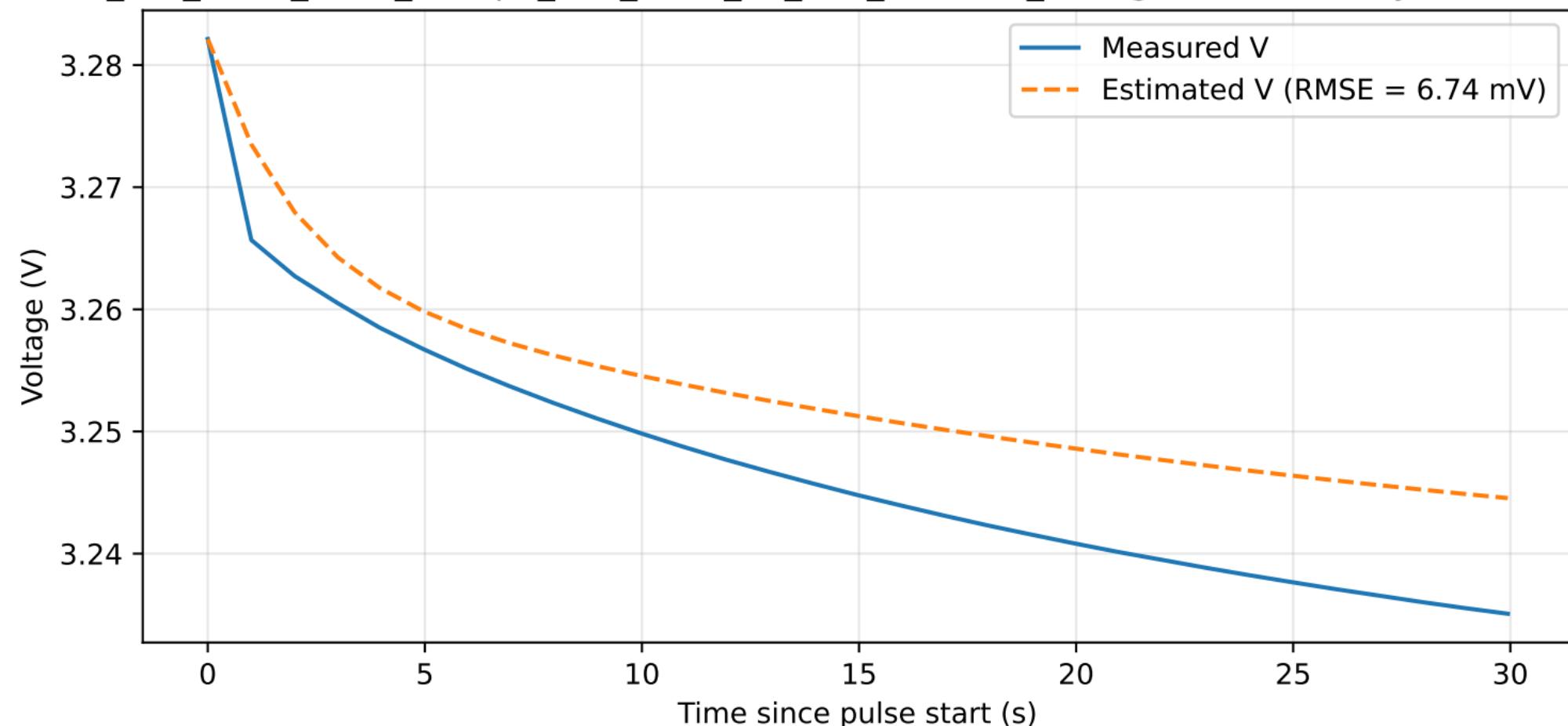
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



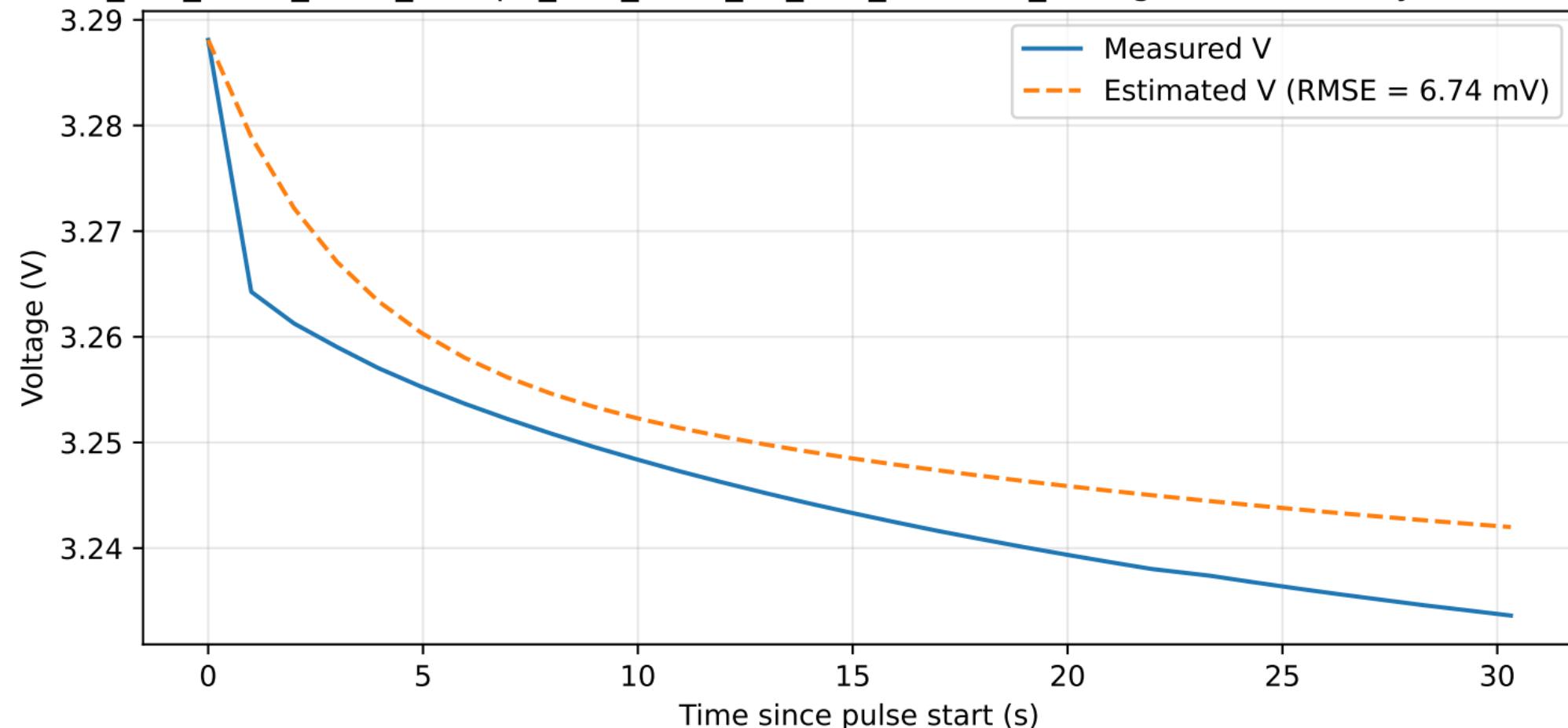
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



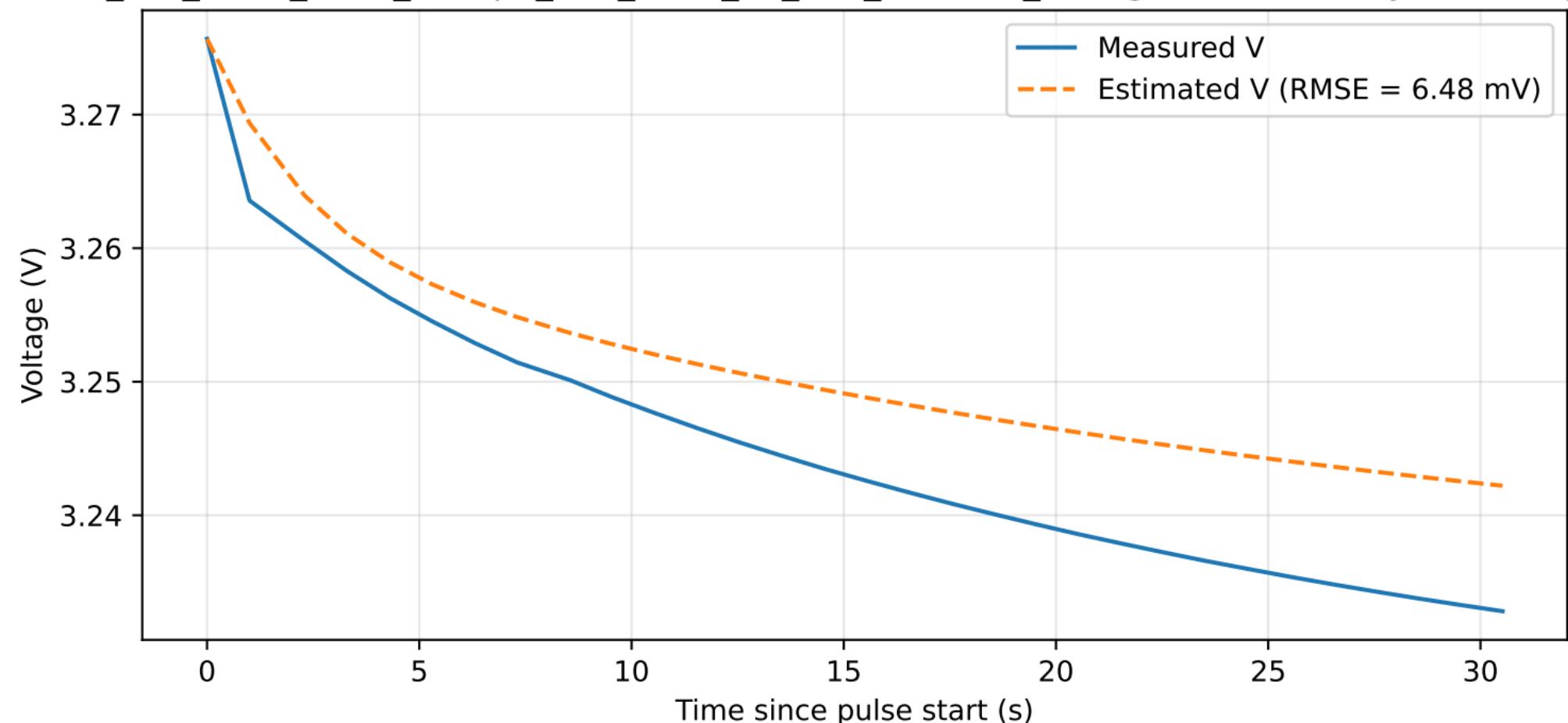
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



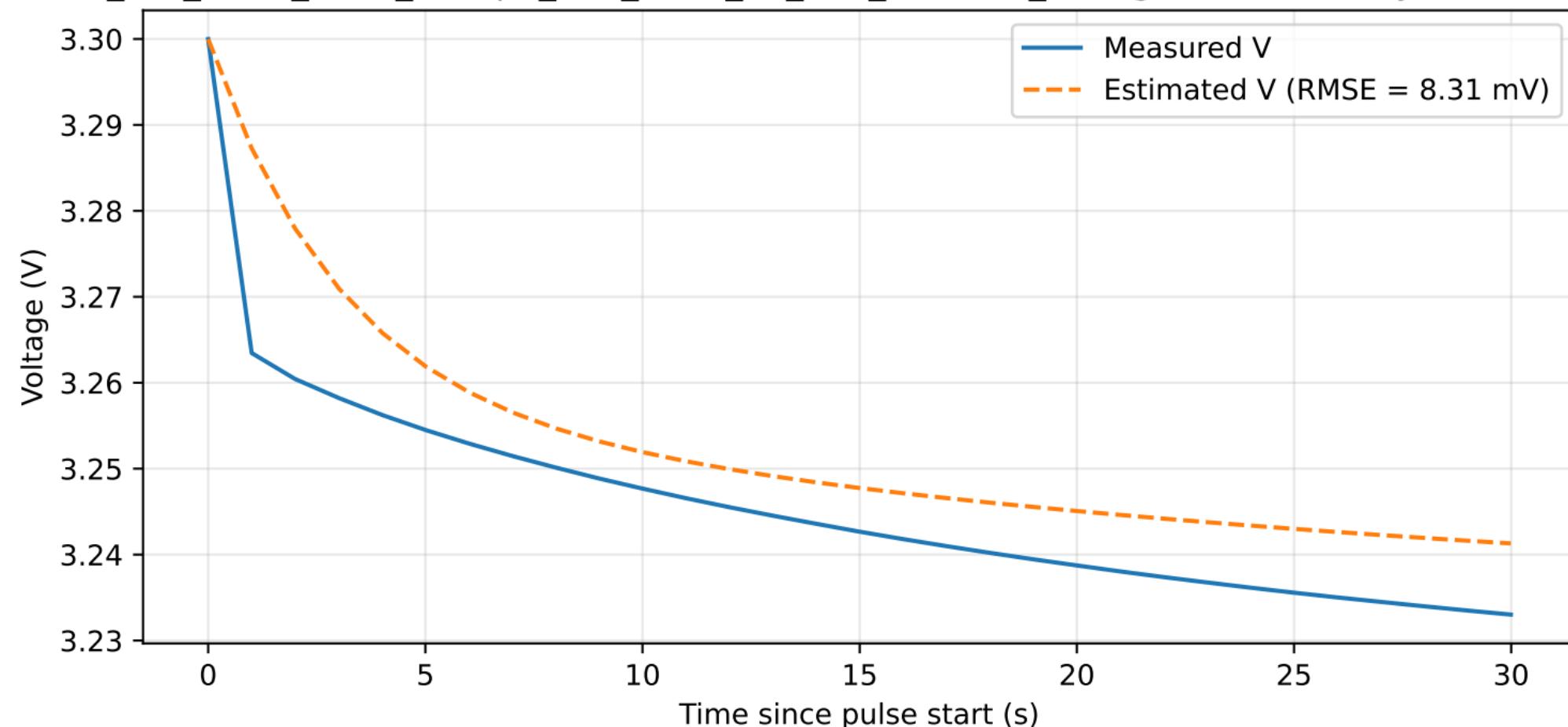
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



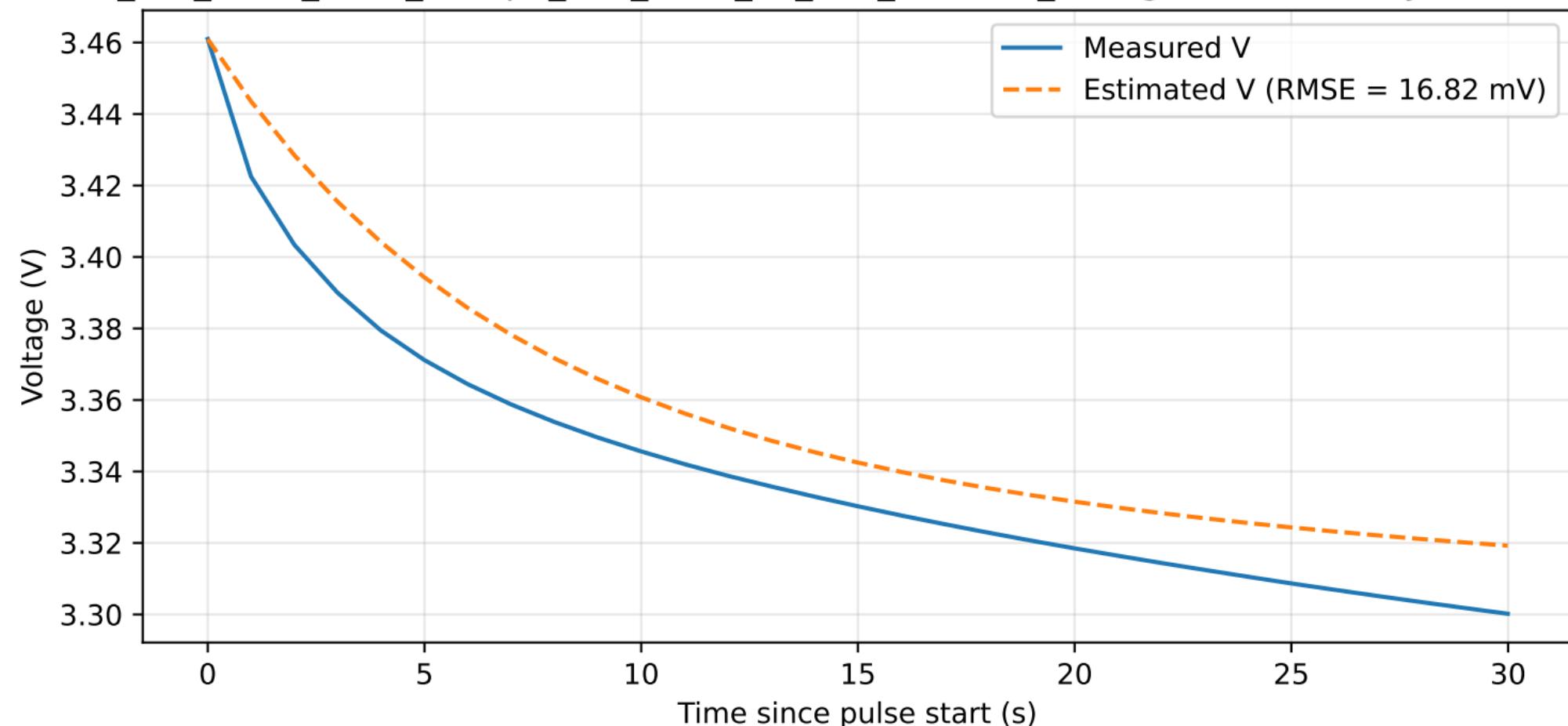
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



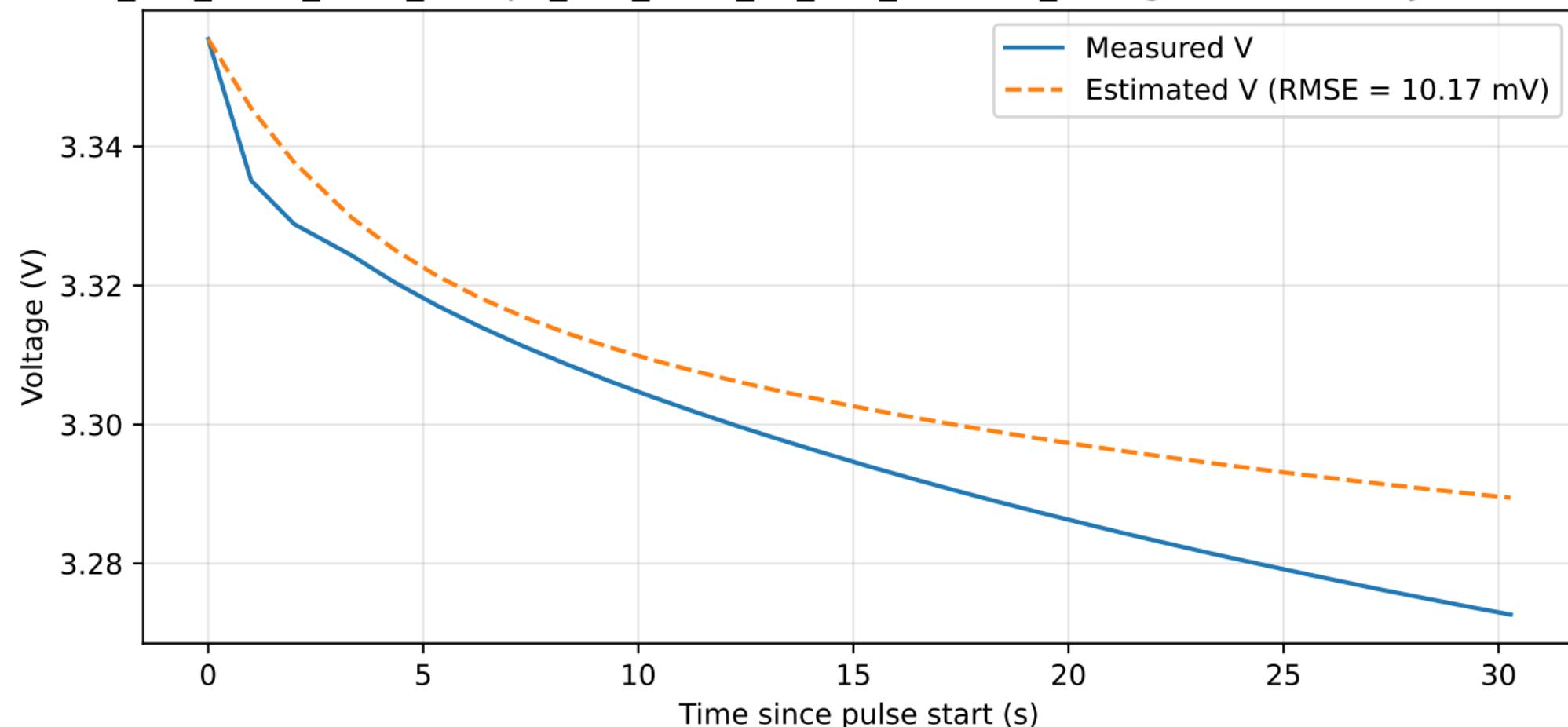
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0043\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



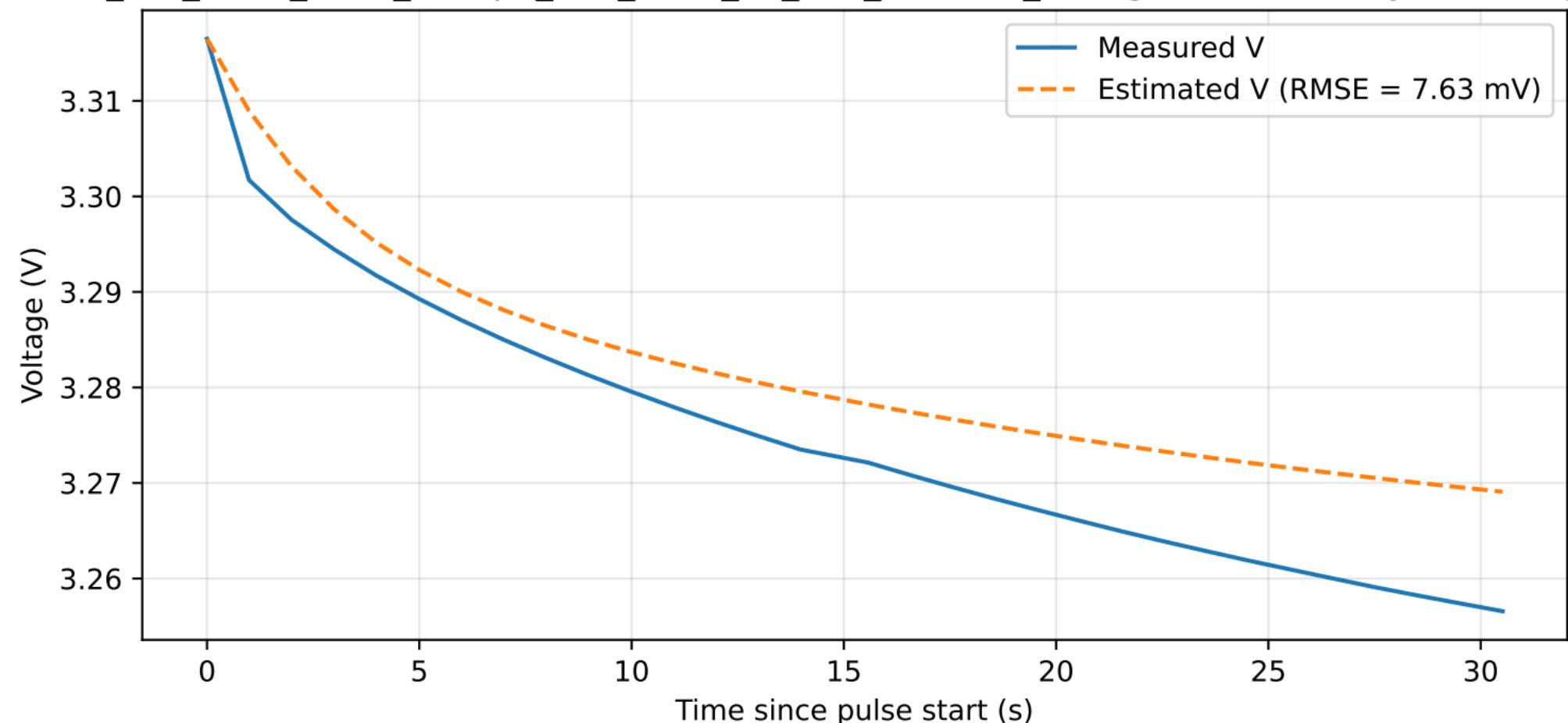
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



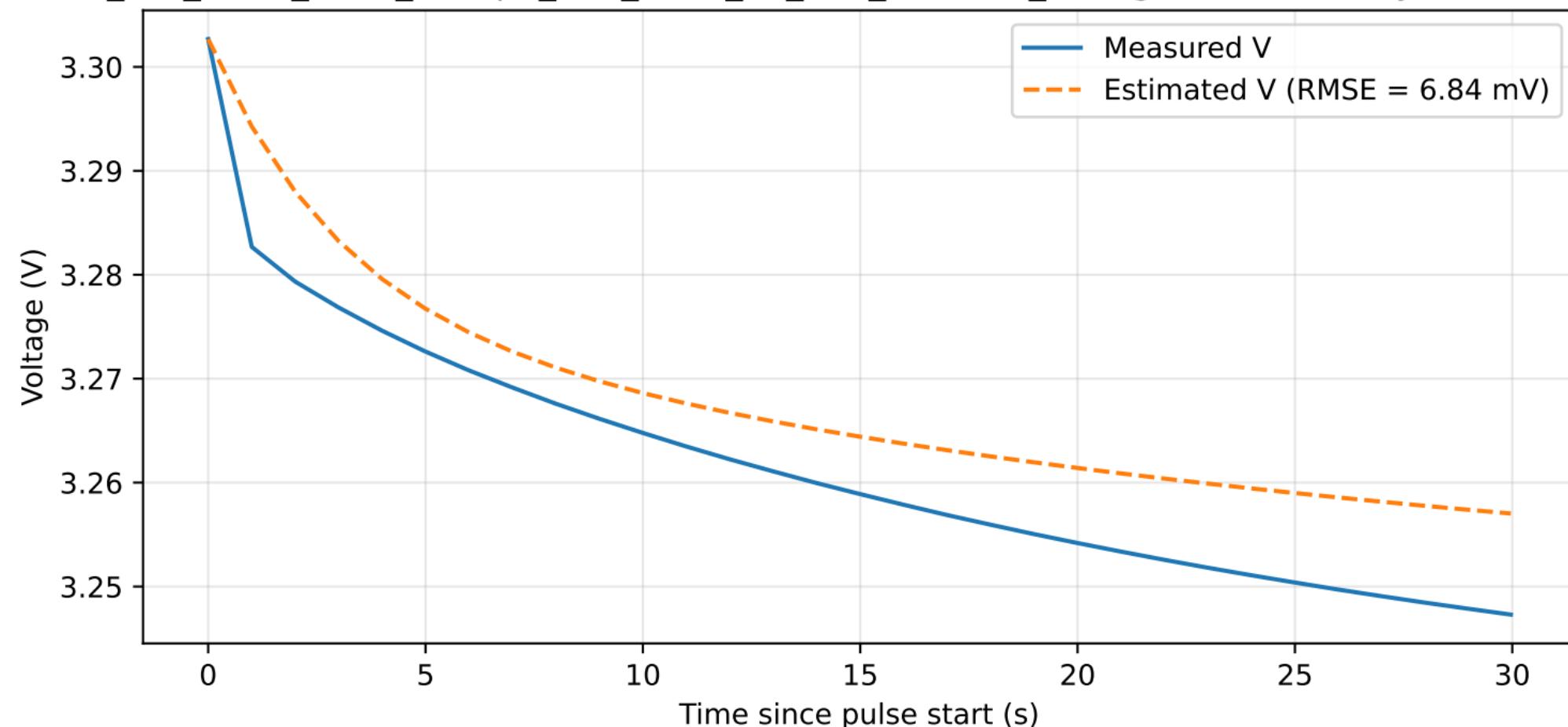
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



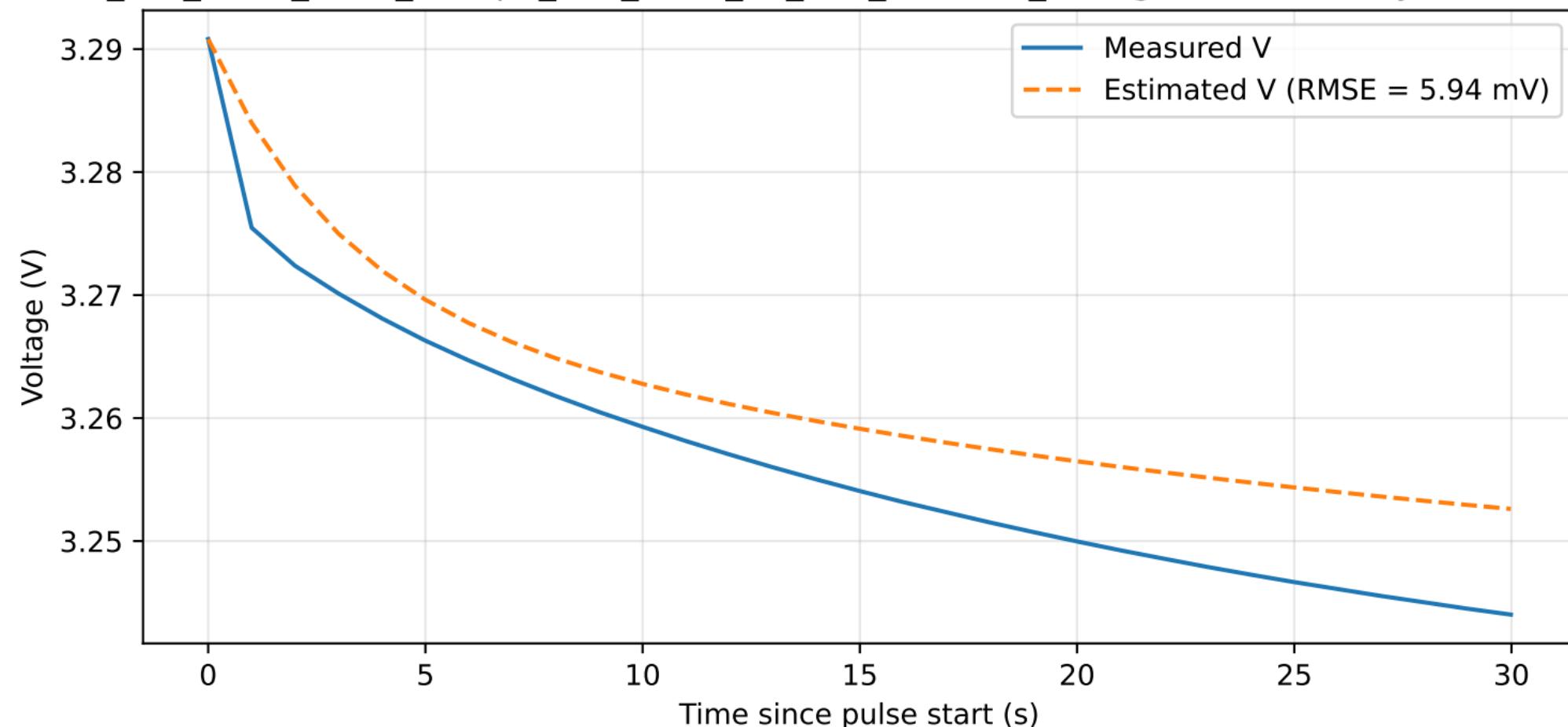
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



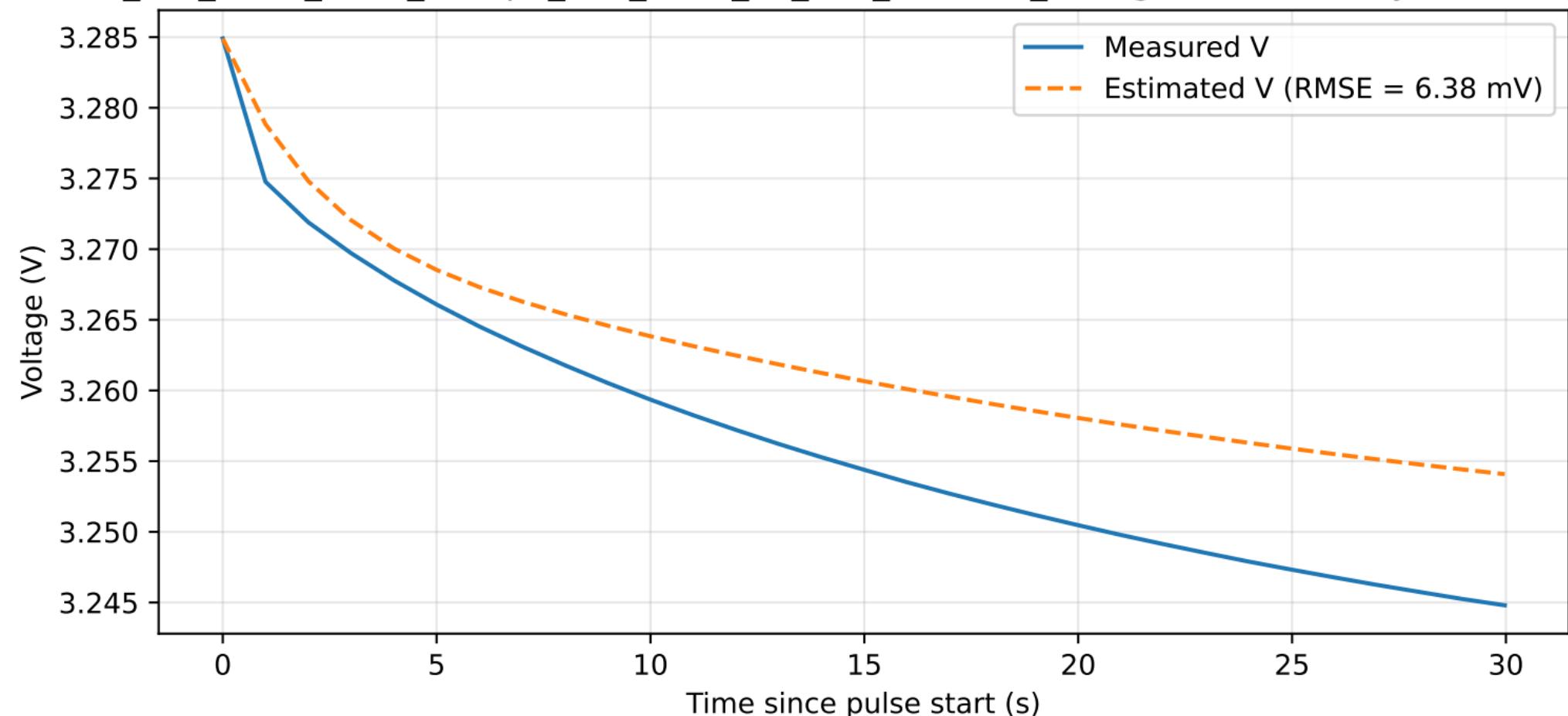
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



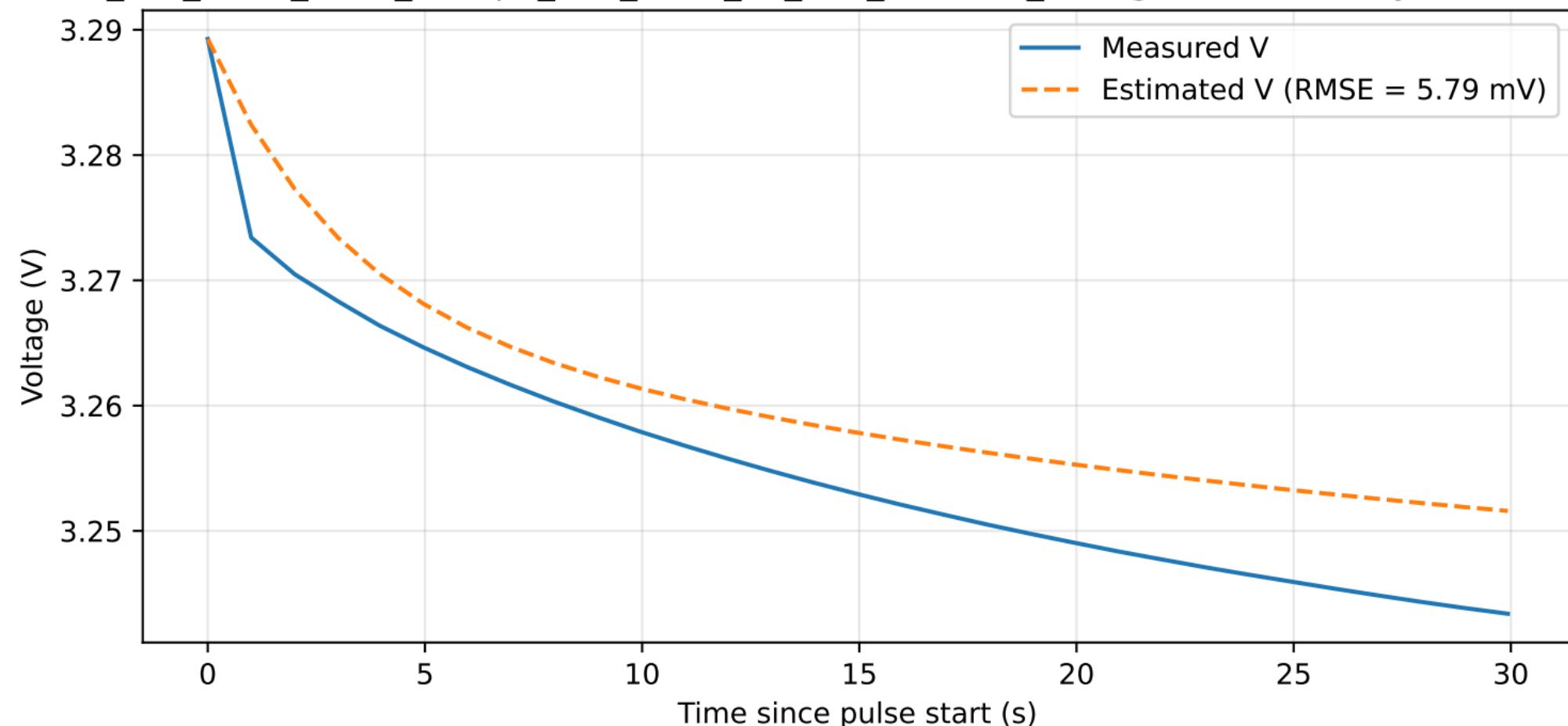
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



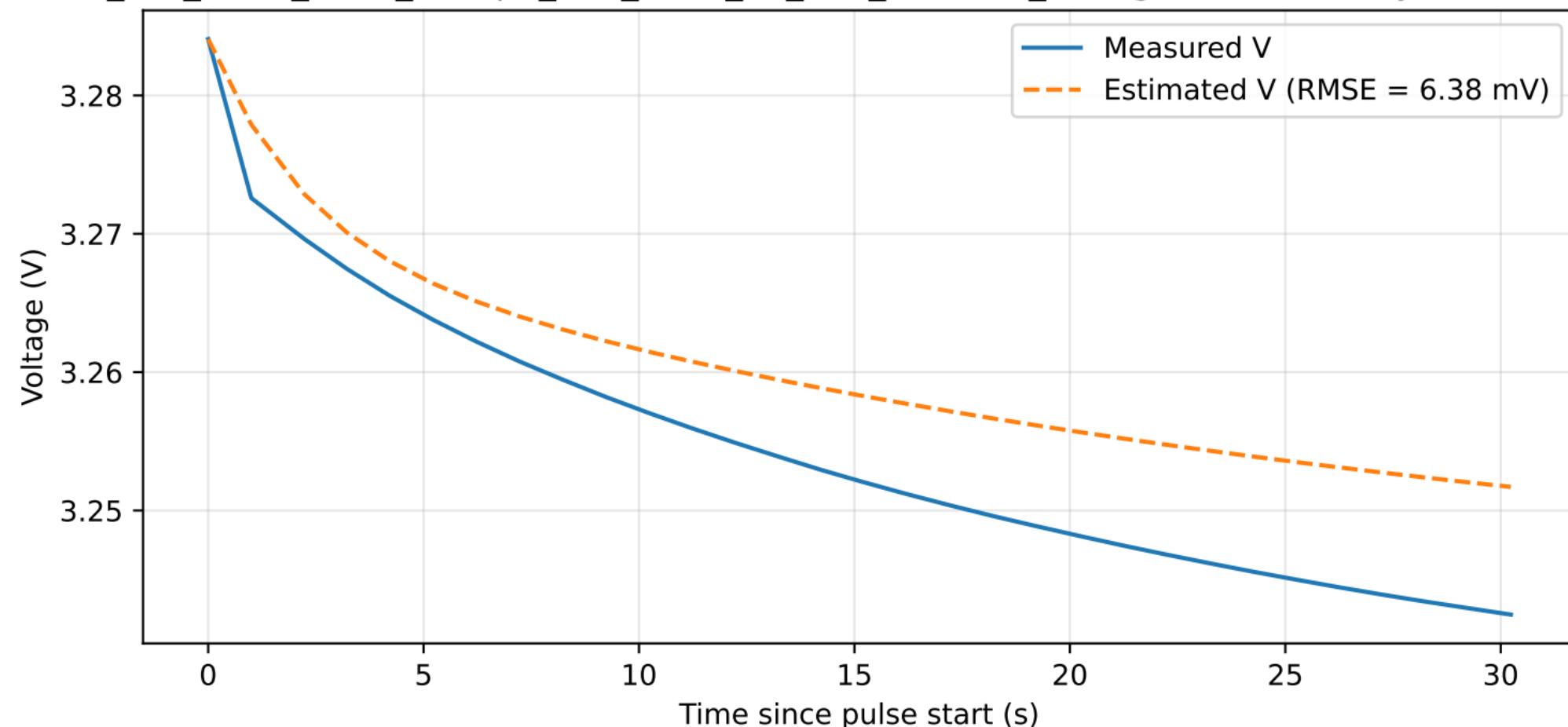
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



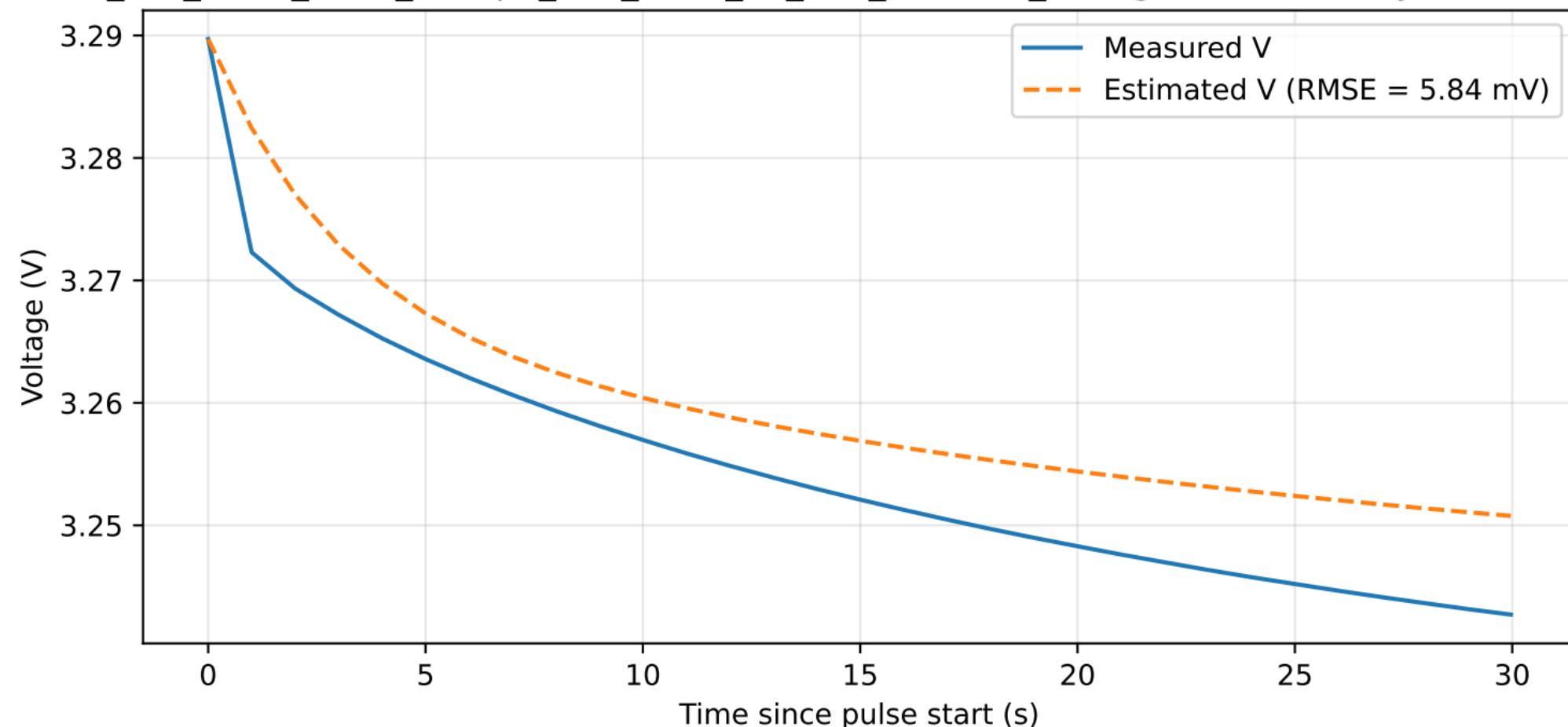
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



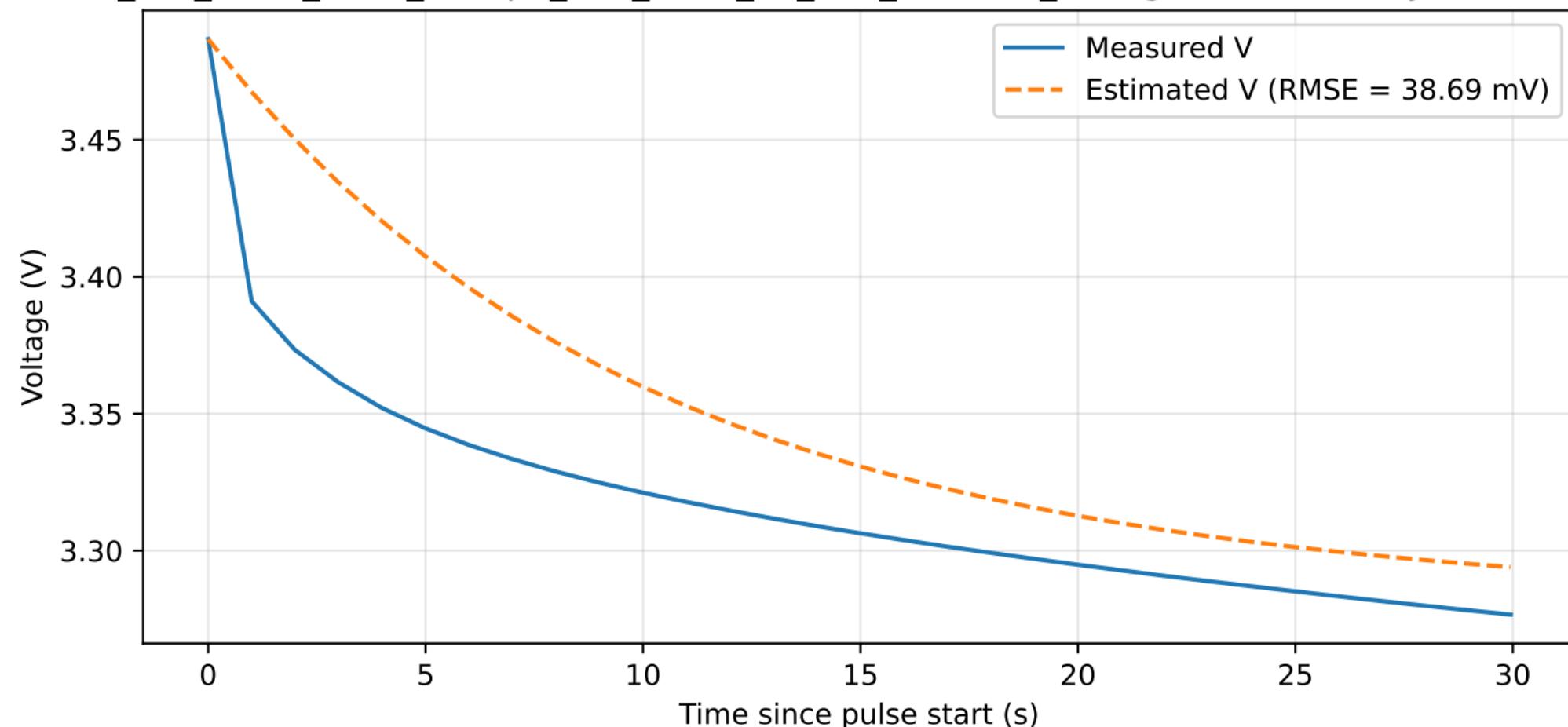
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



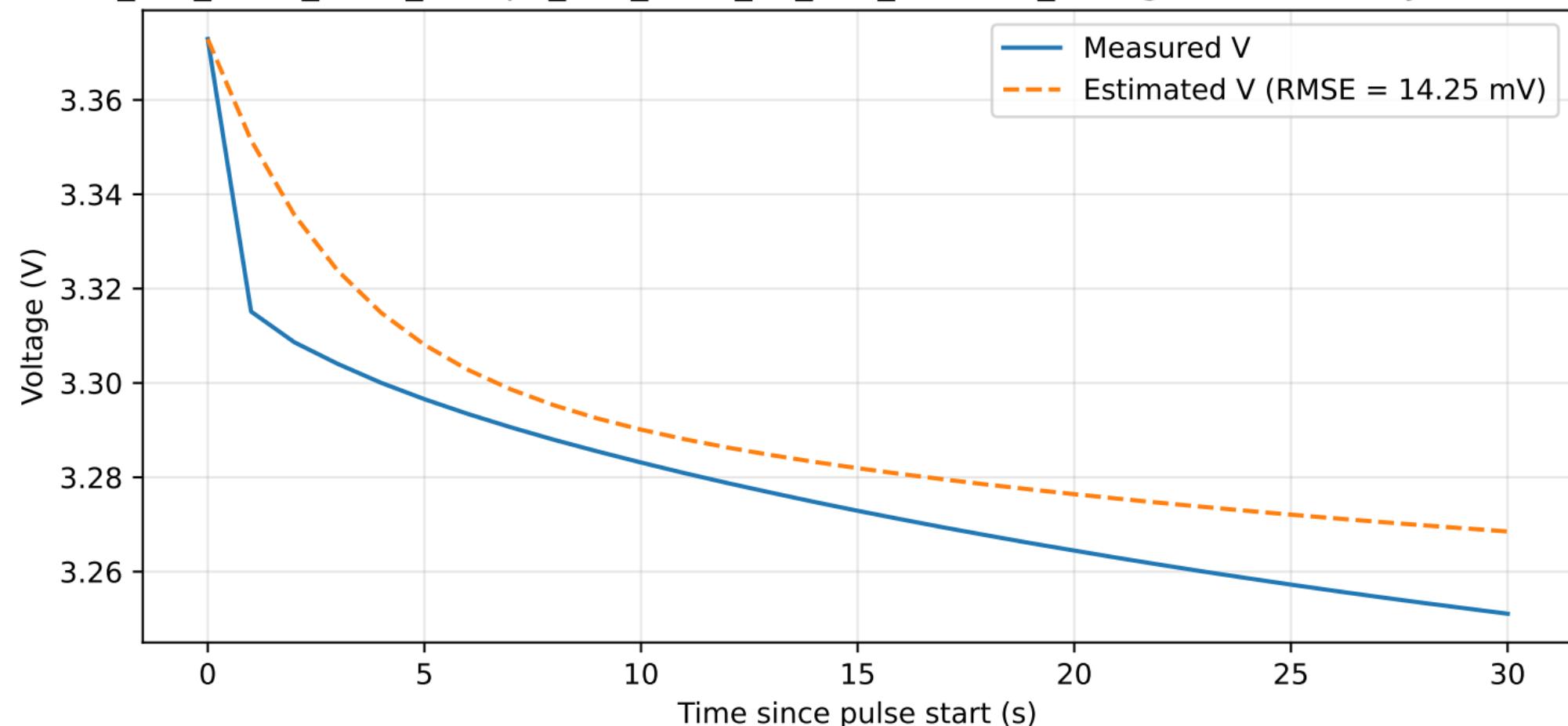
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0046\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



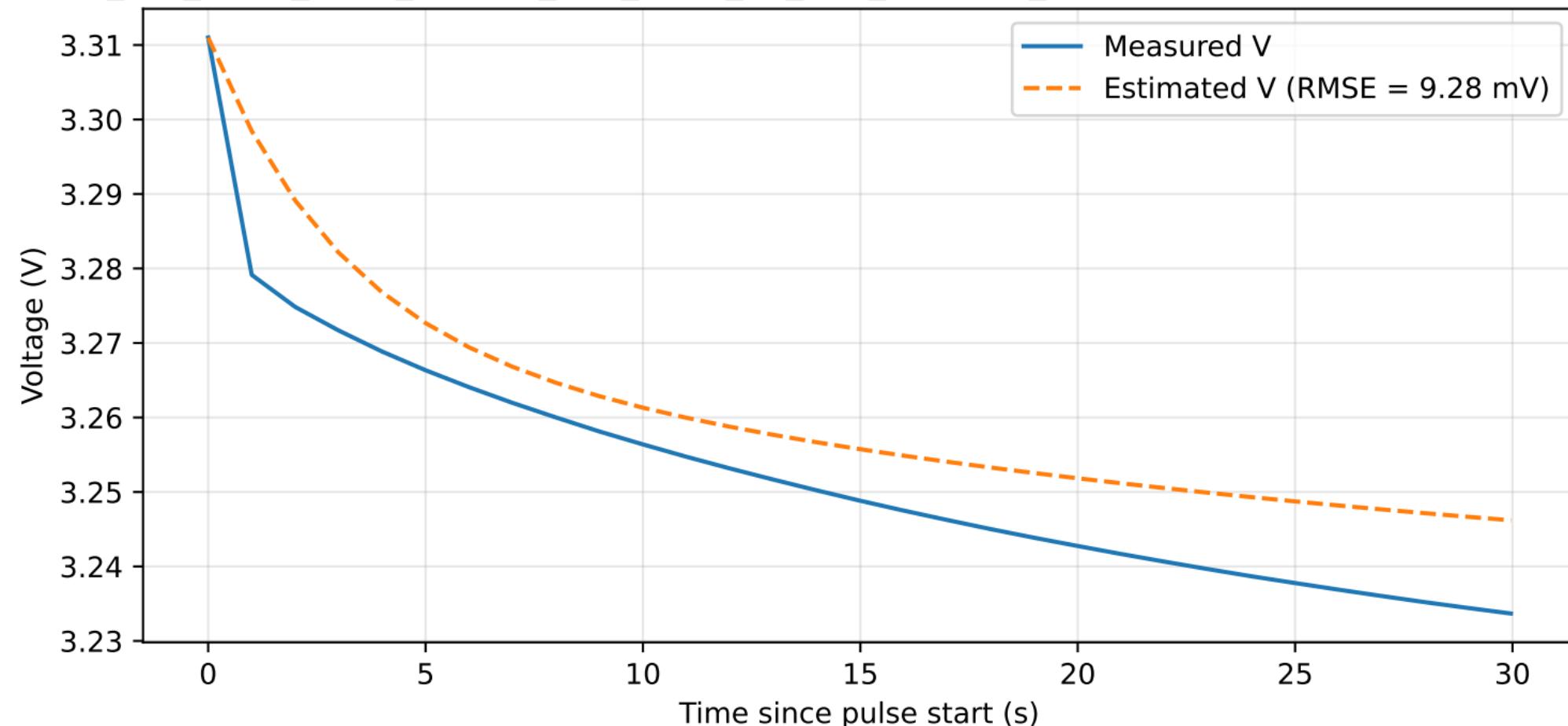
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



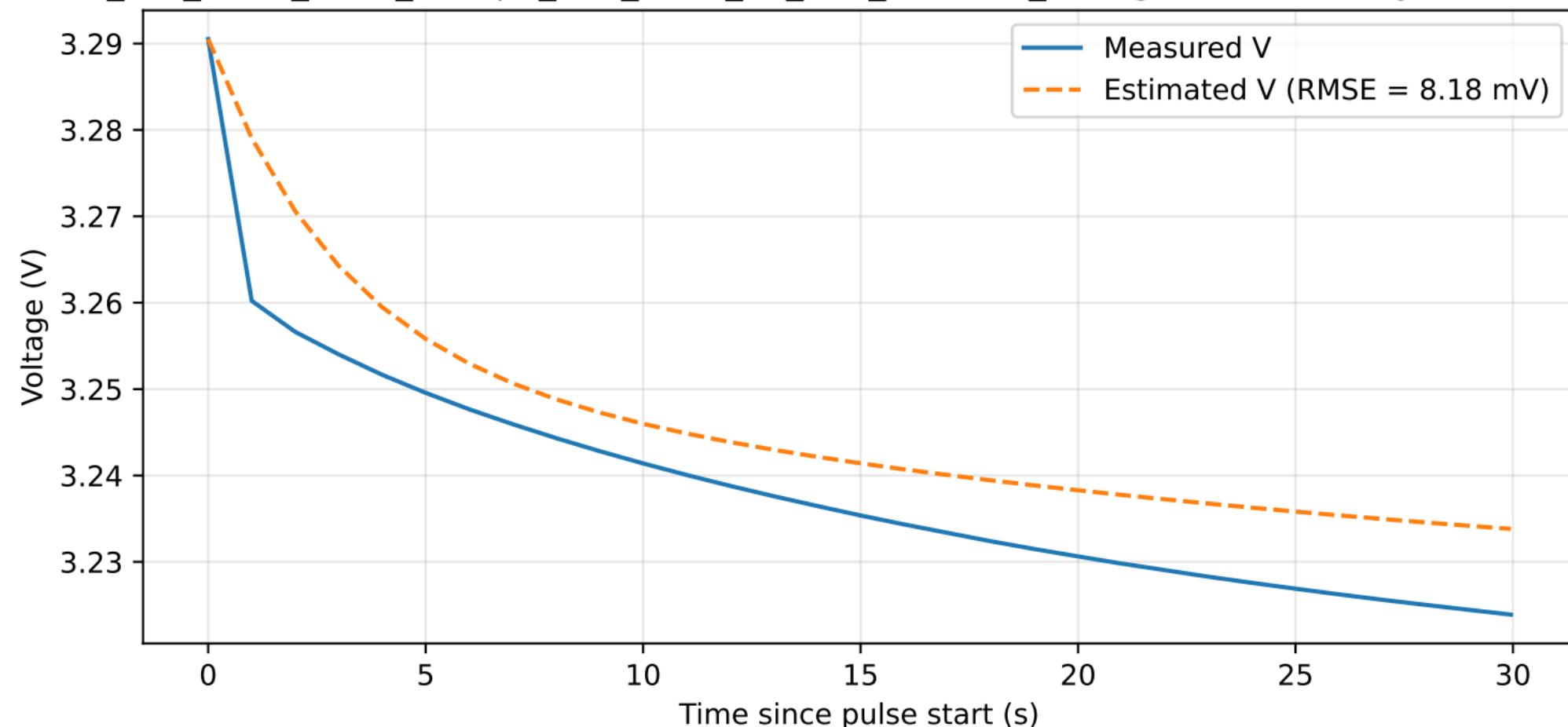
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



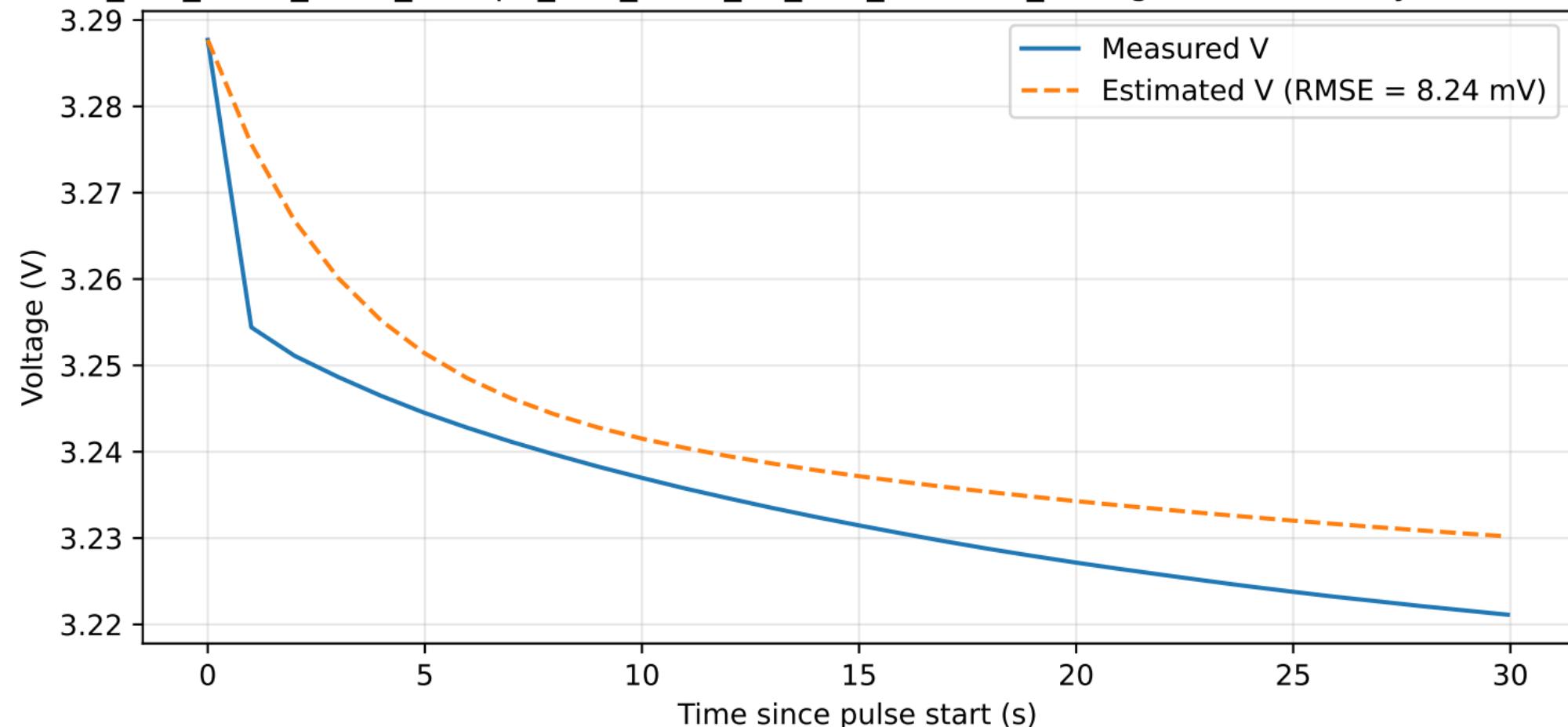
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



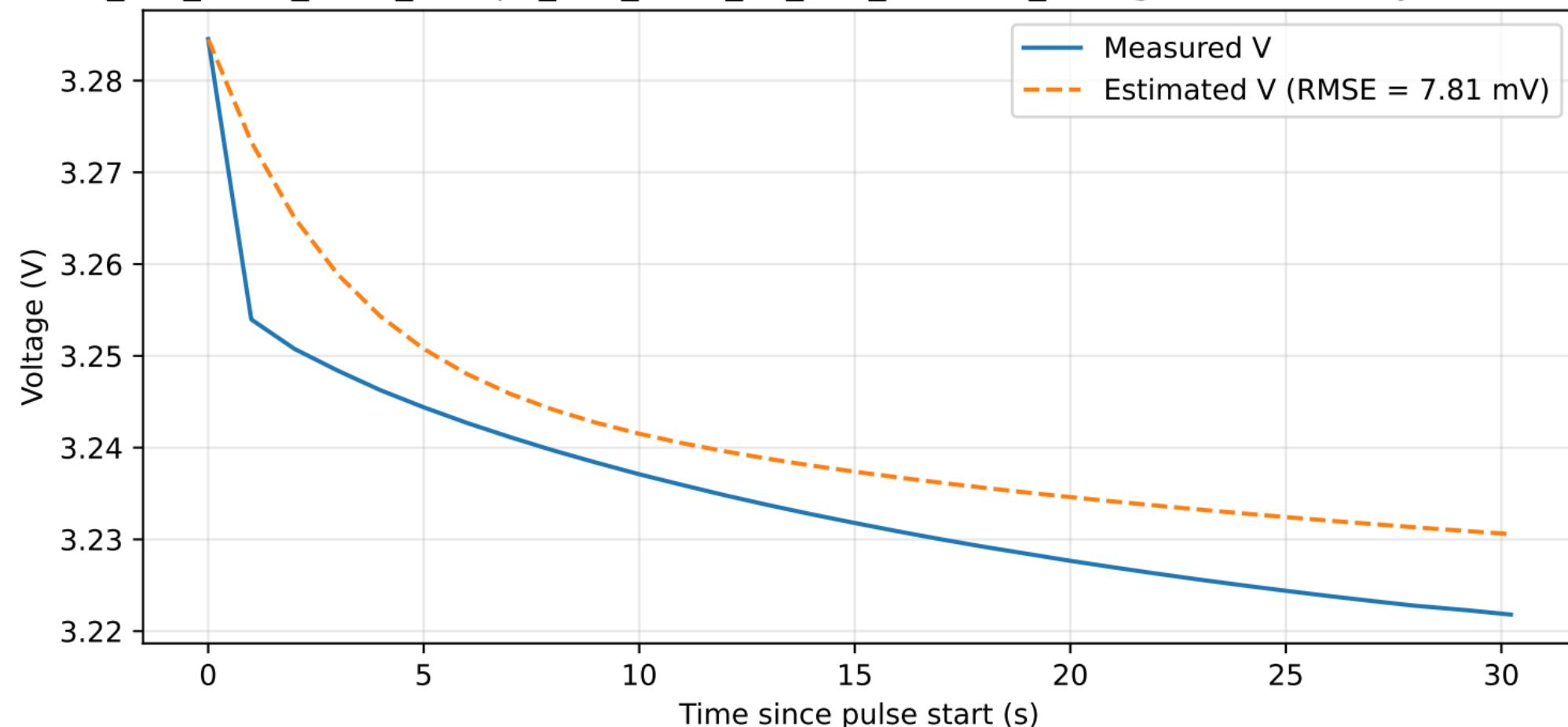
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



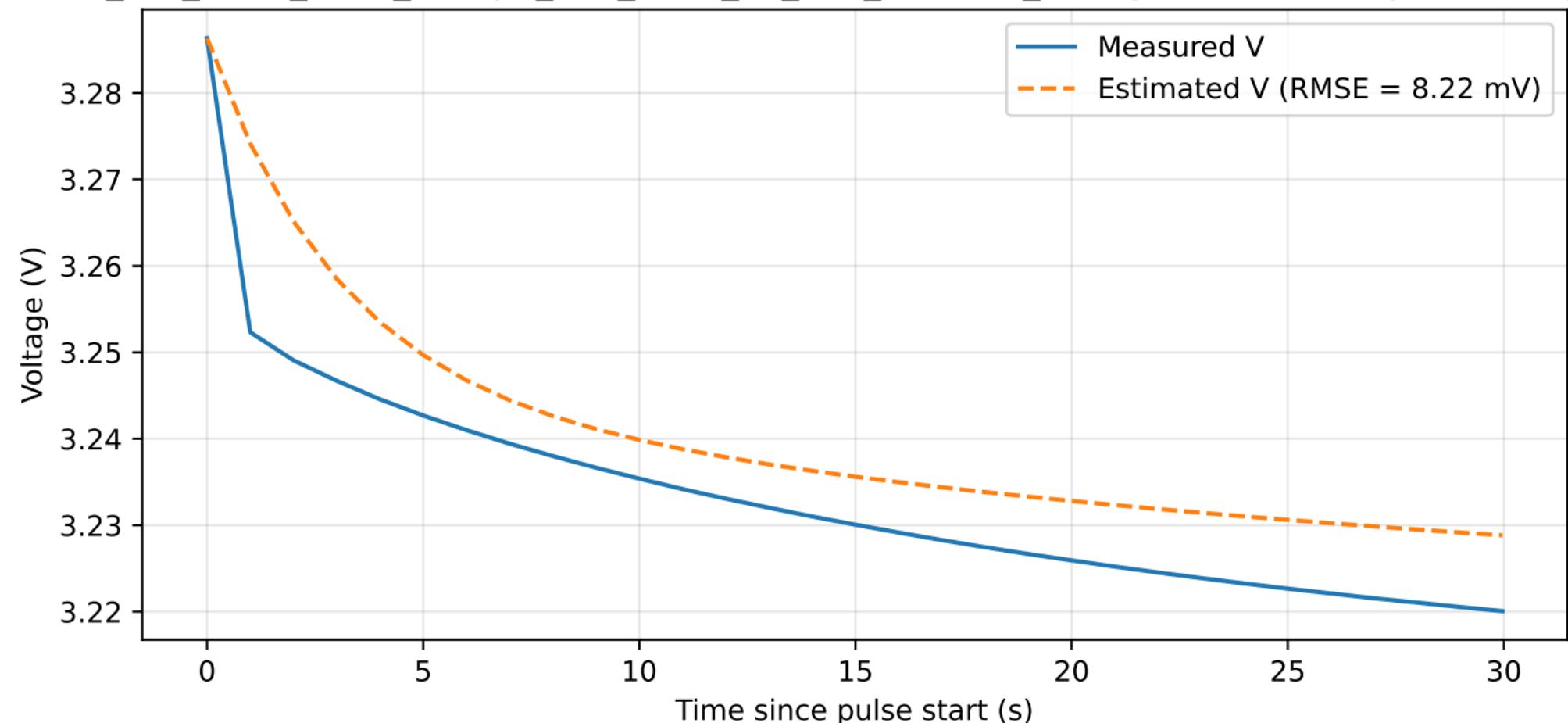
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



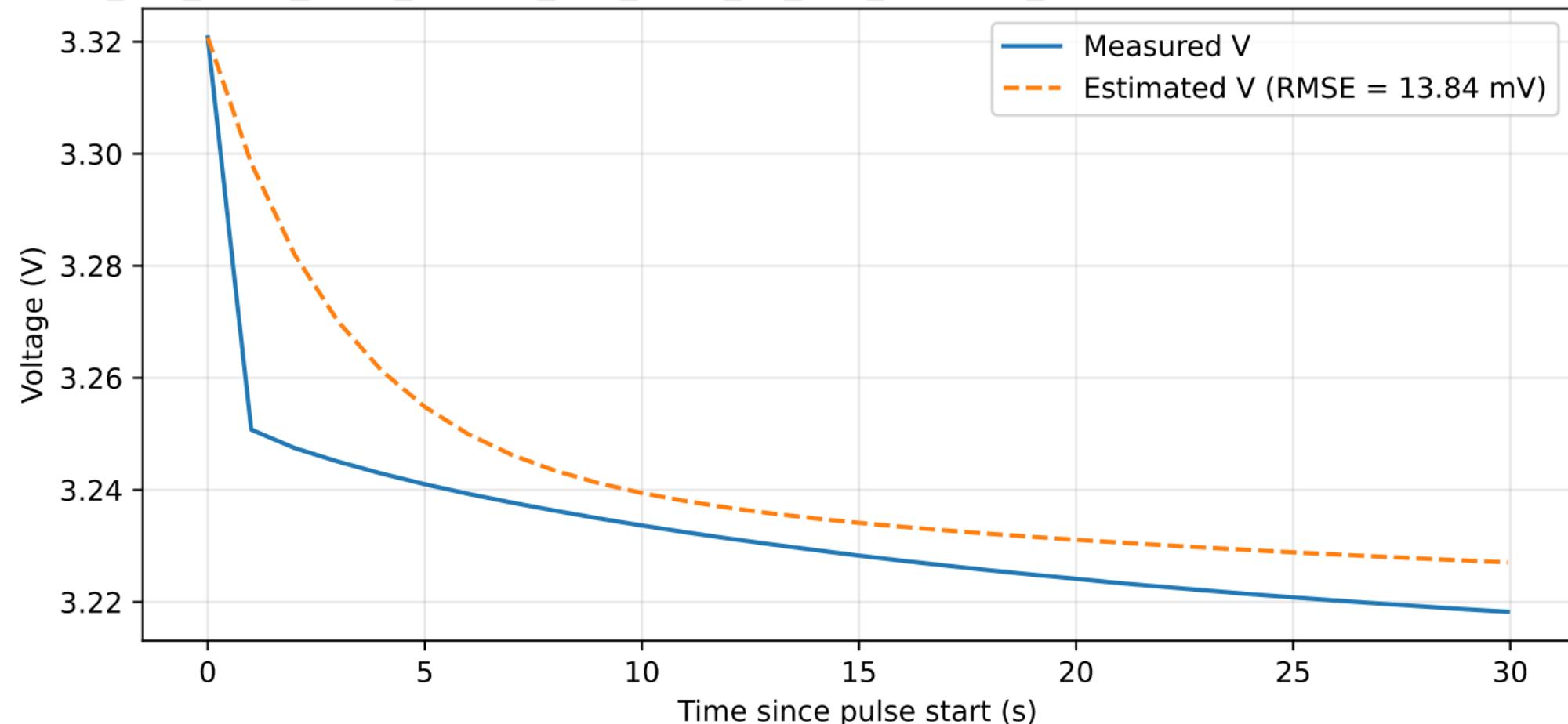
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



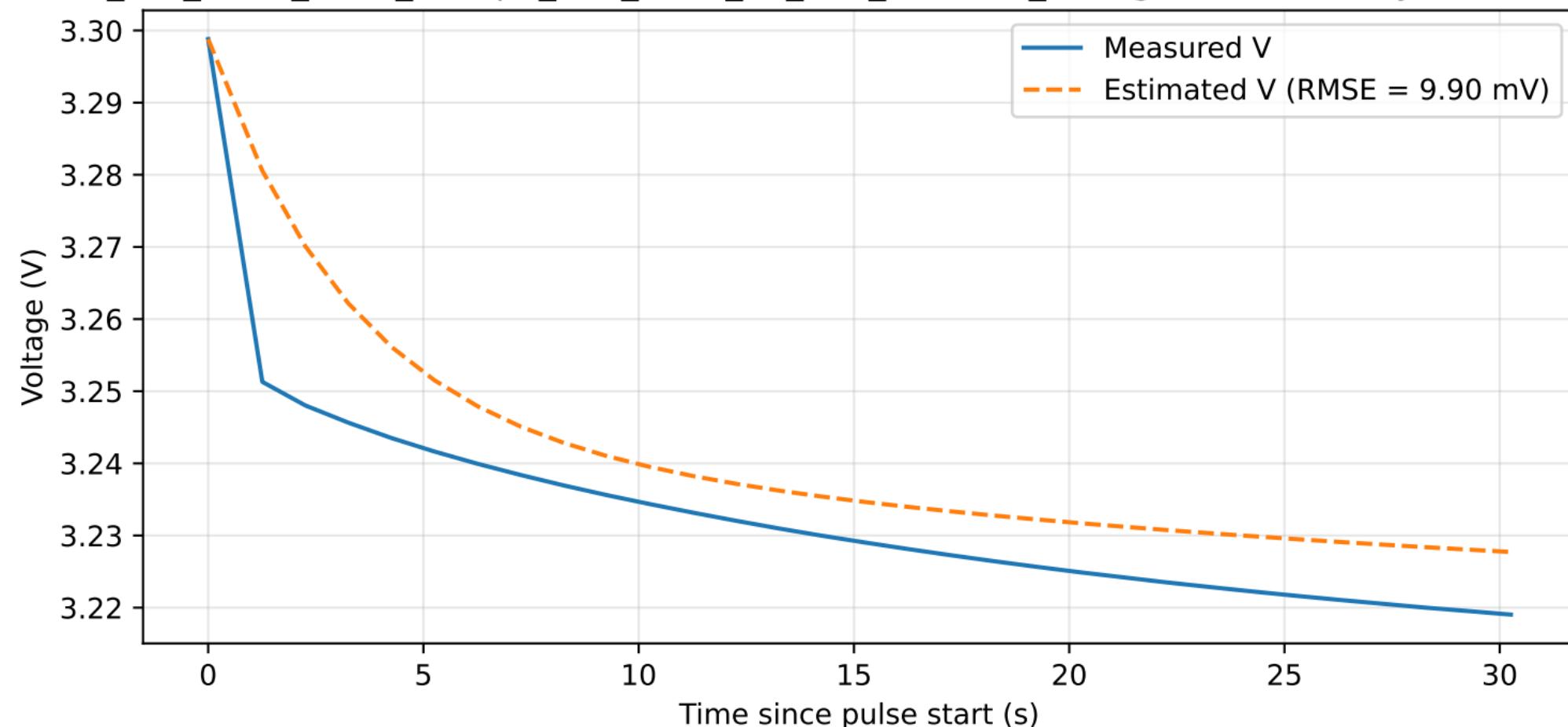
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



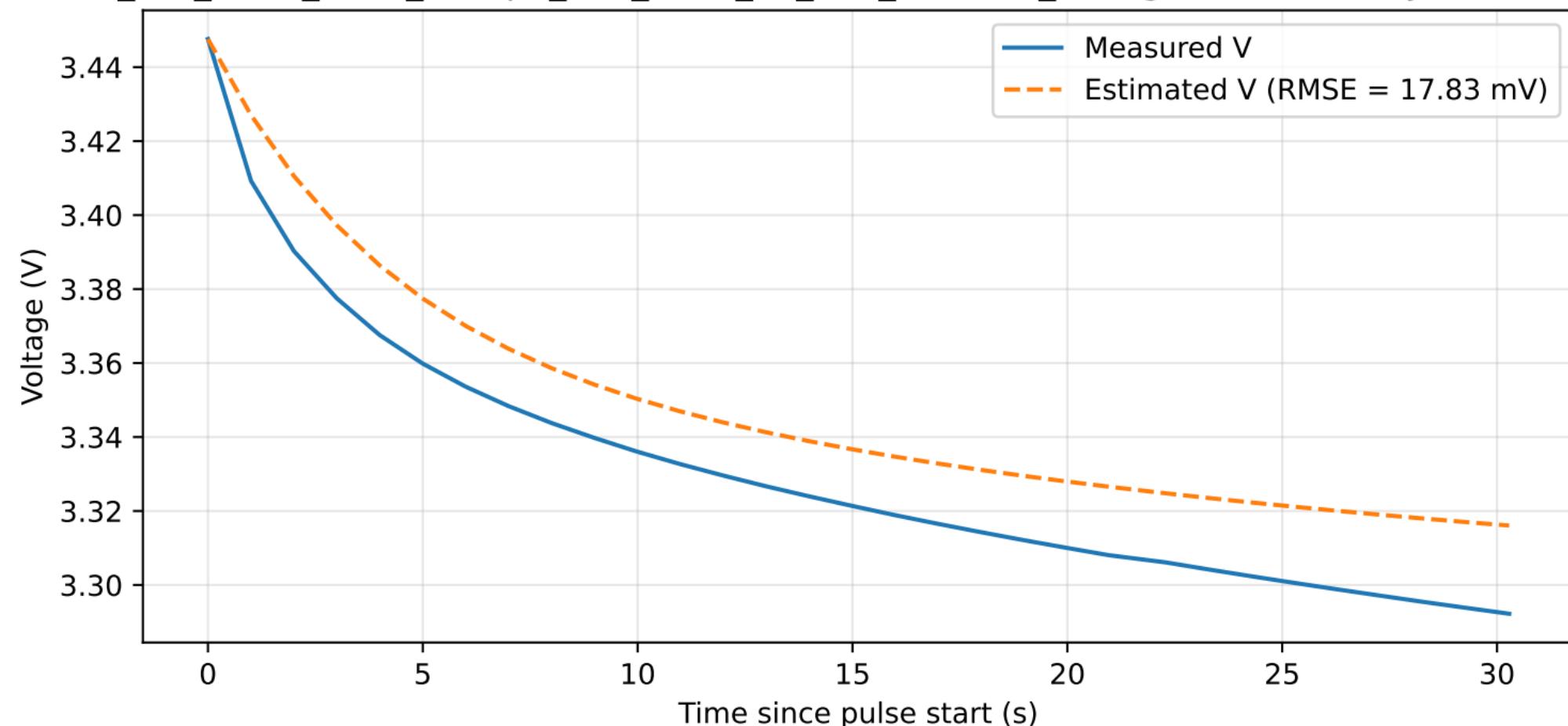
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



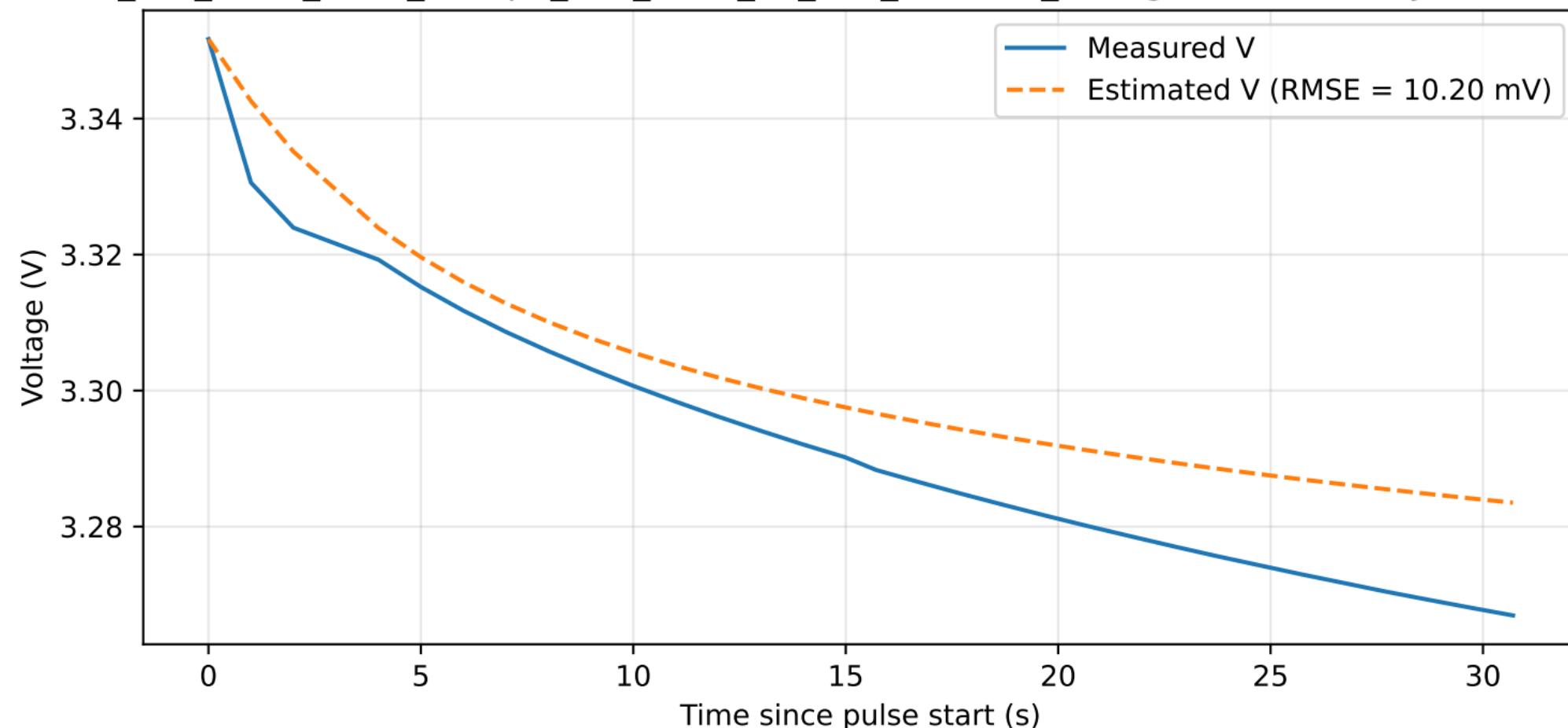
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0049\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



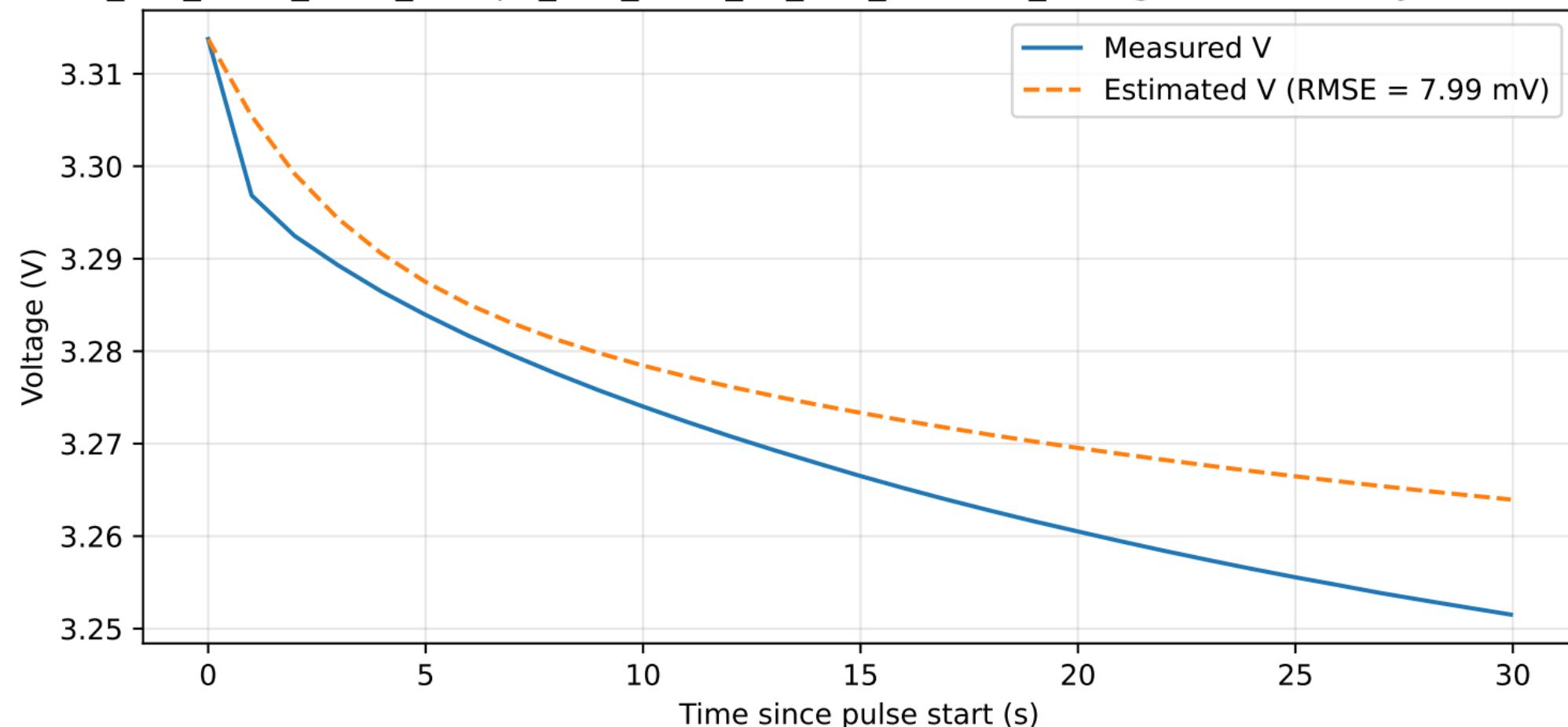
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



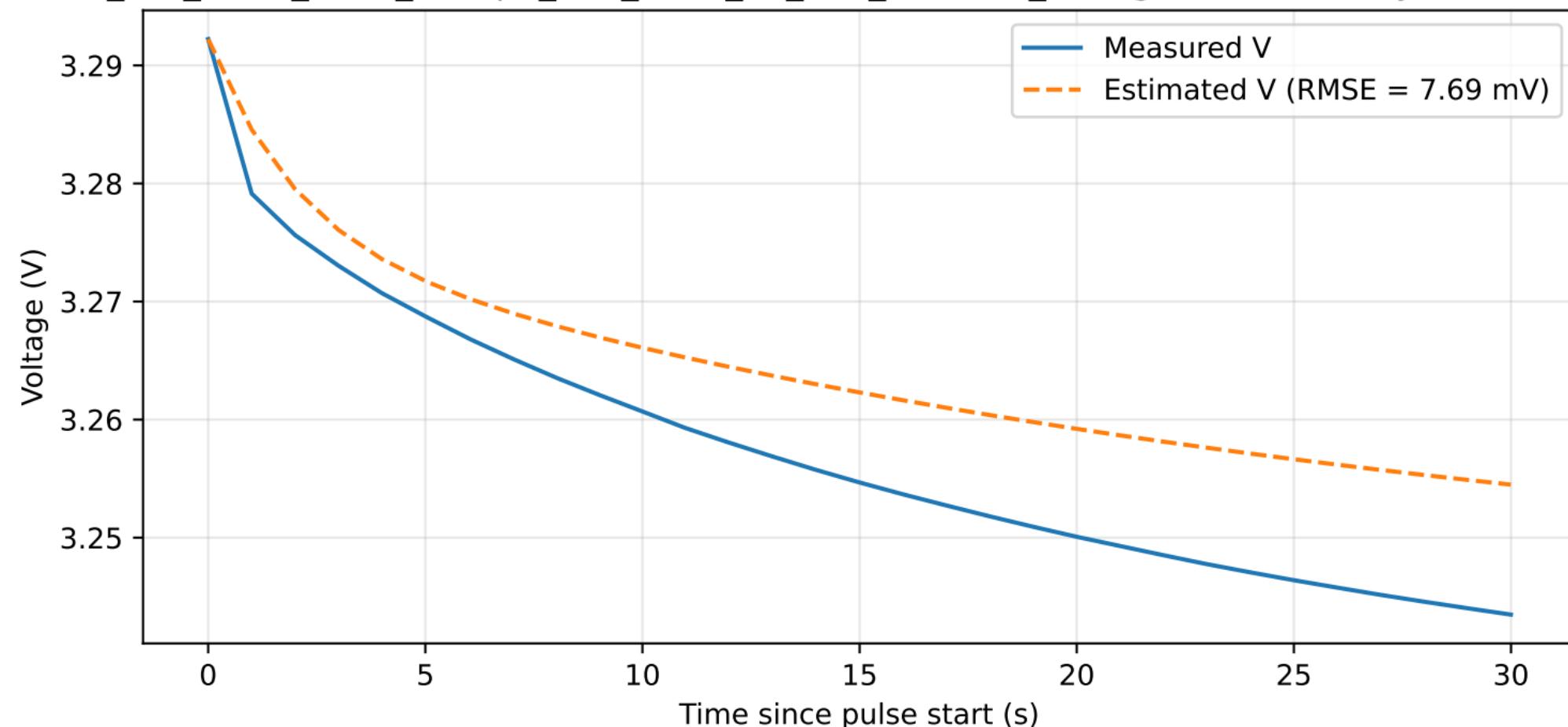
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



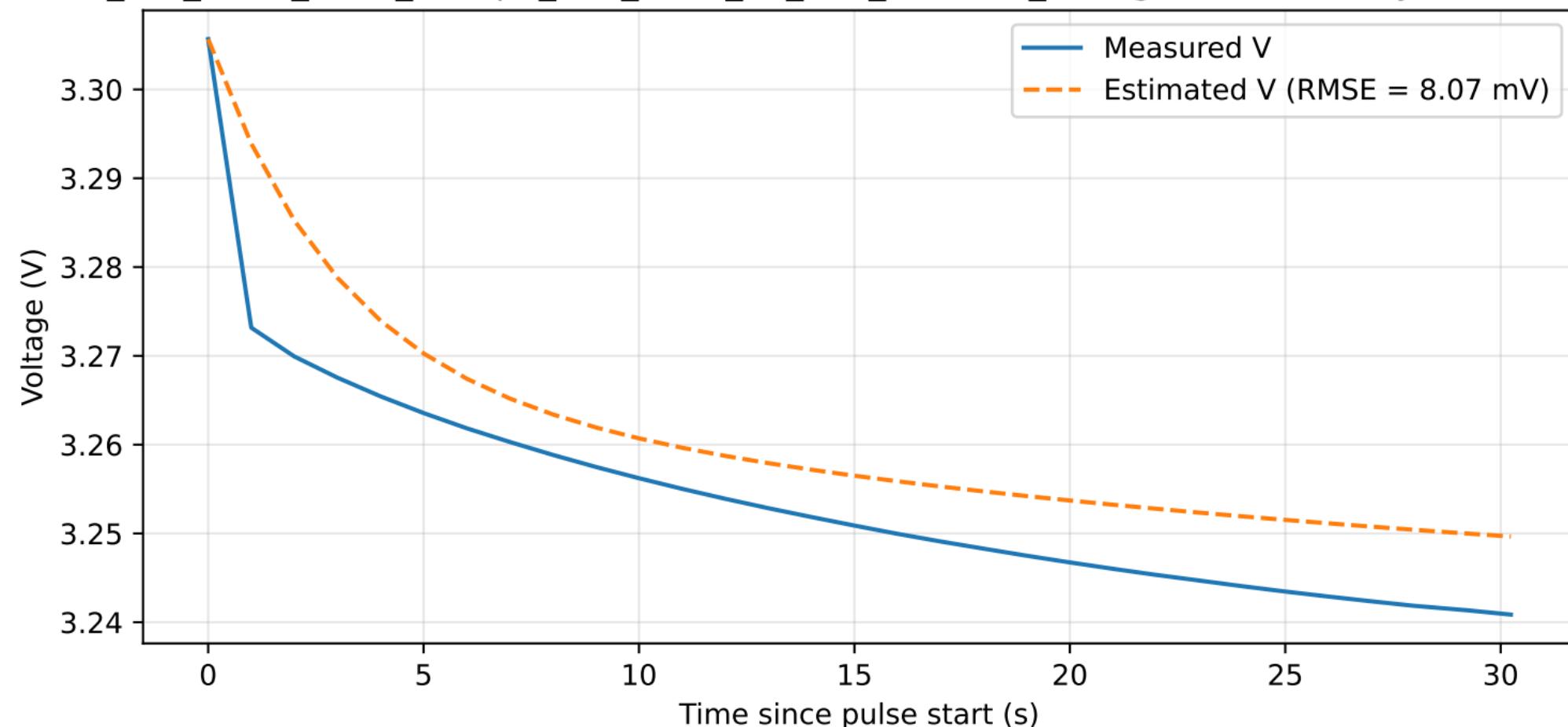
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



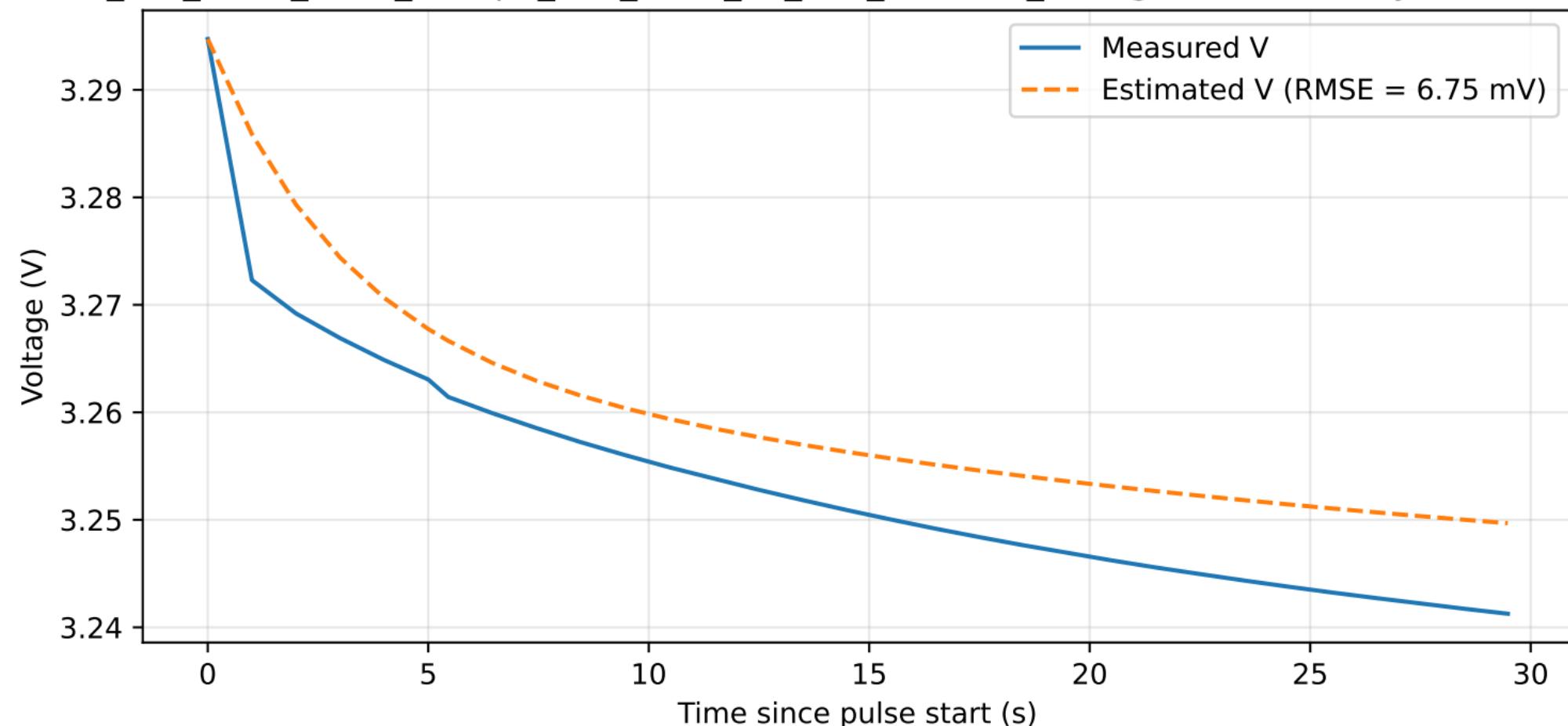
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



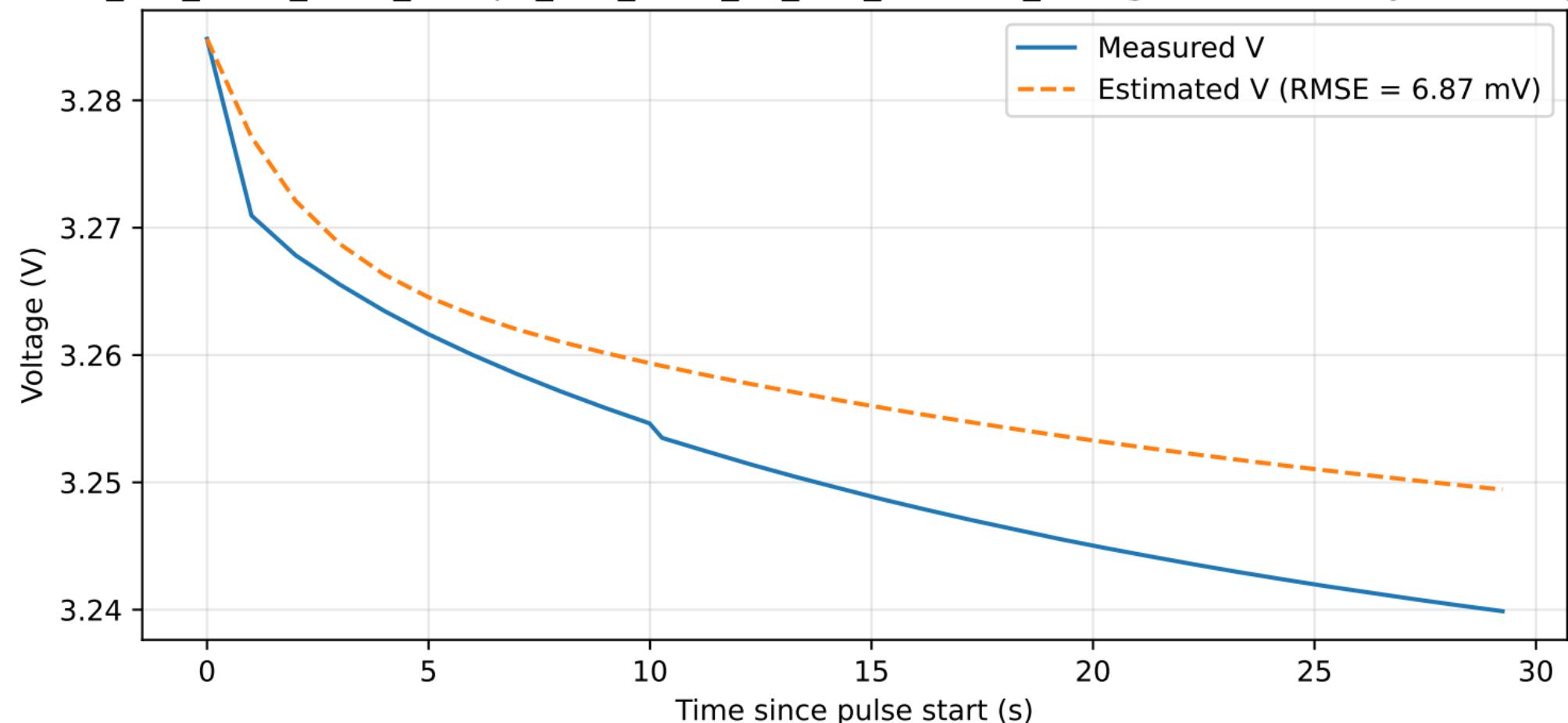
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



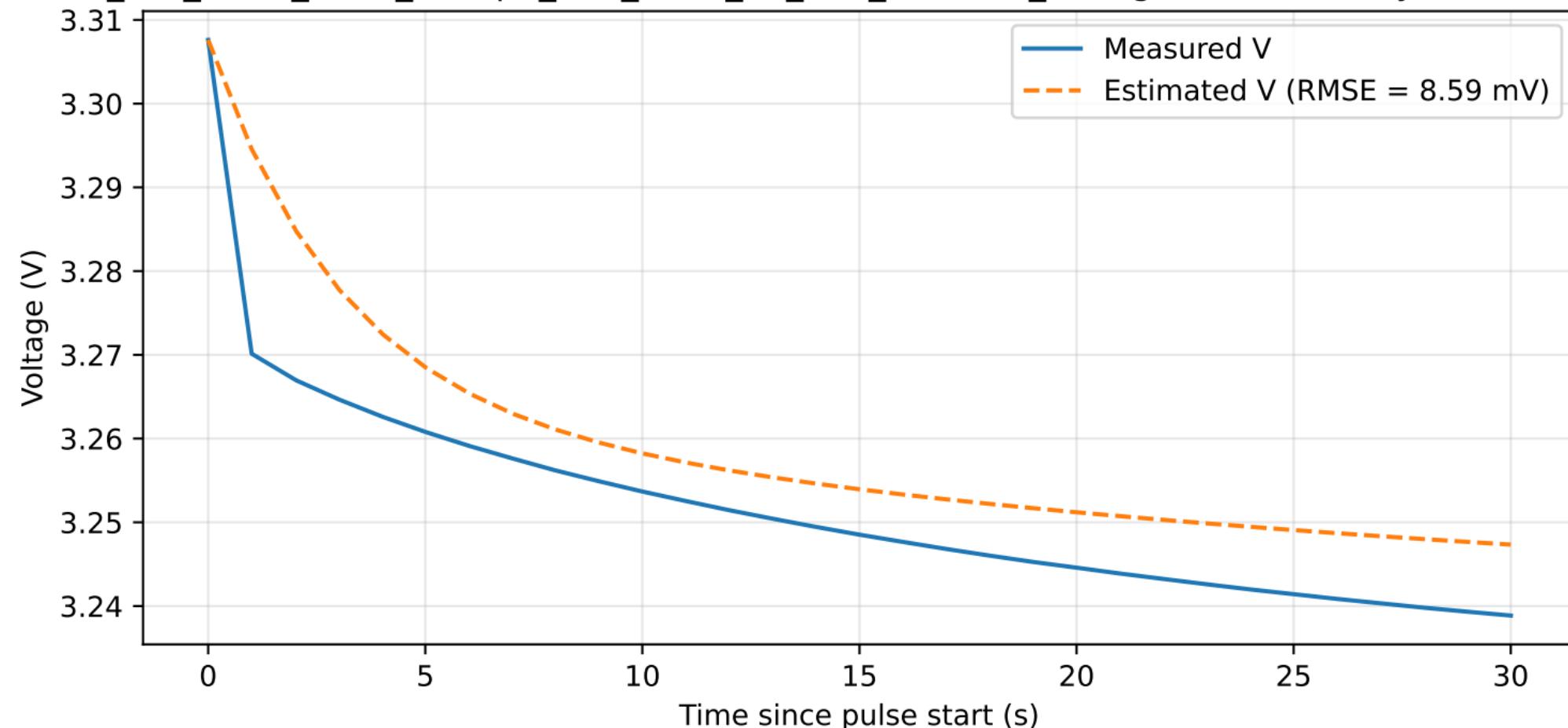
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



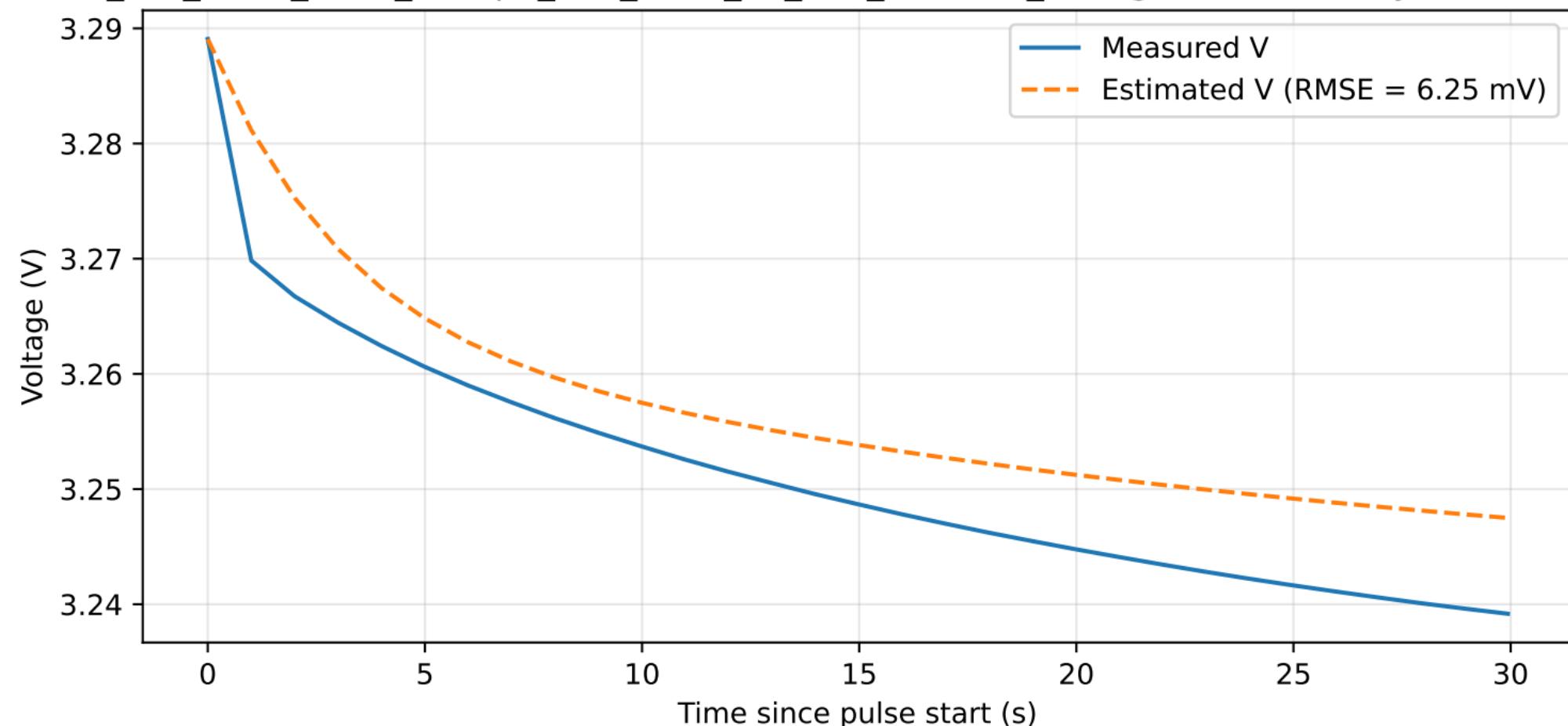
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



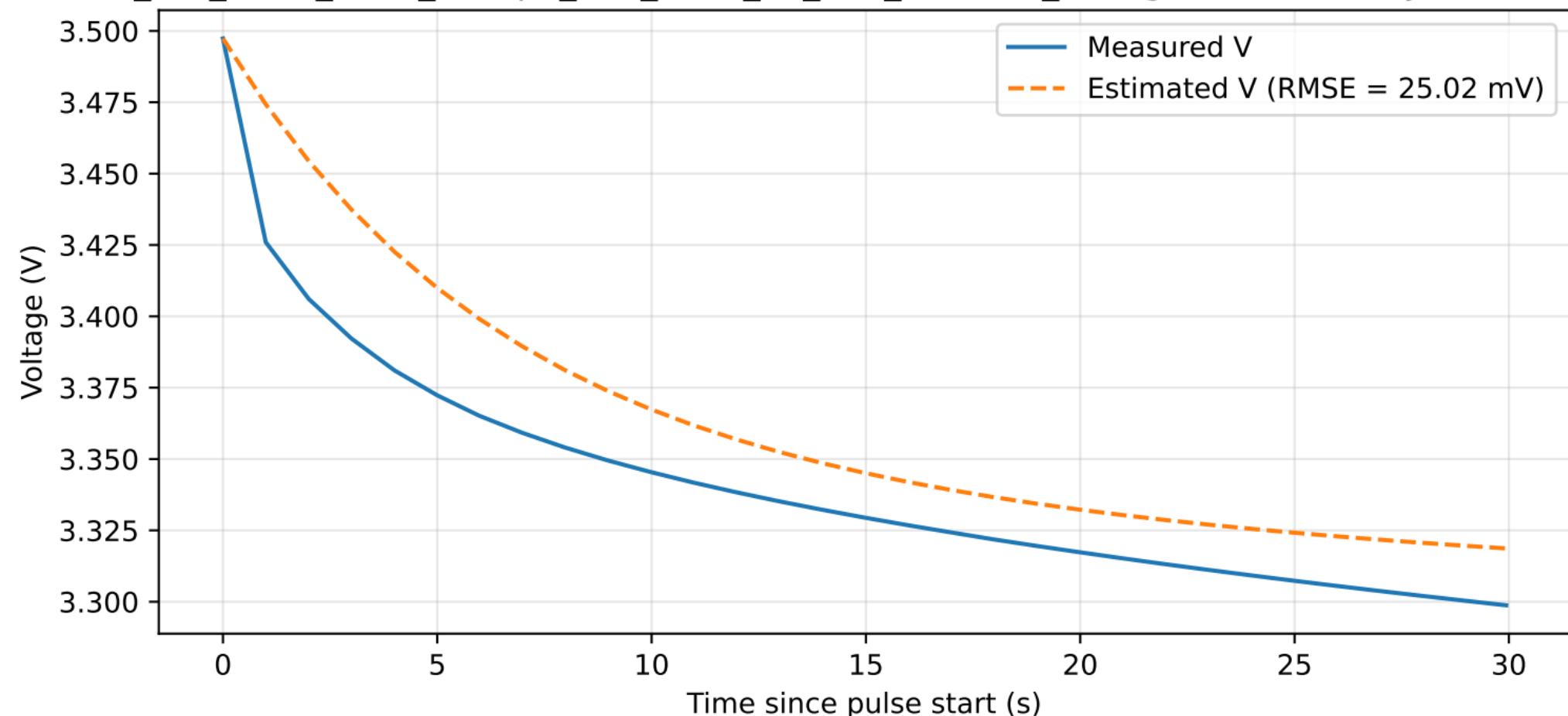
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



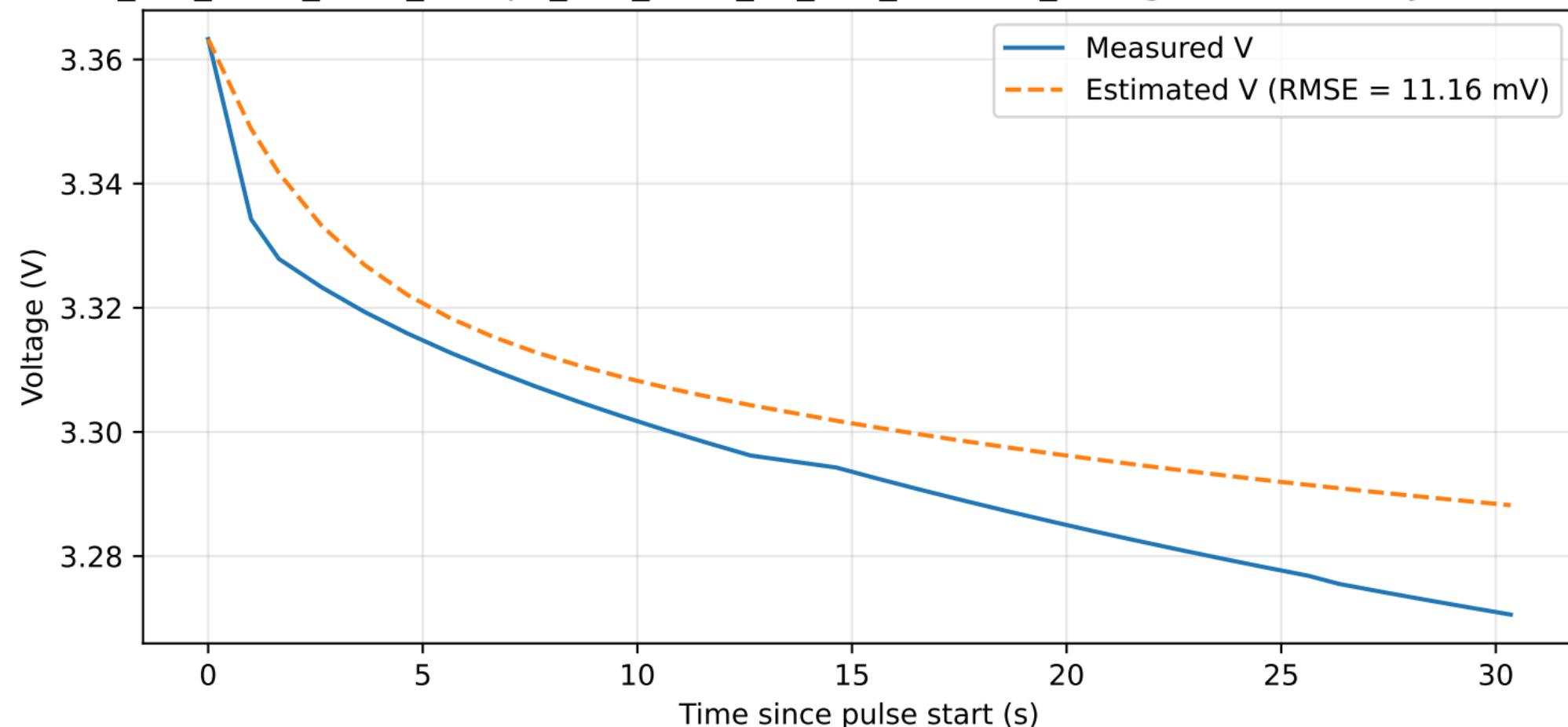
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0056\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



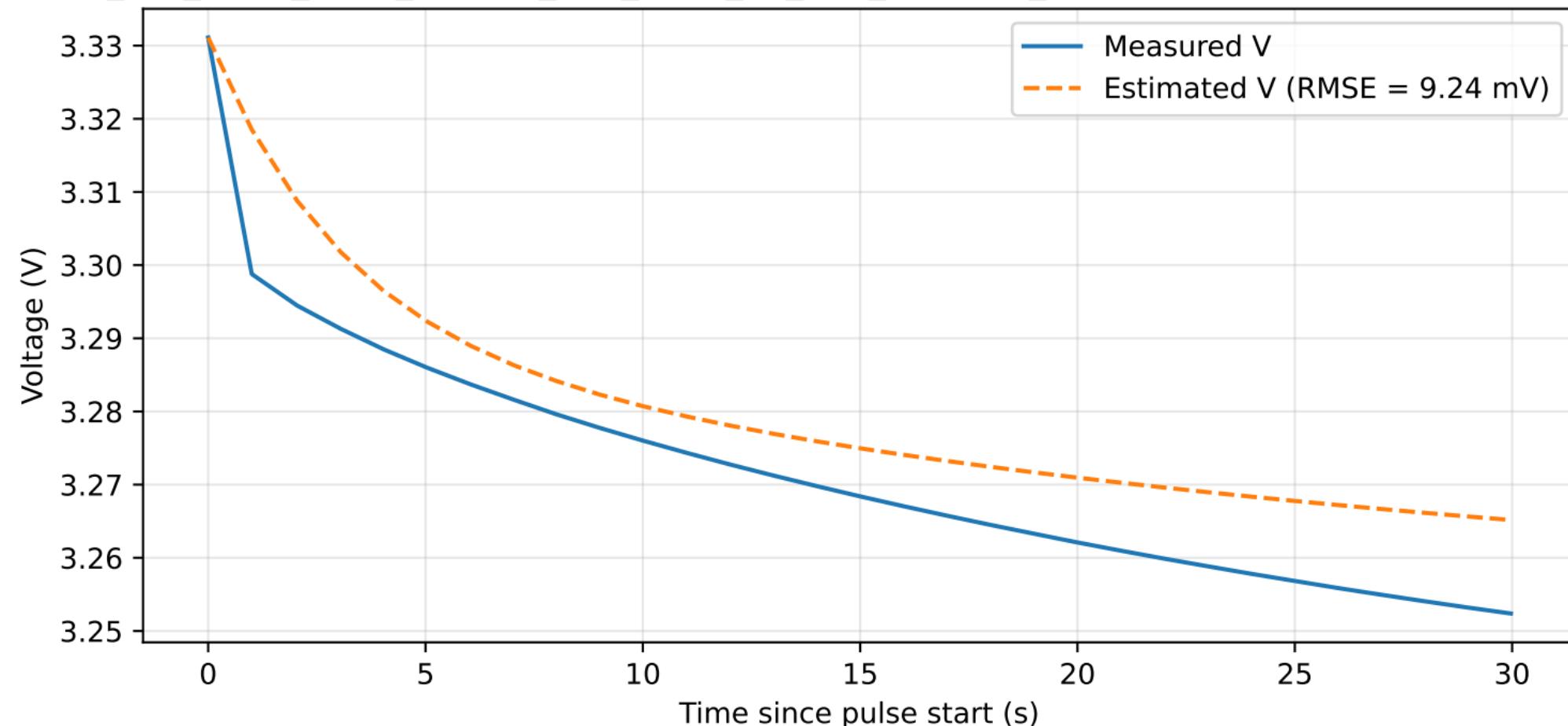
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



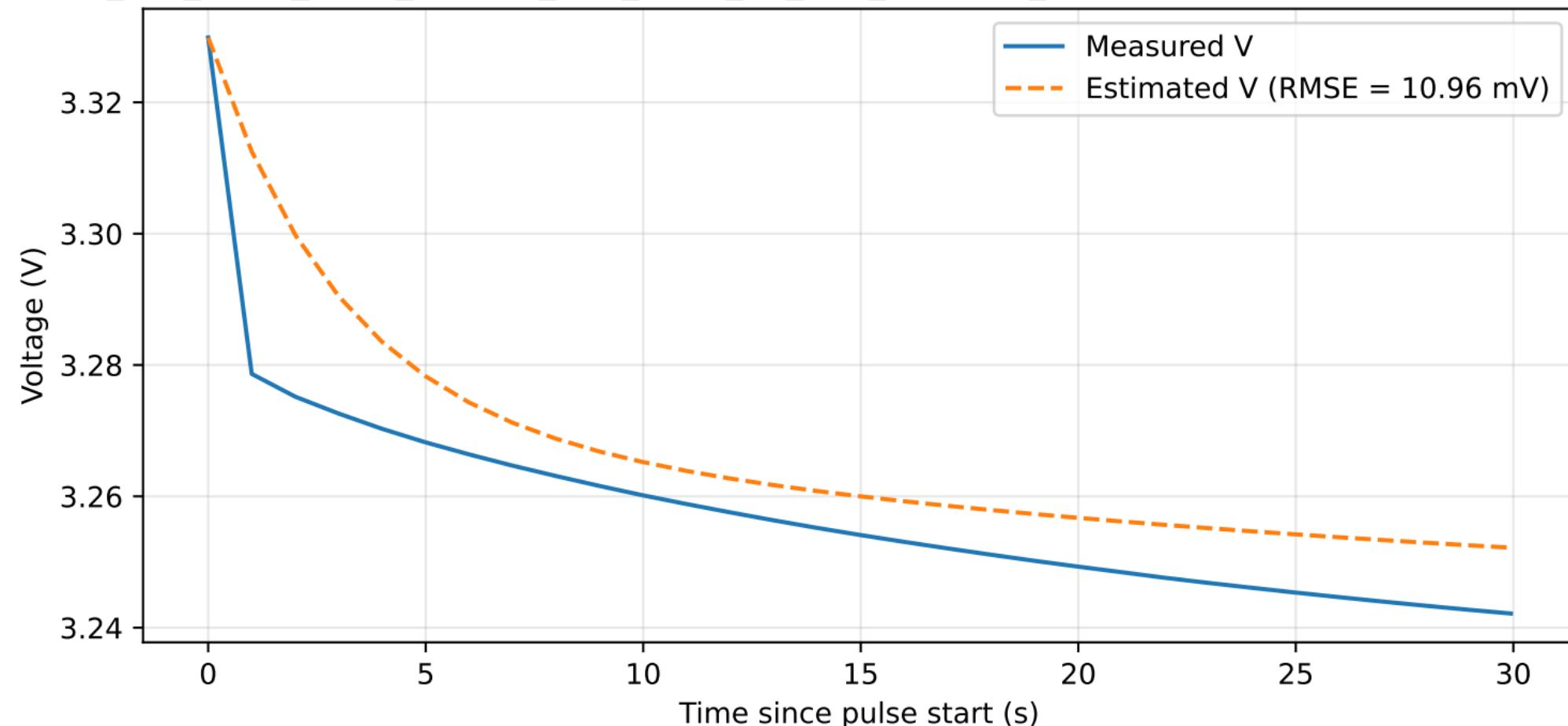
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



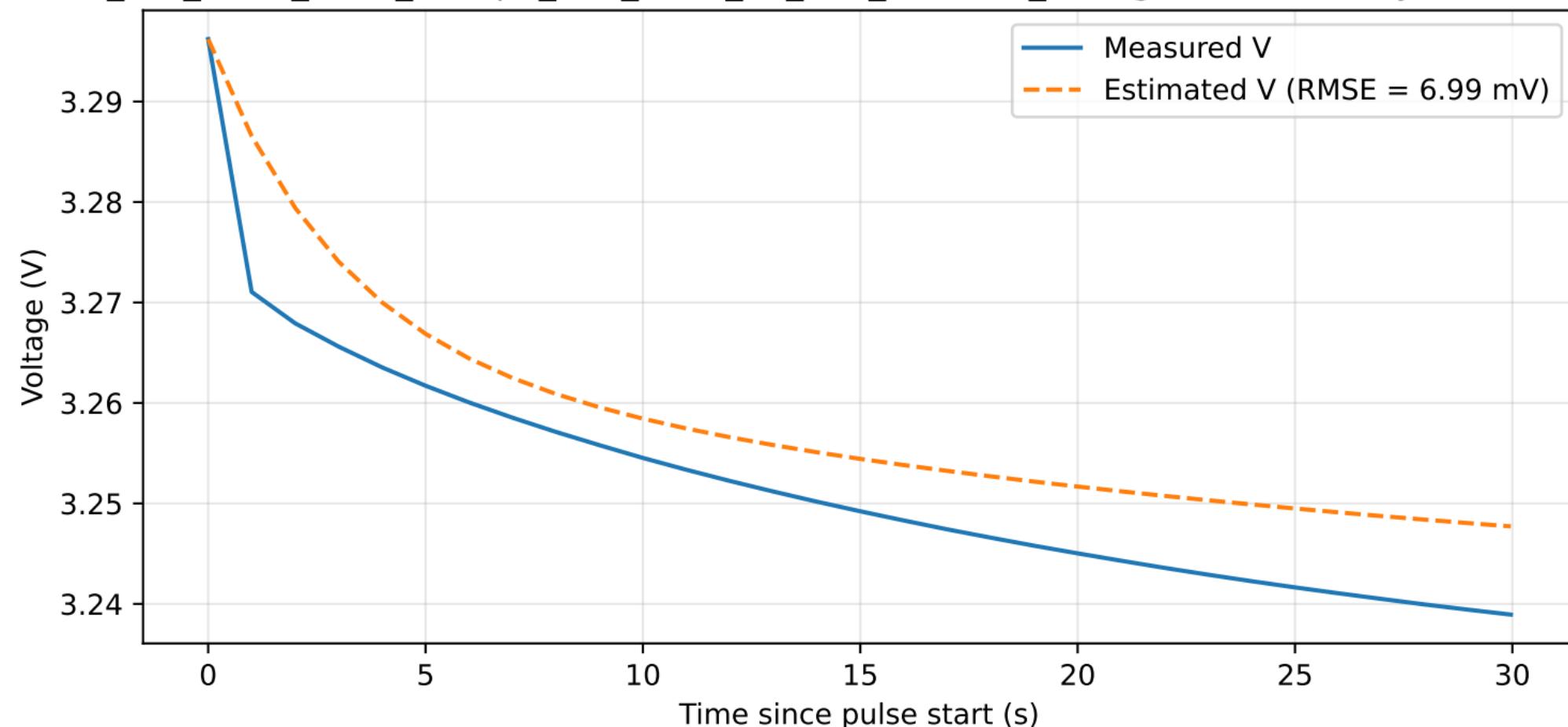
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



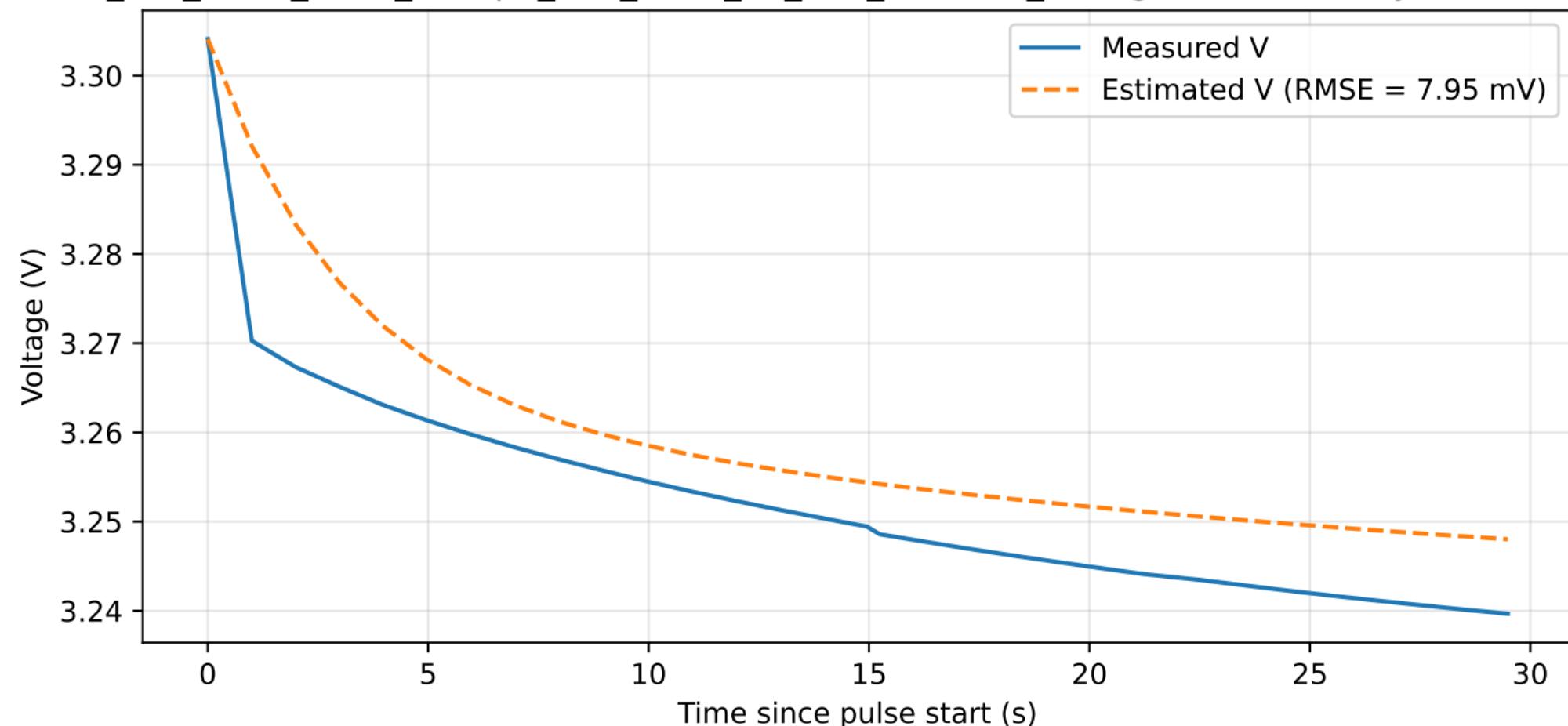
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



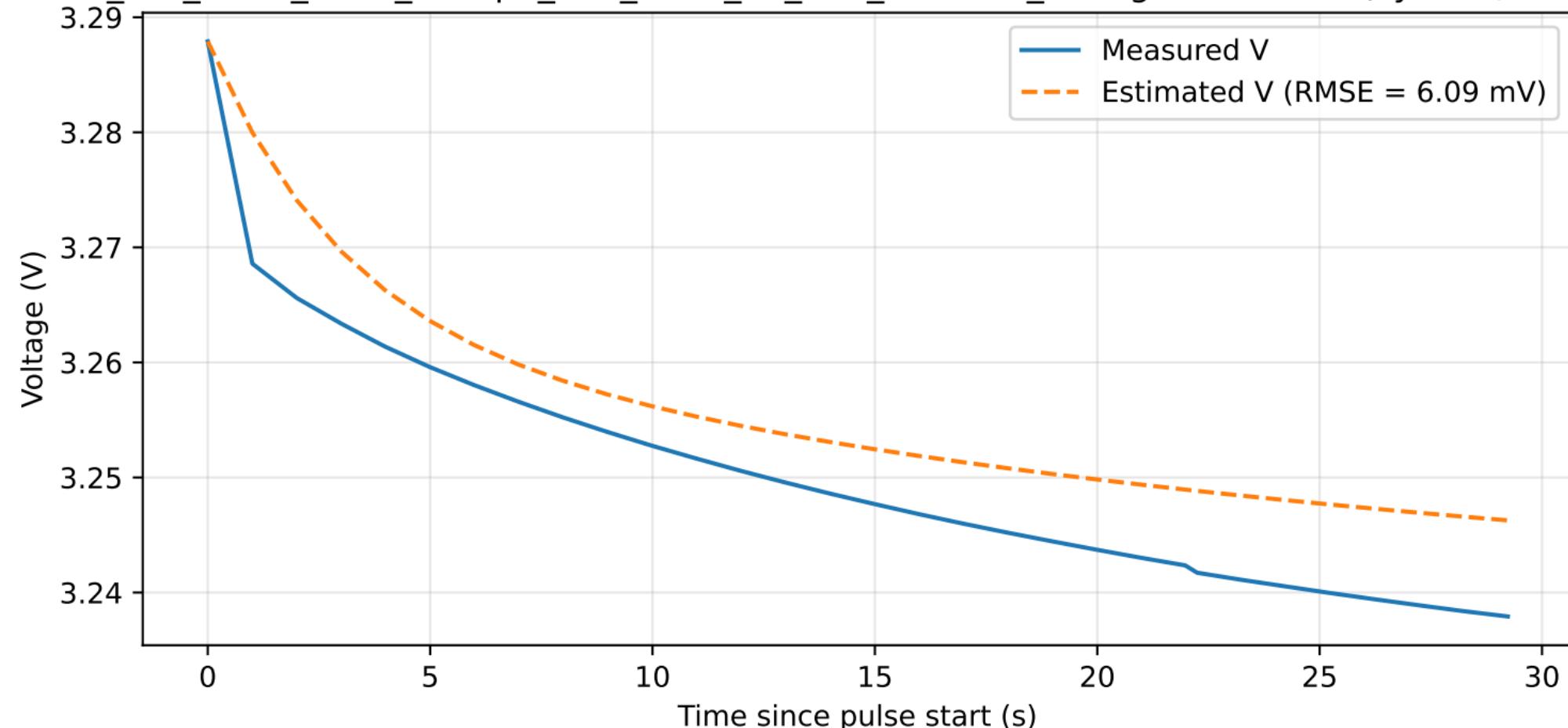
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



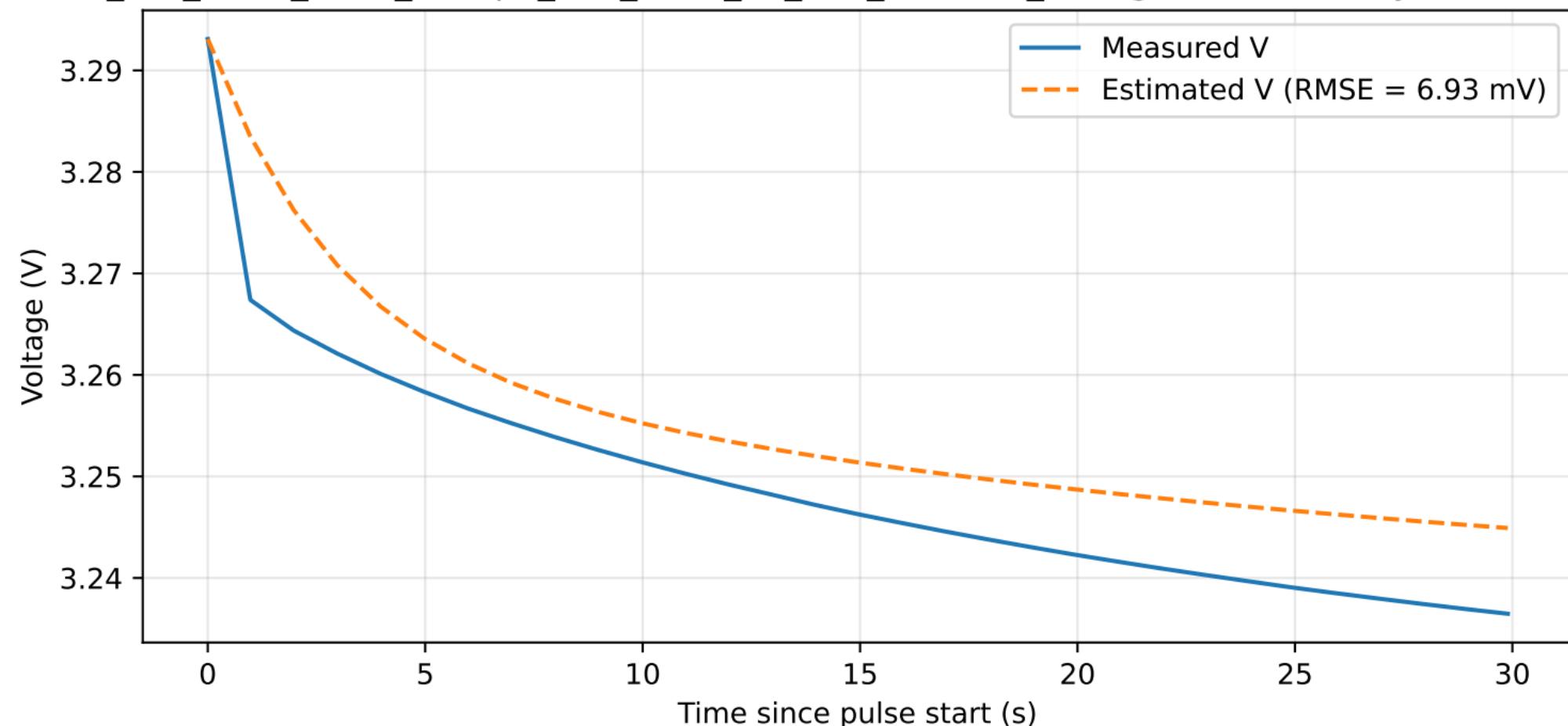
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



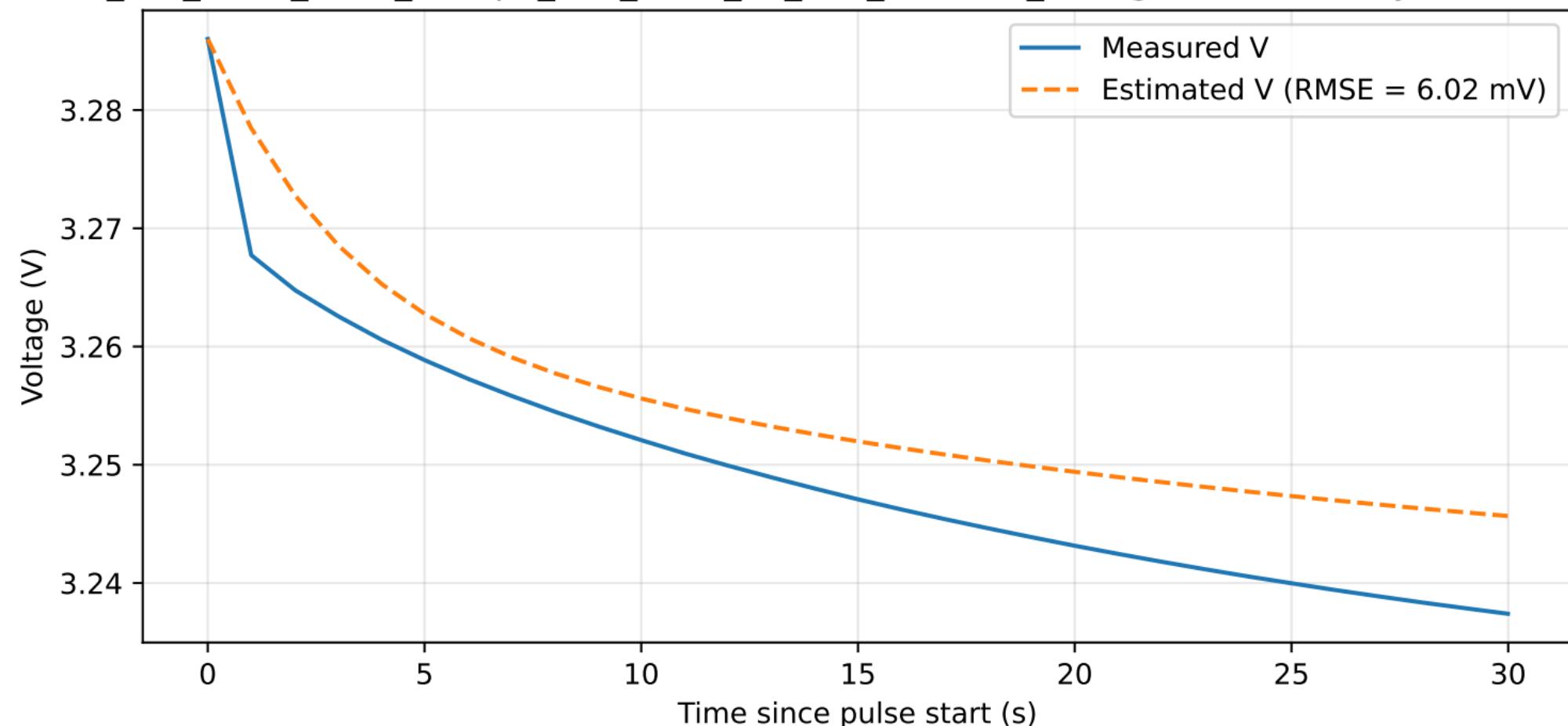
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



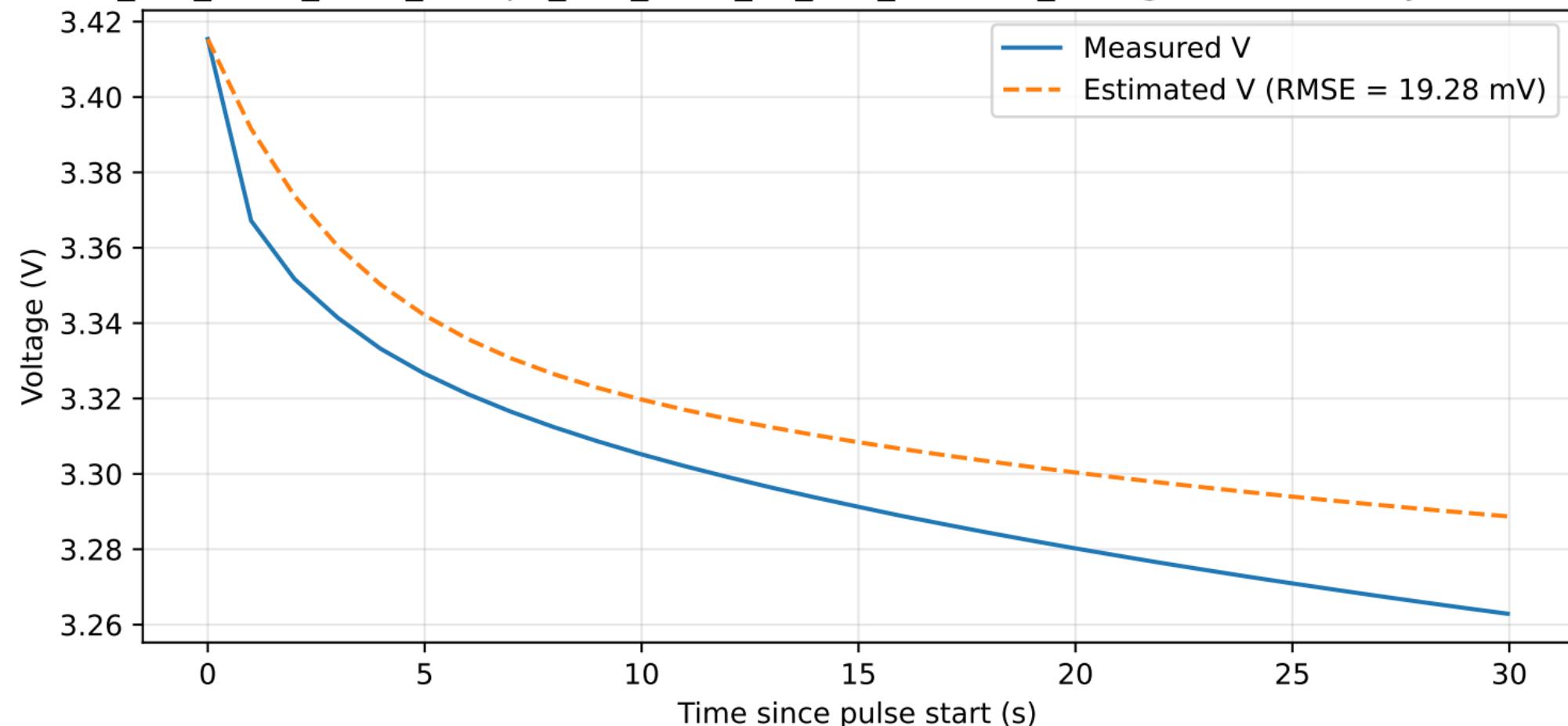
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



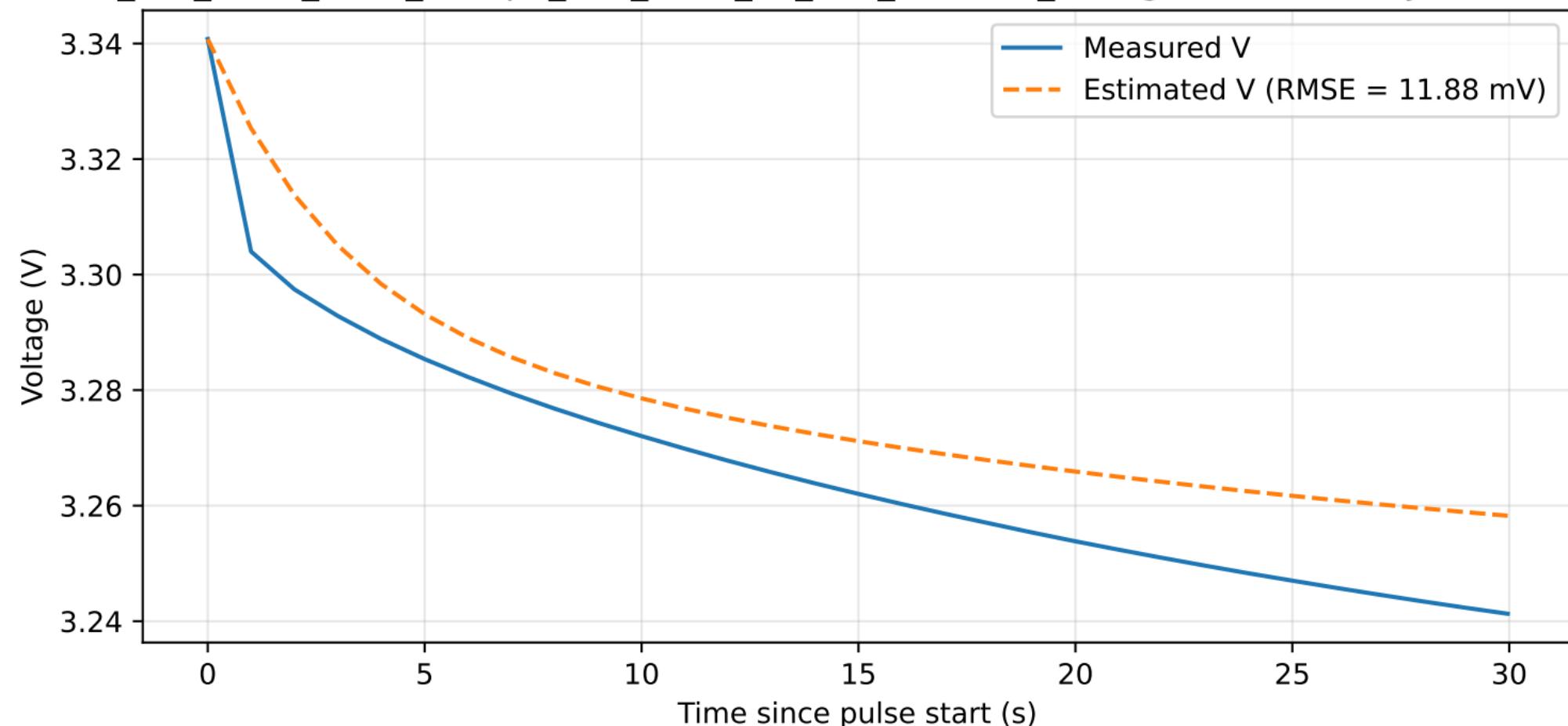
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0087\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



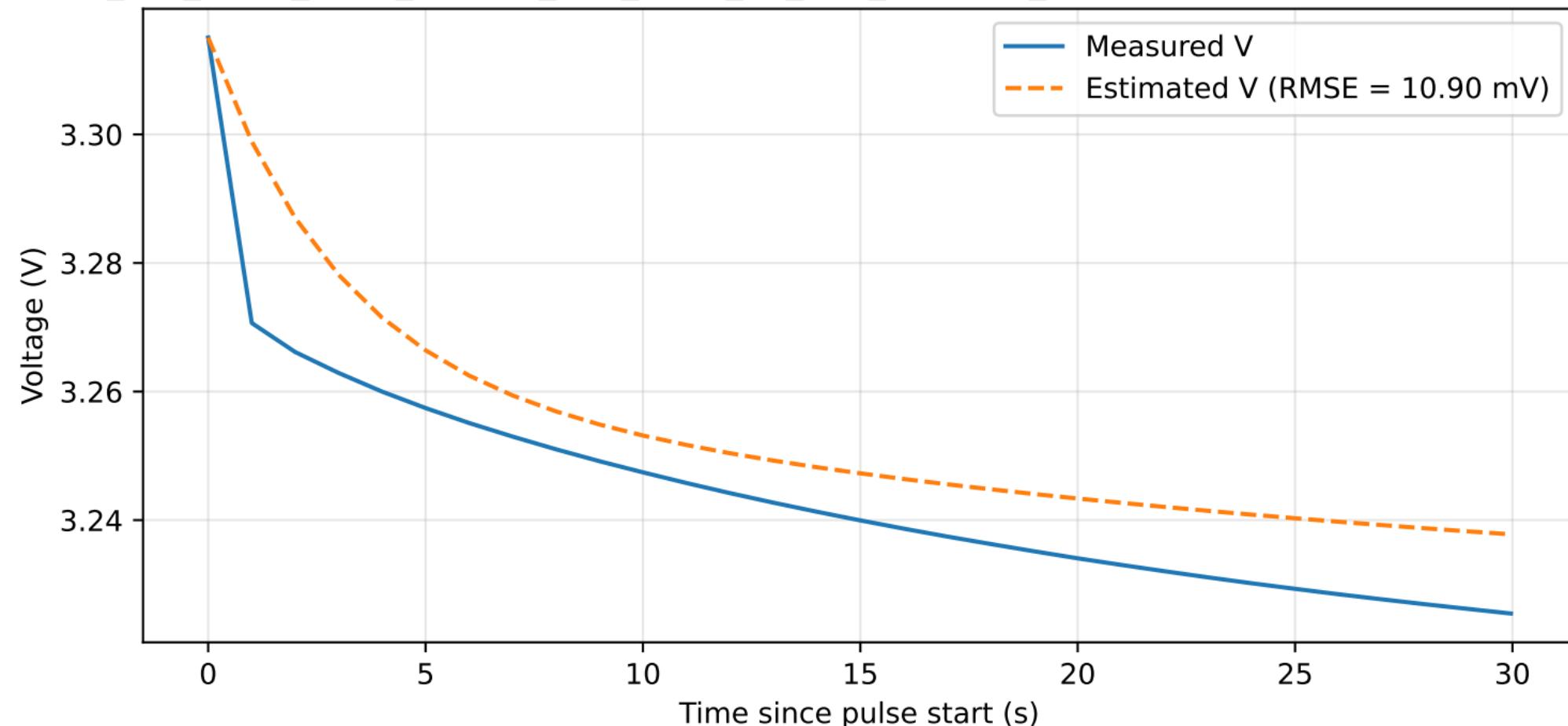
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



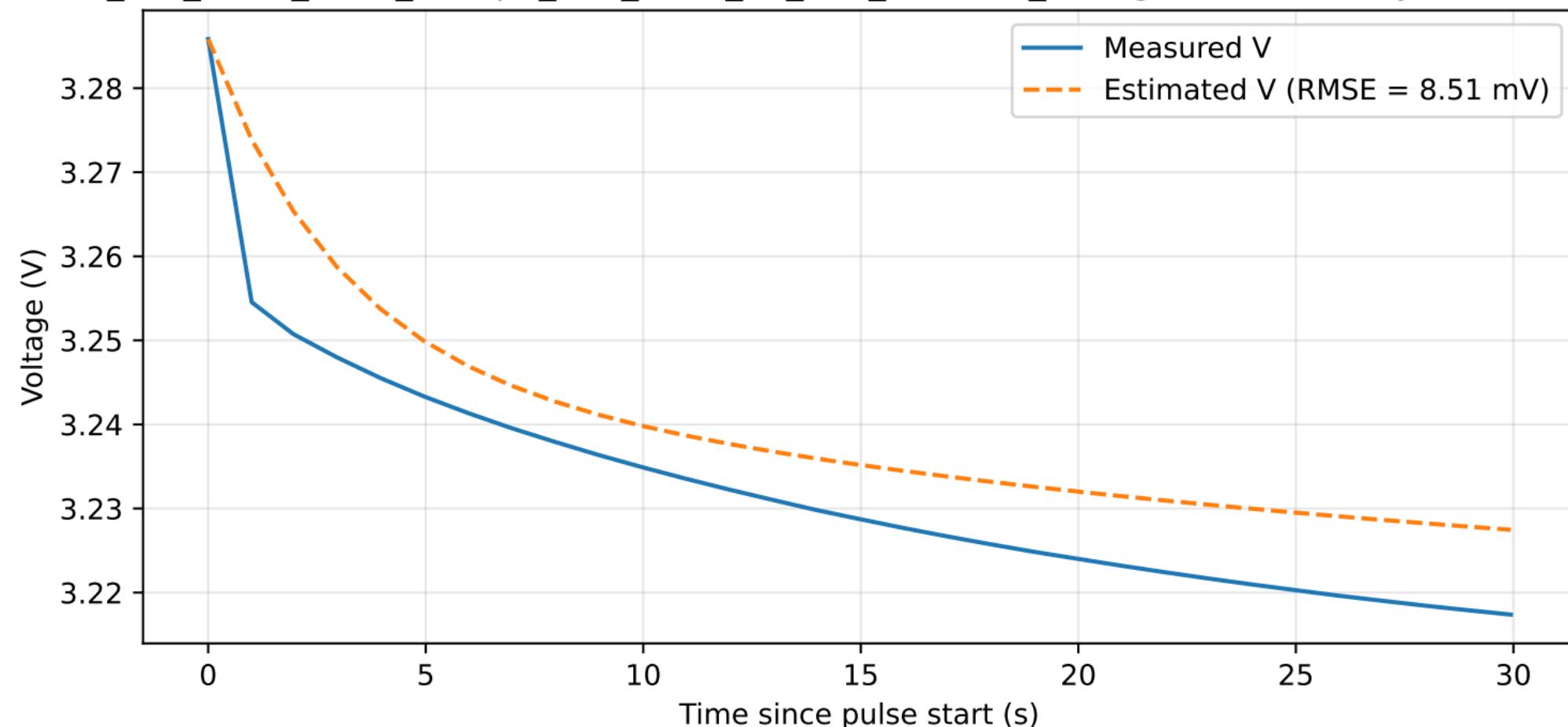
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



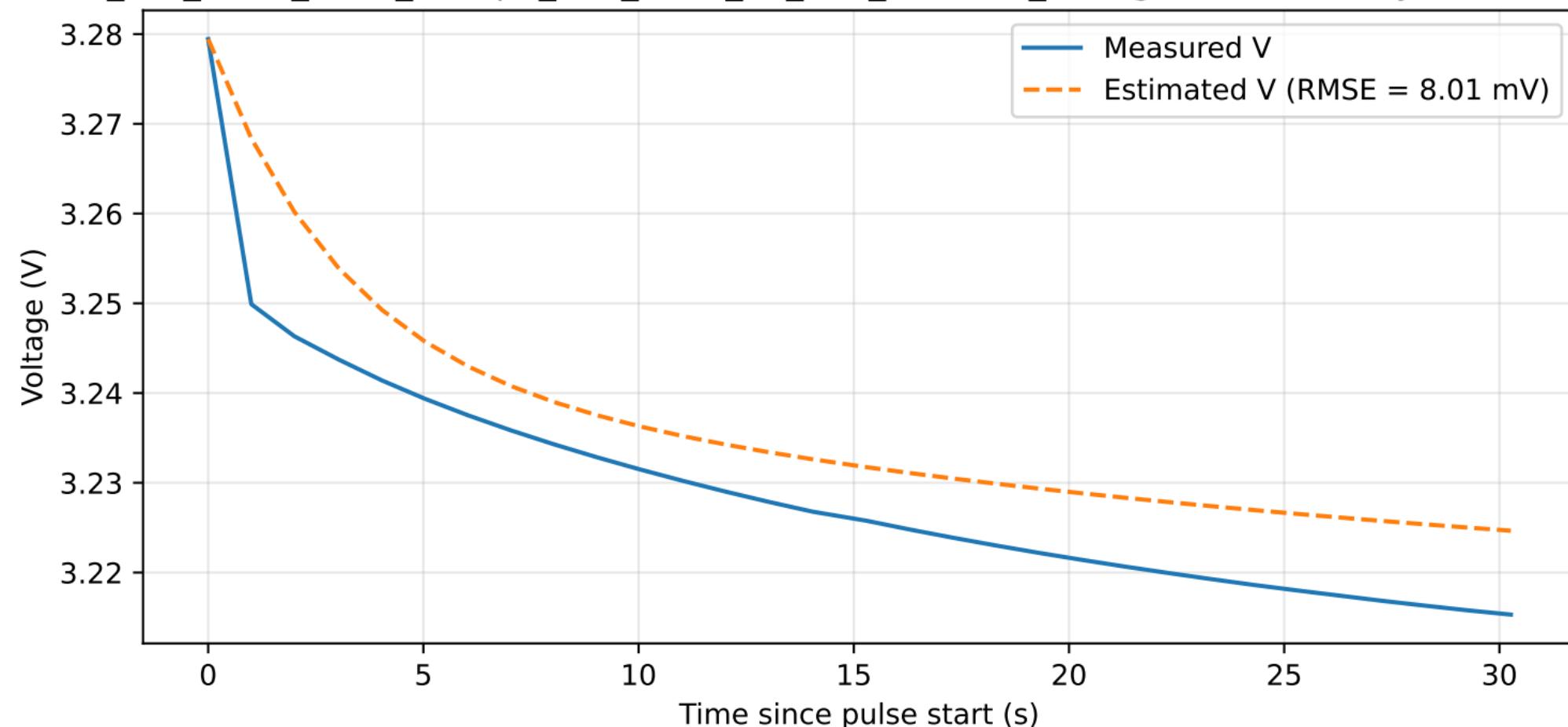
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



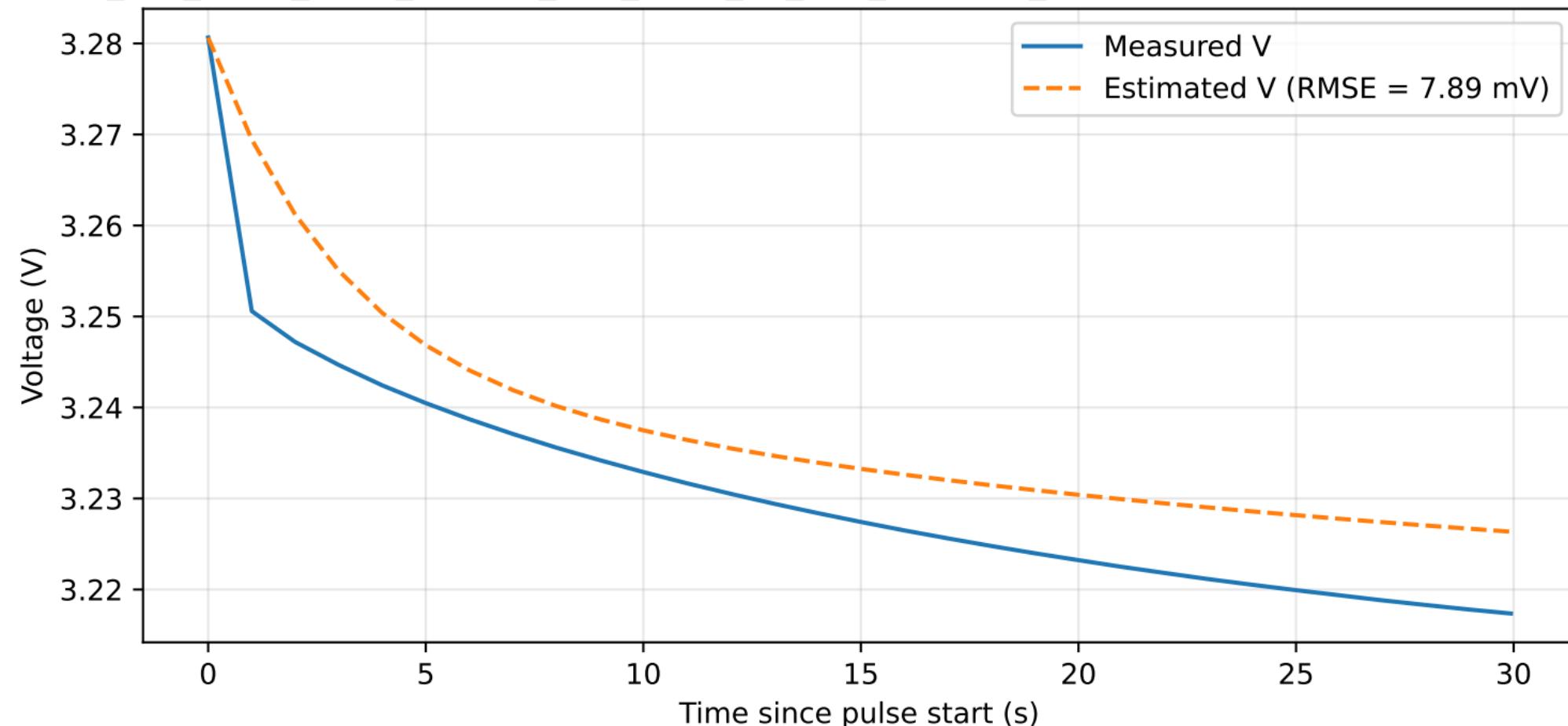
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



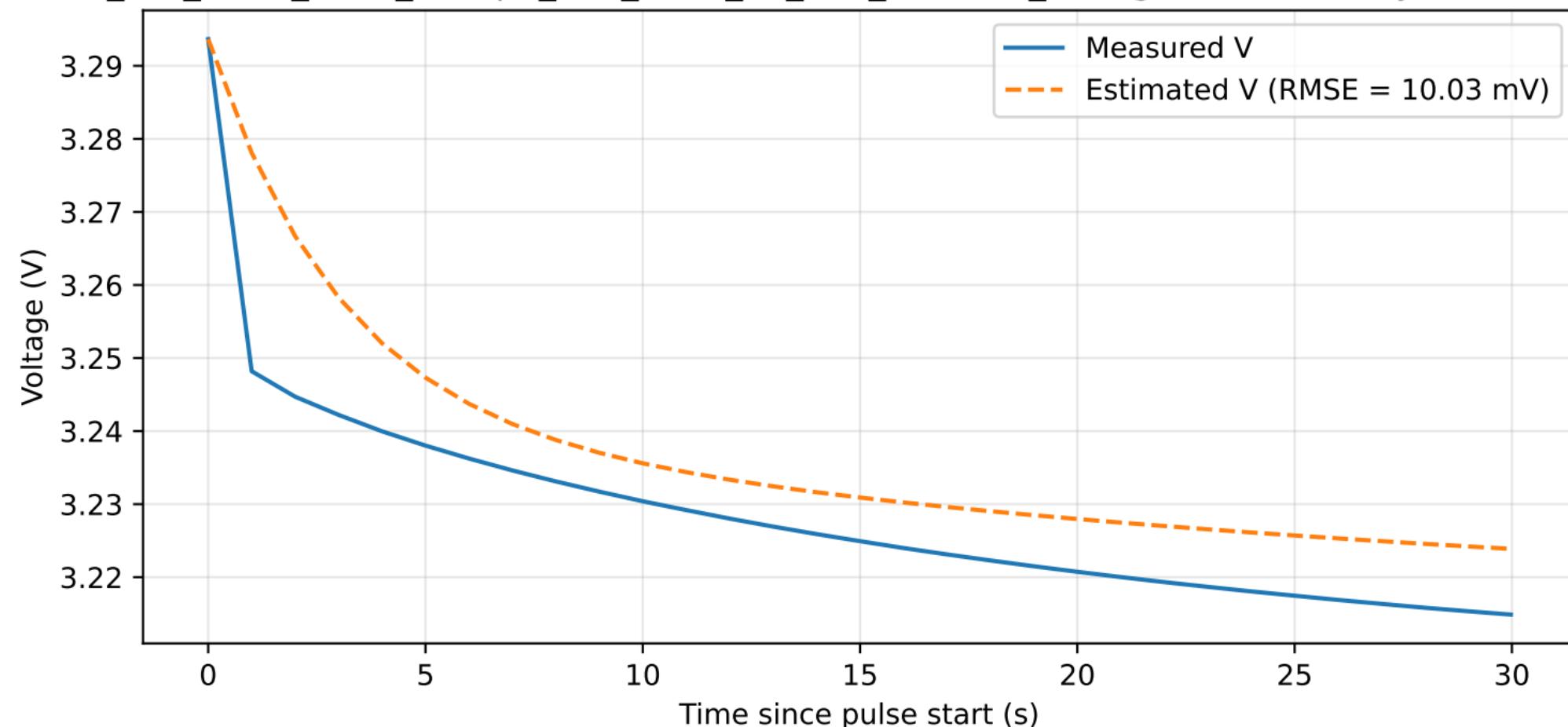
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



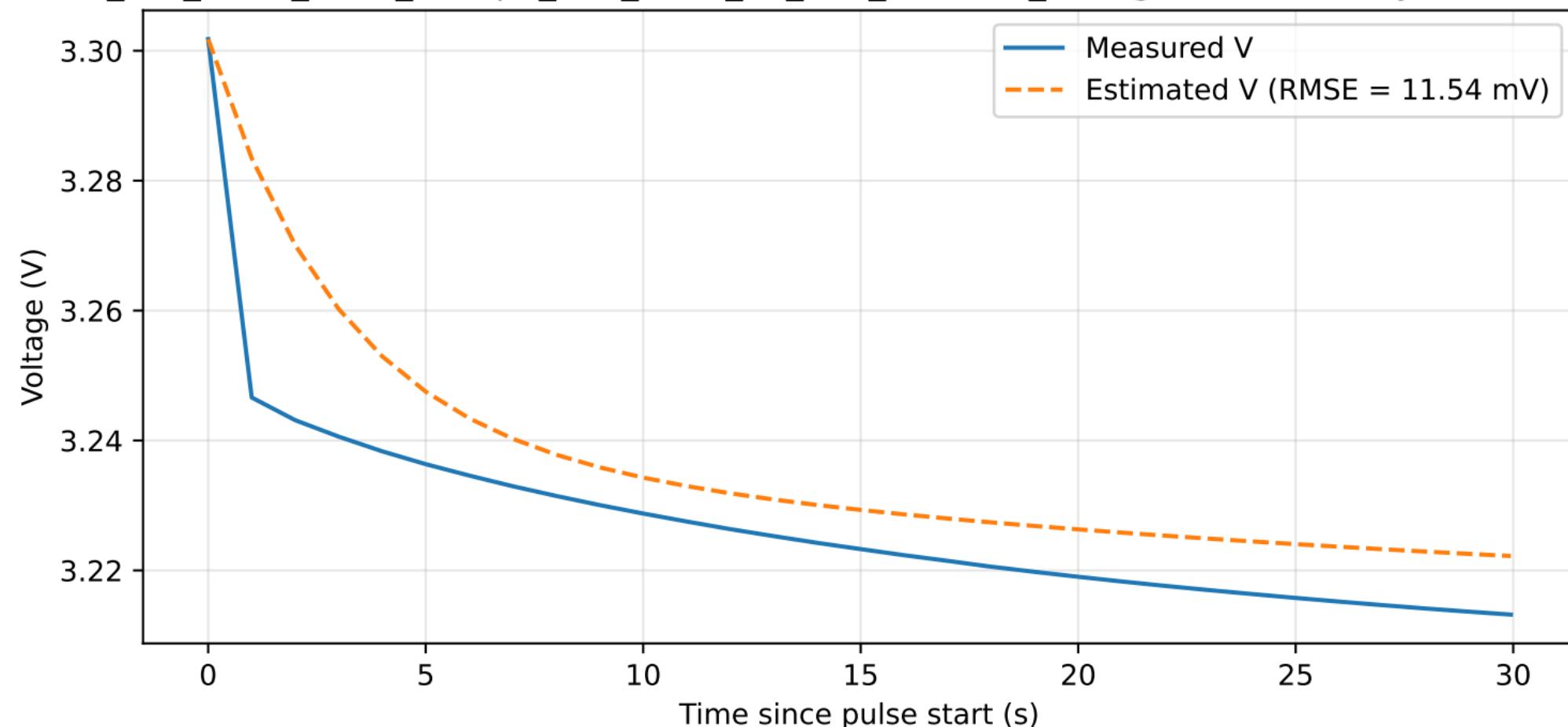
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



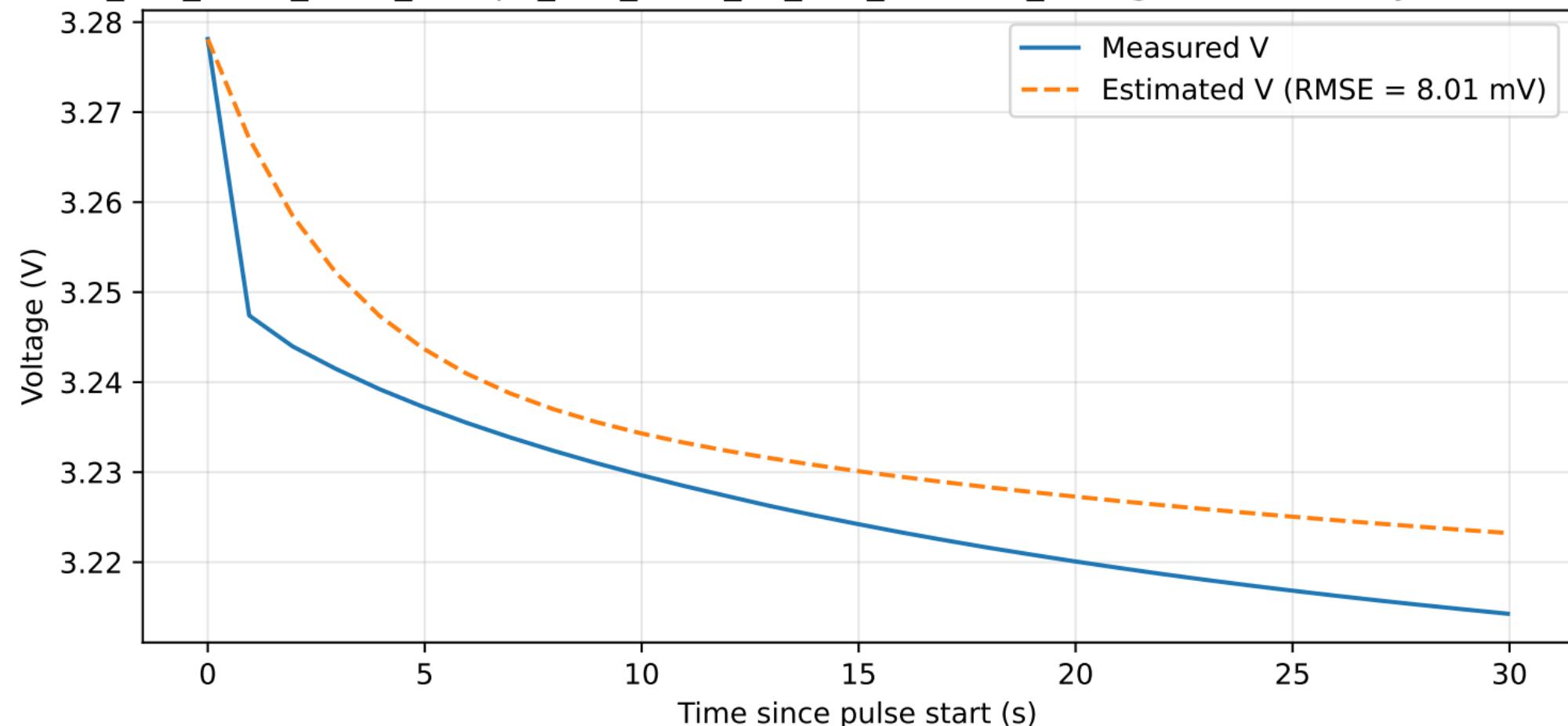
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



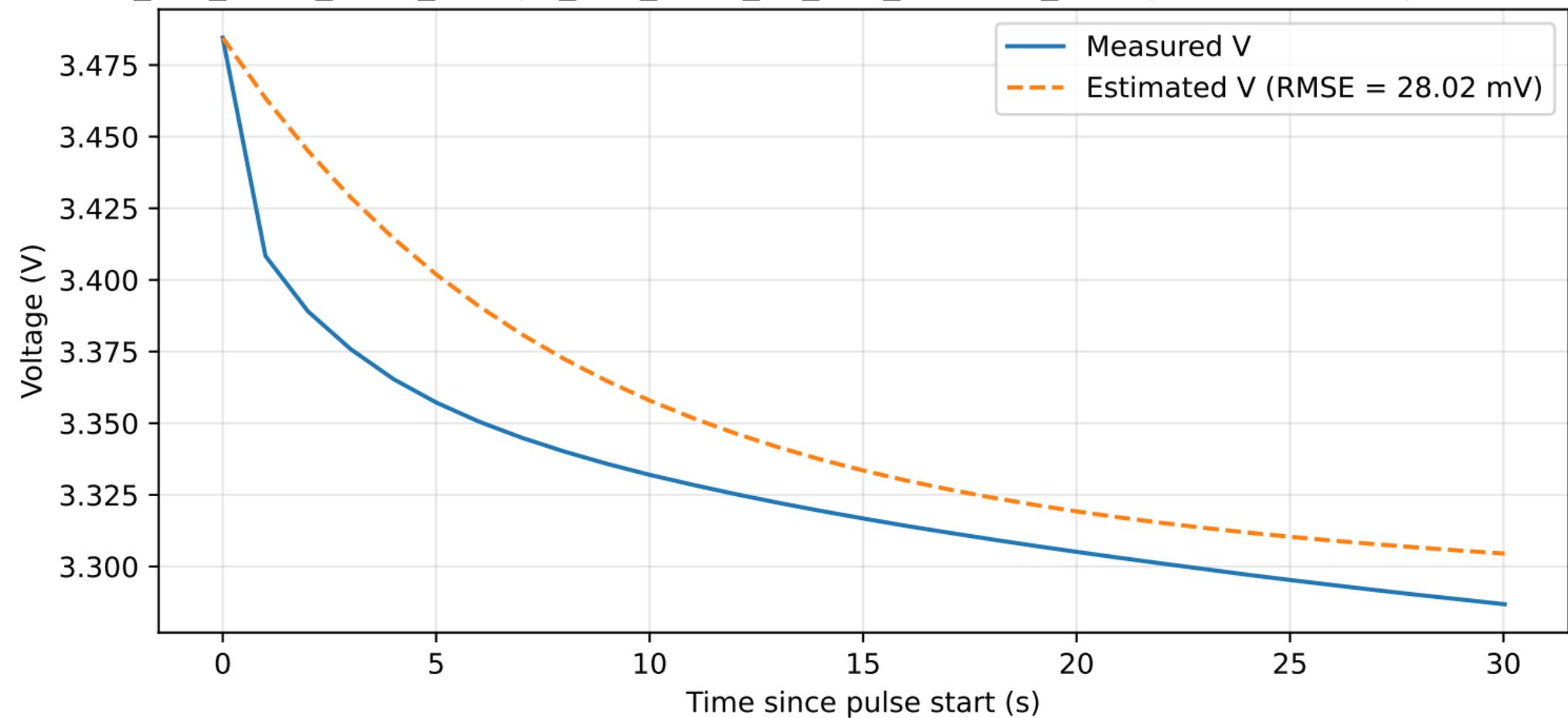
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



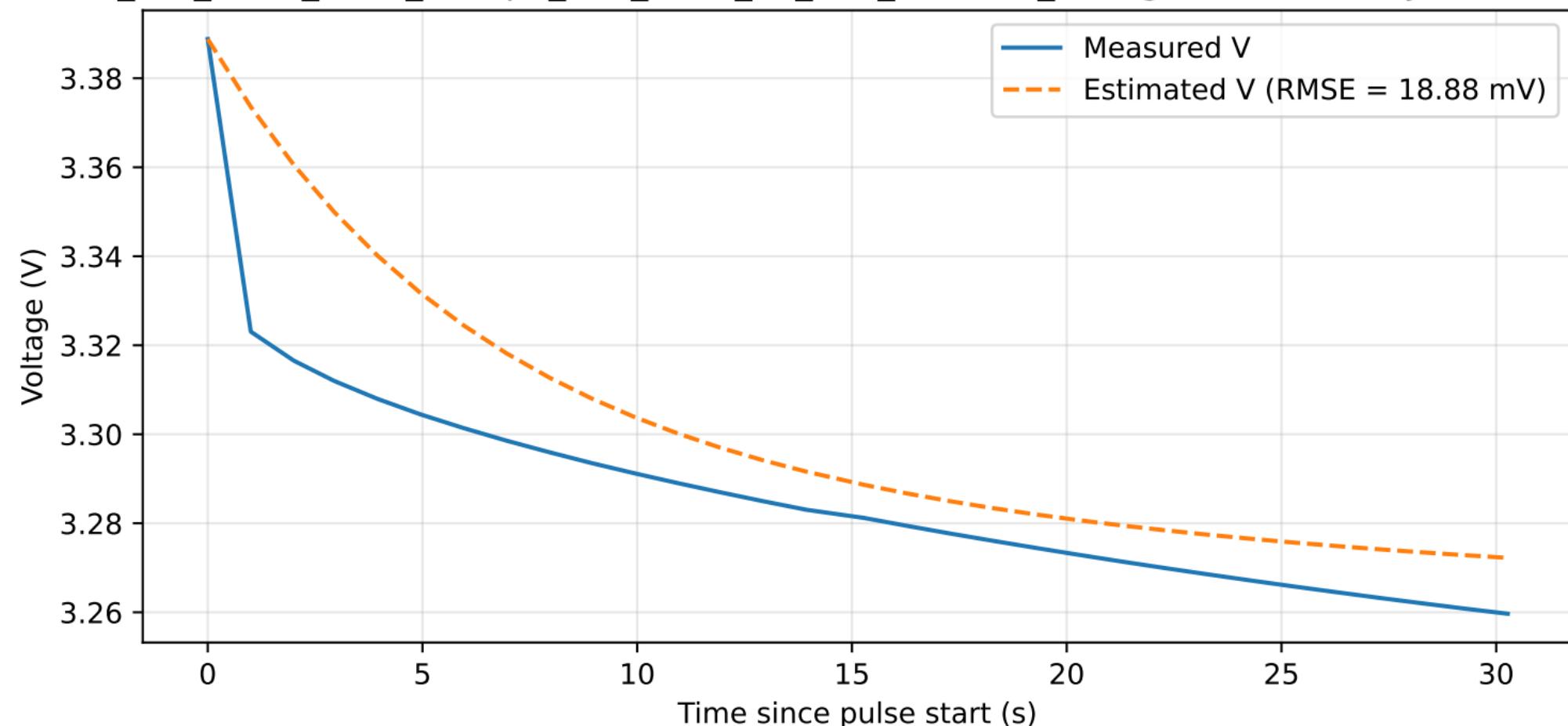
# RD\_LFP\_HPPC\_REPT\_Group0\_150\_0091\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



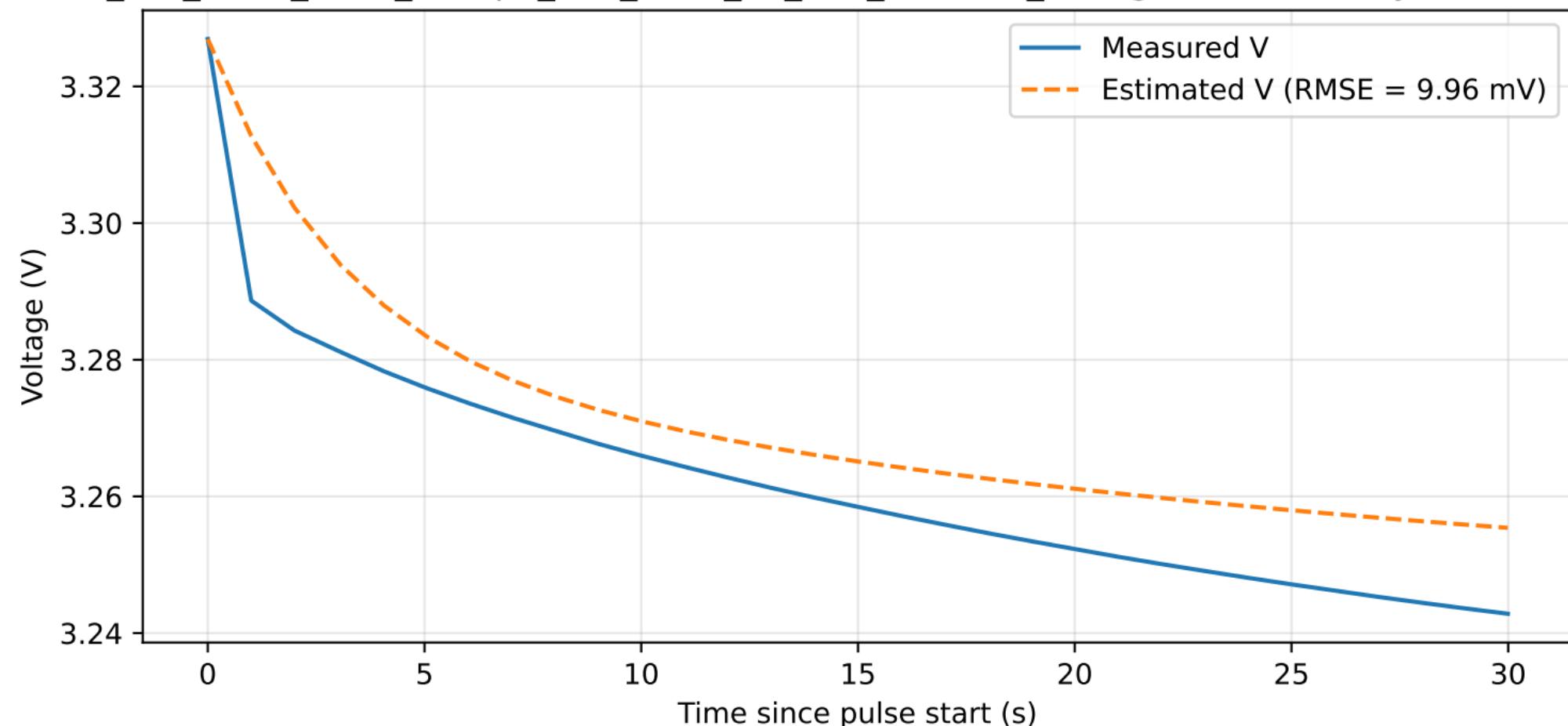
RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



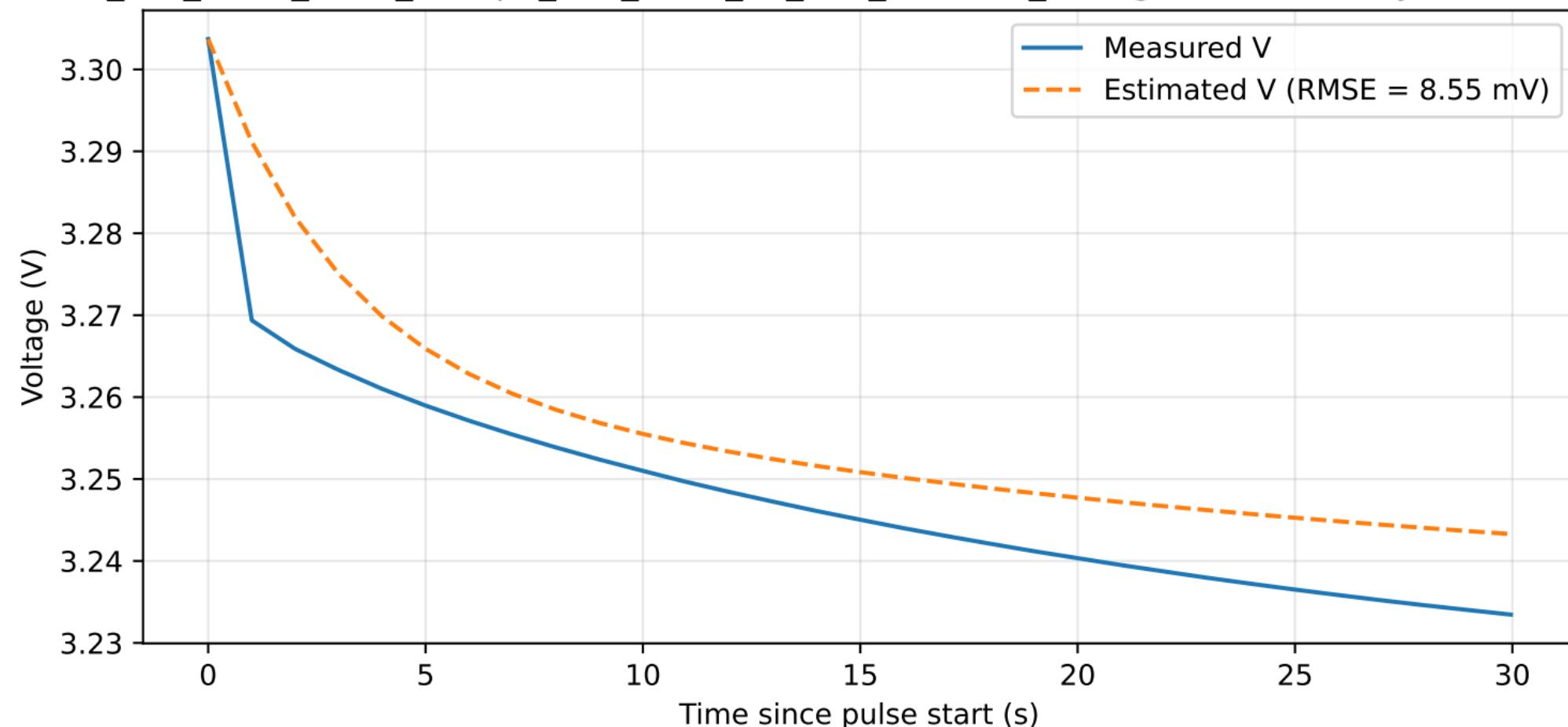
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



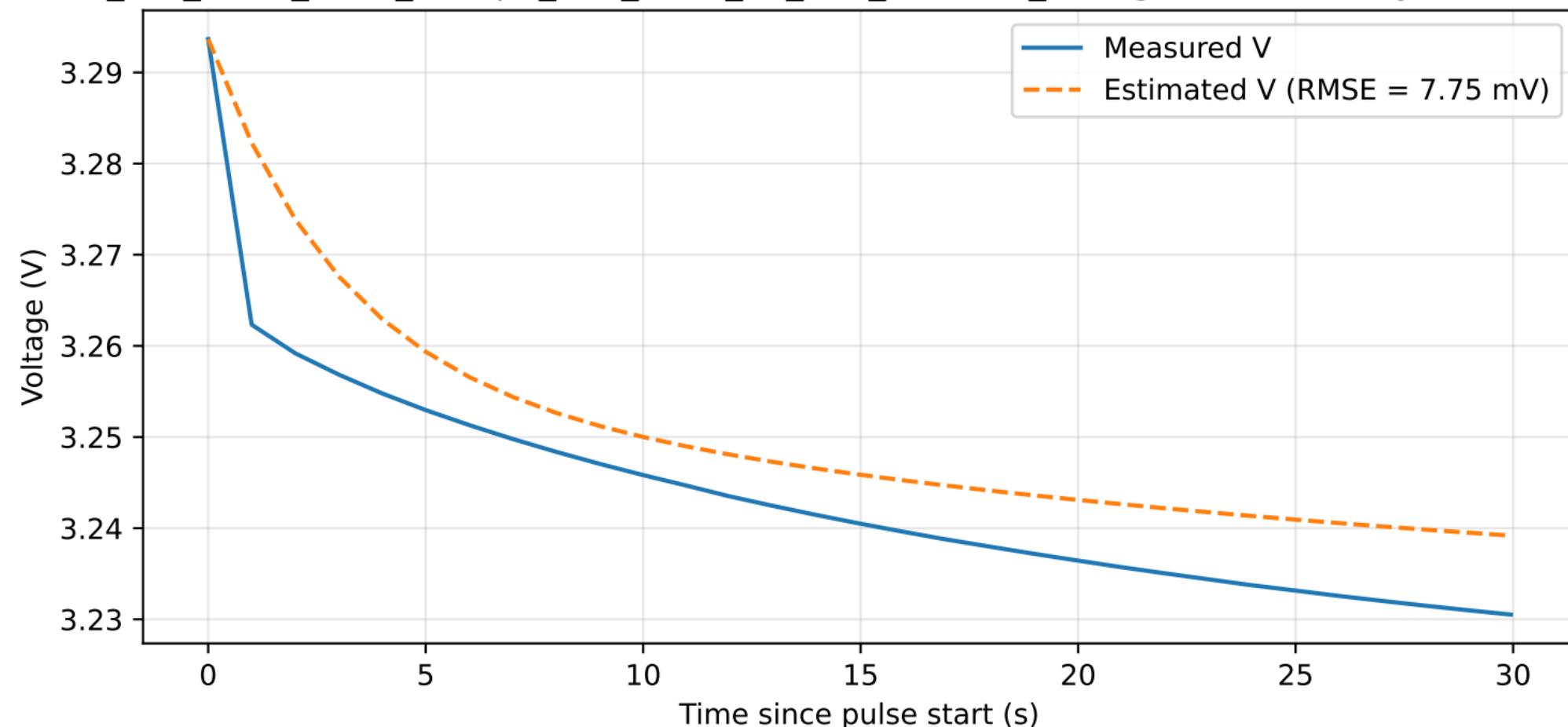
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



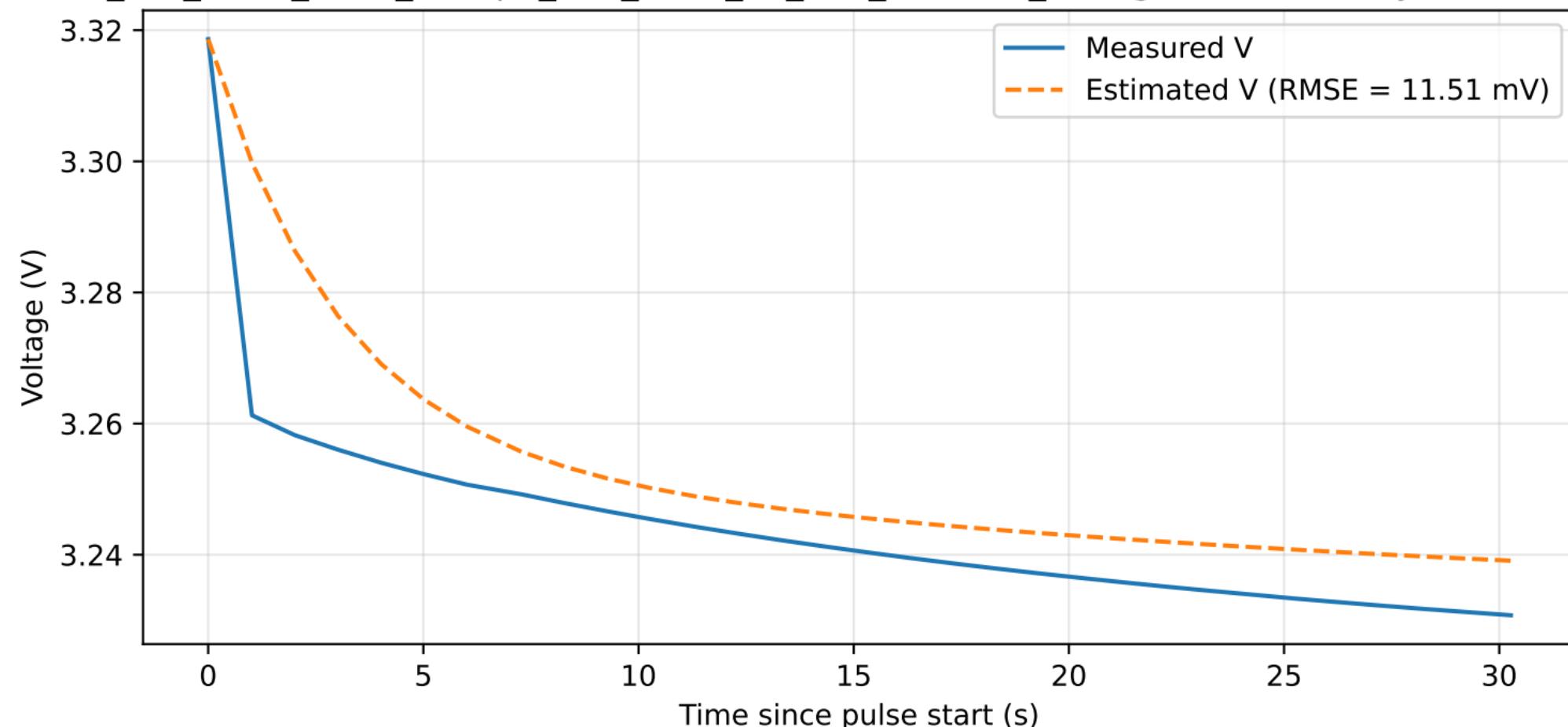
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



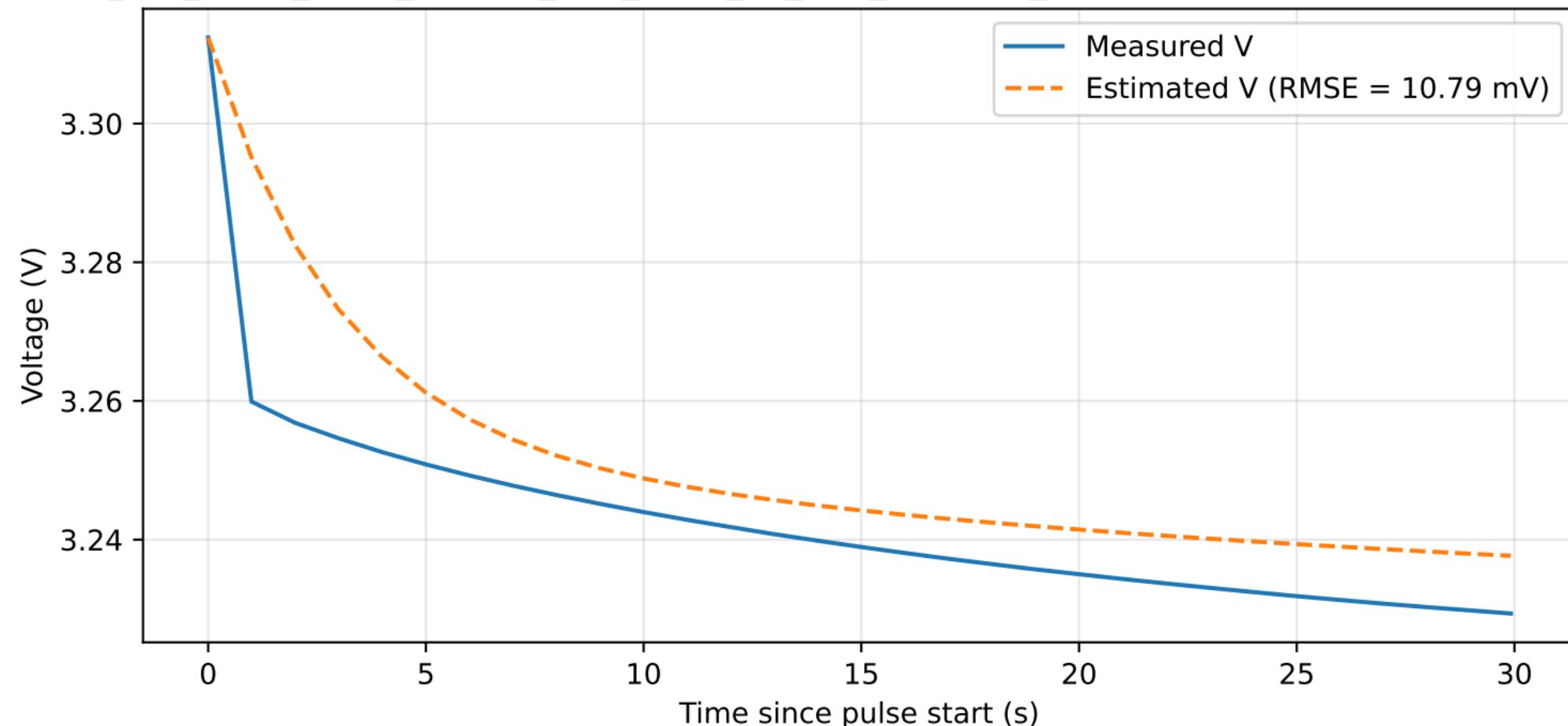
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



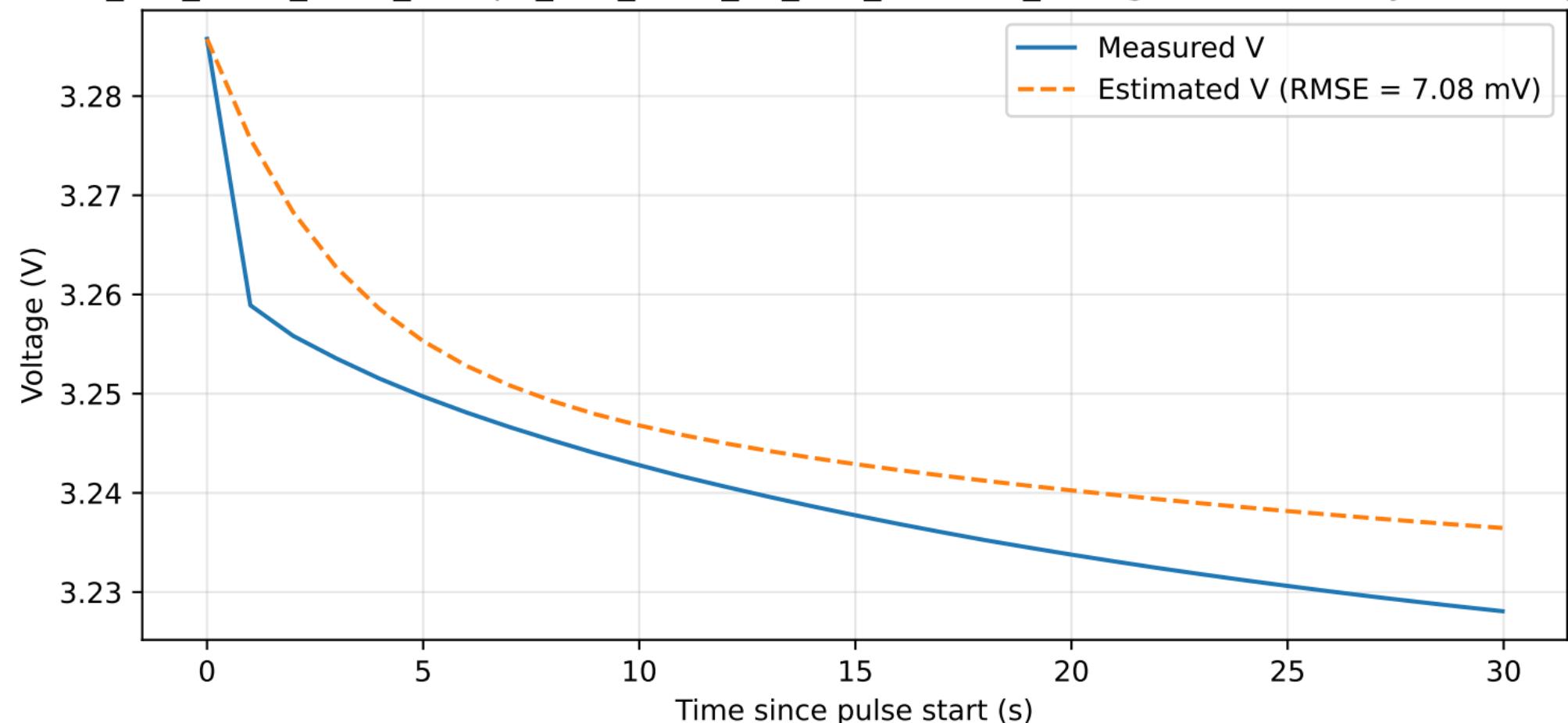
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



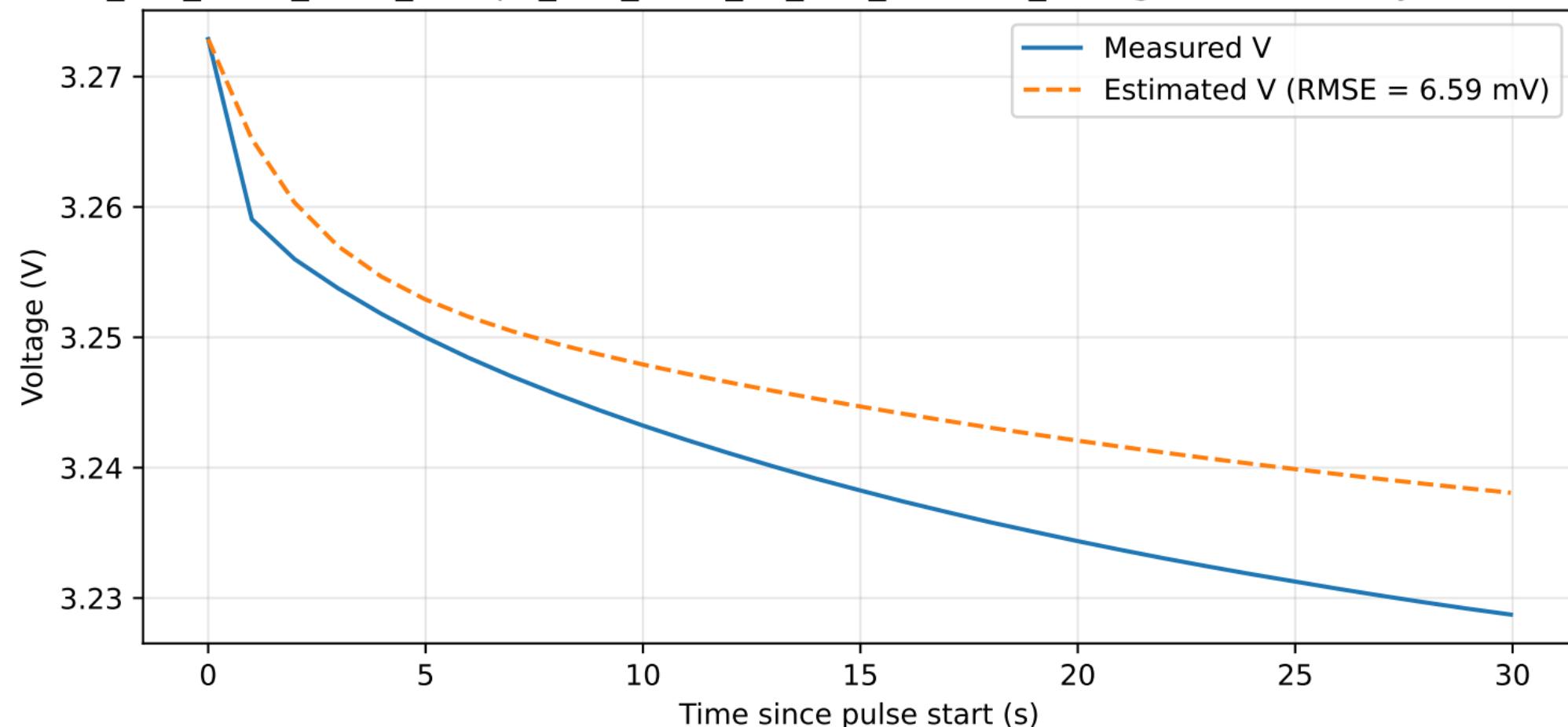
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



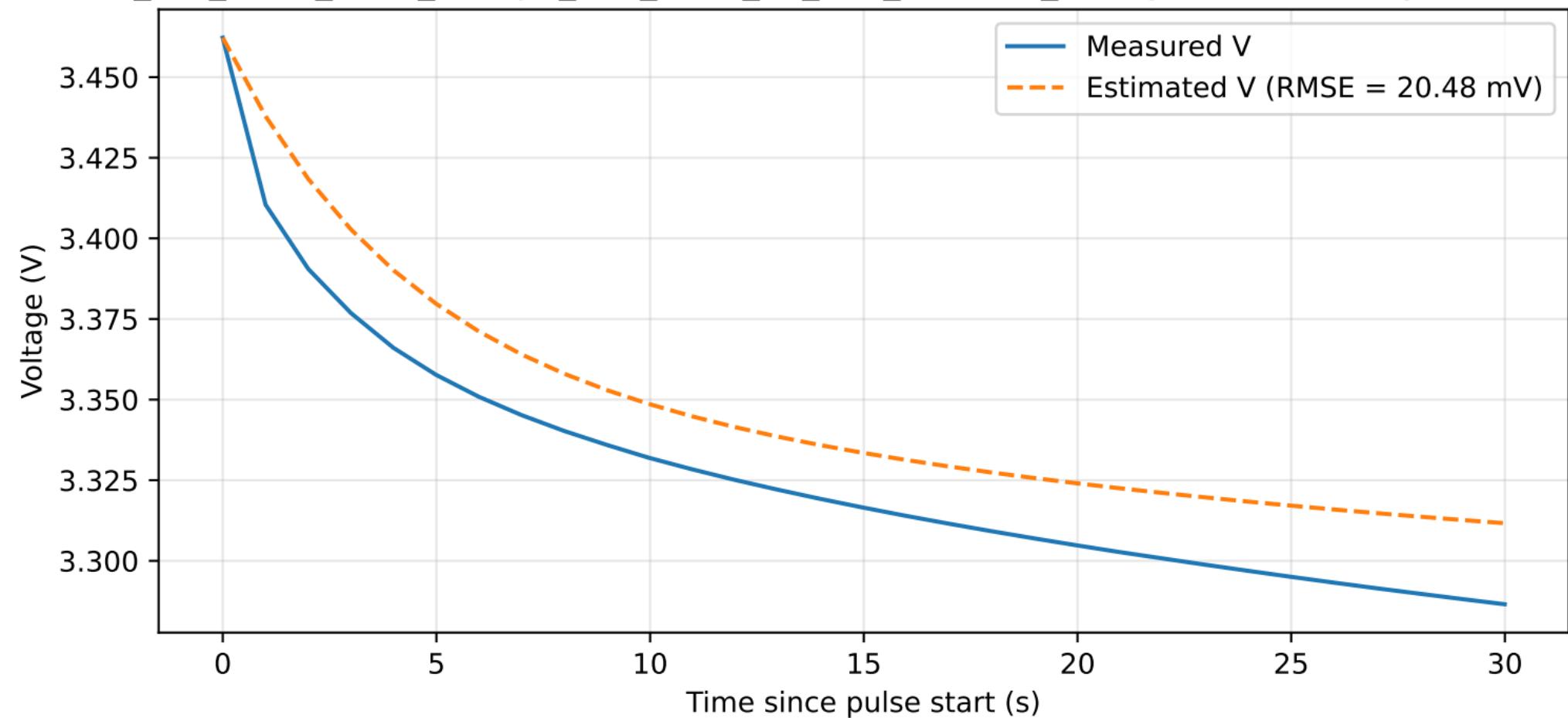
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



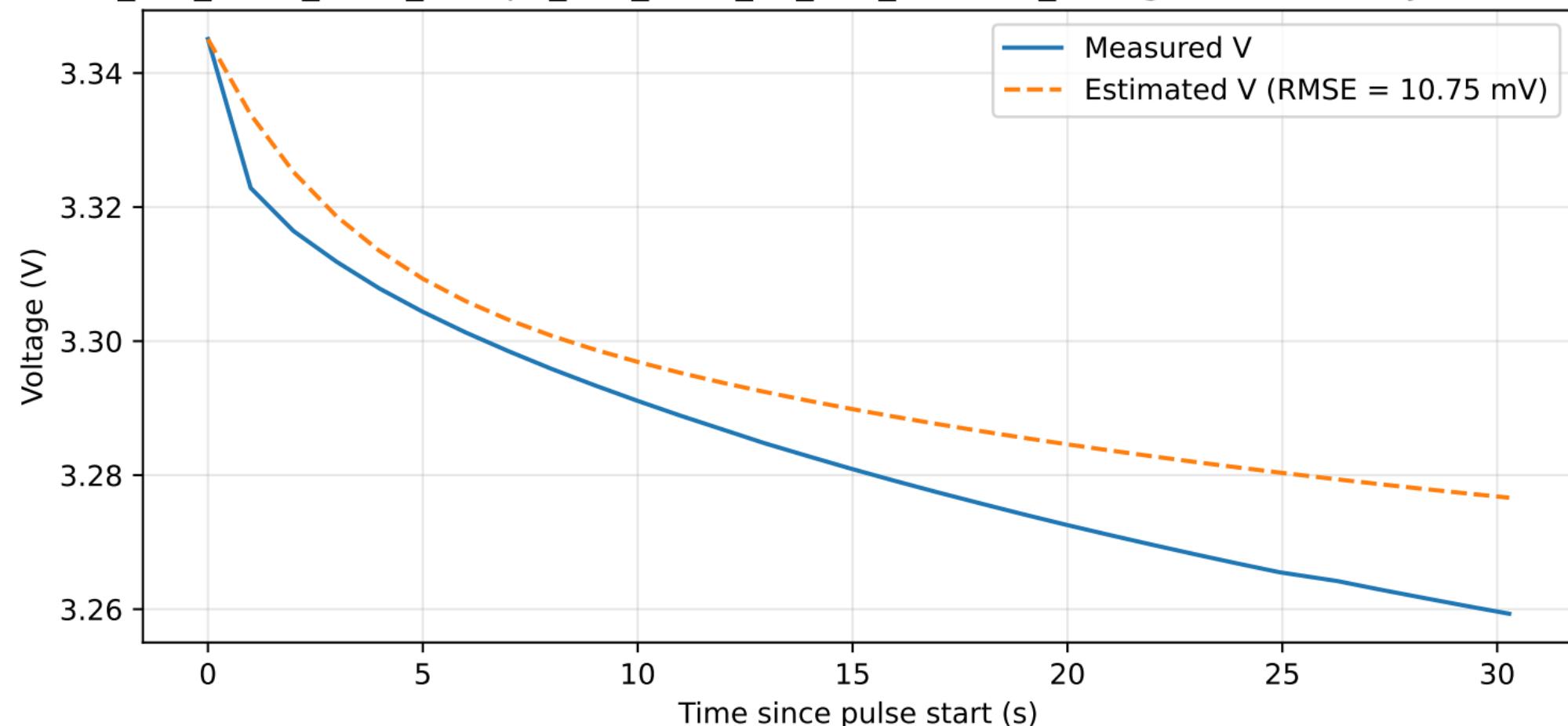
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0065\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



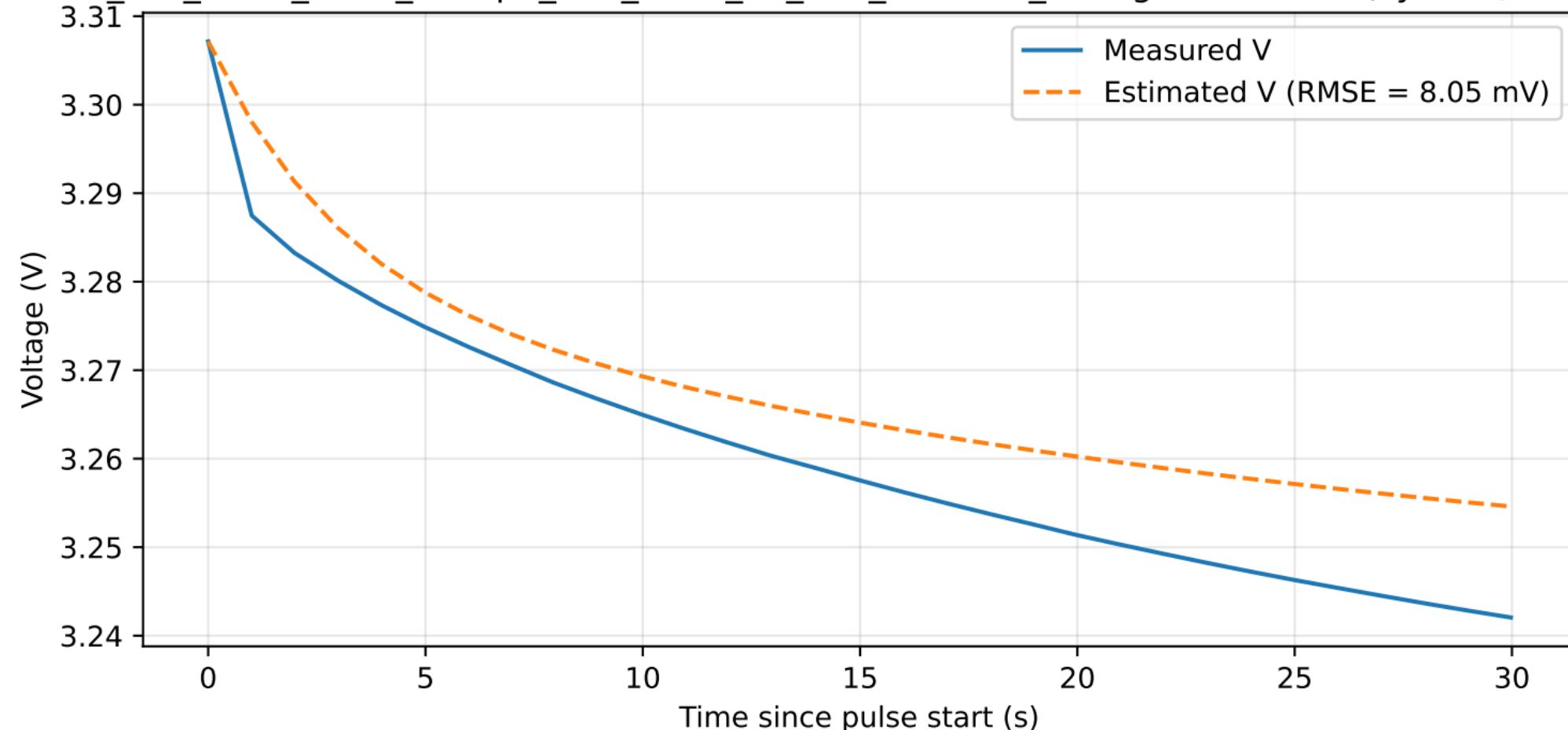
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



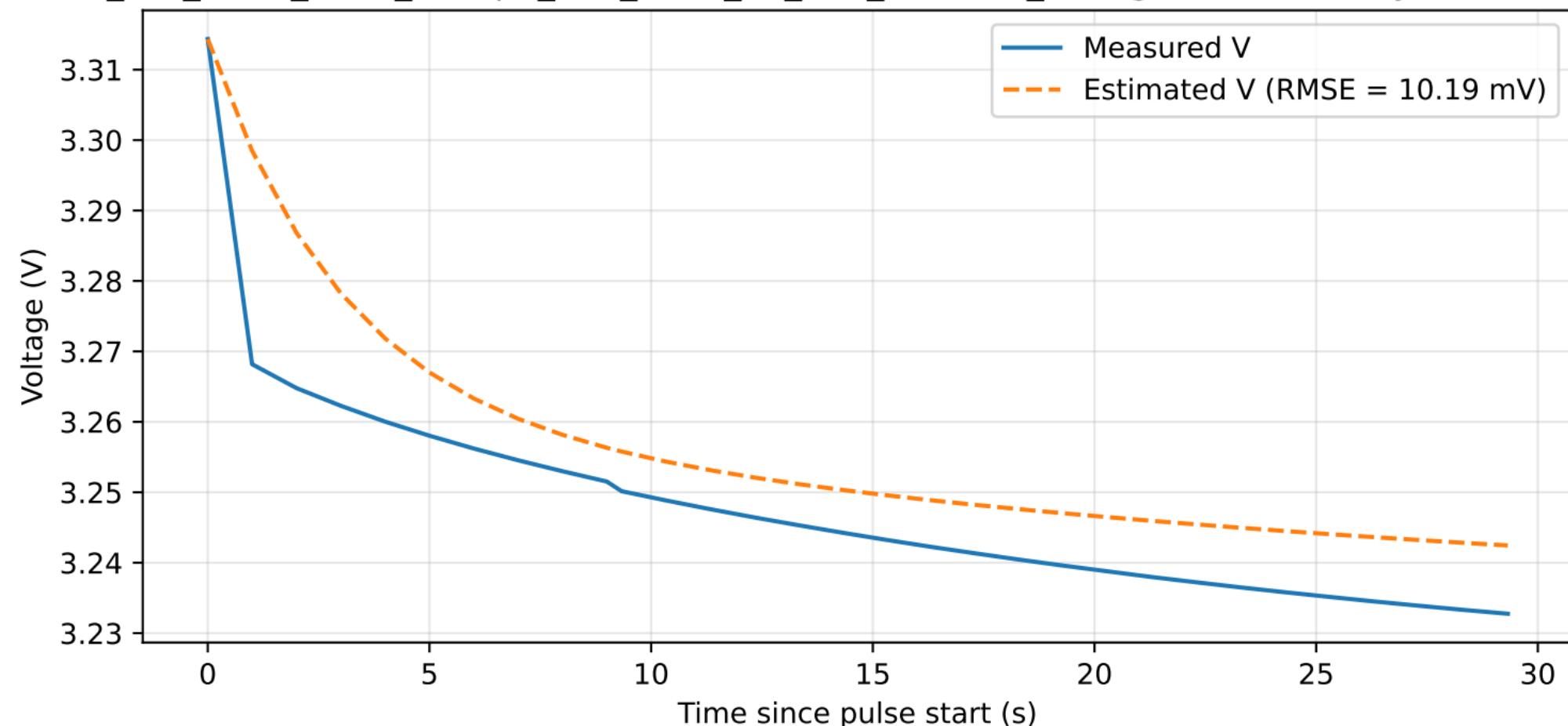
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



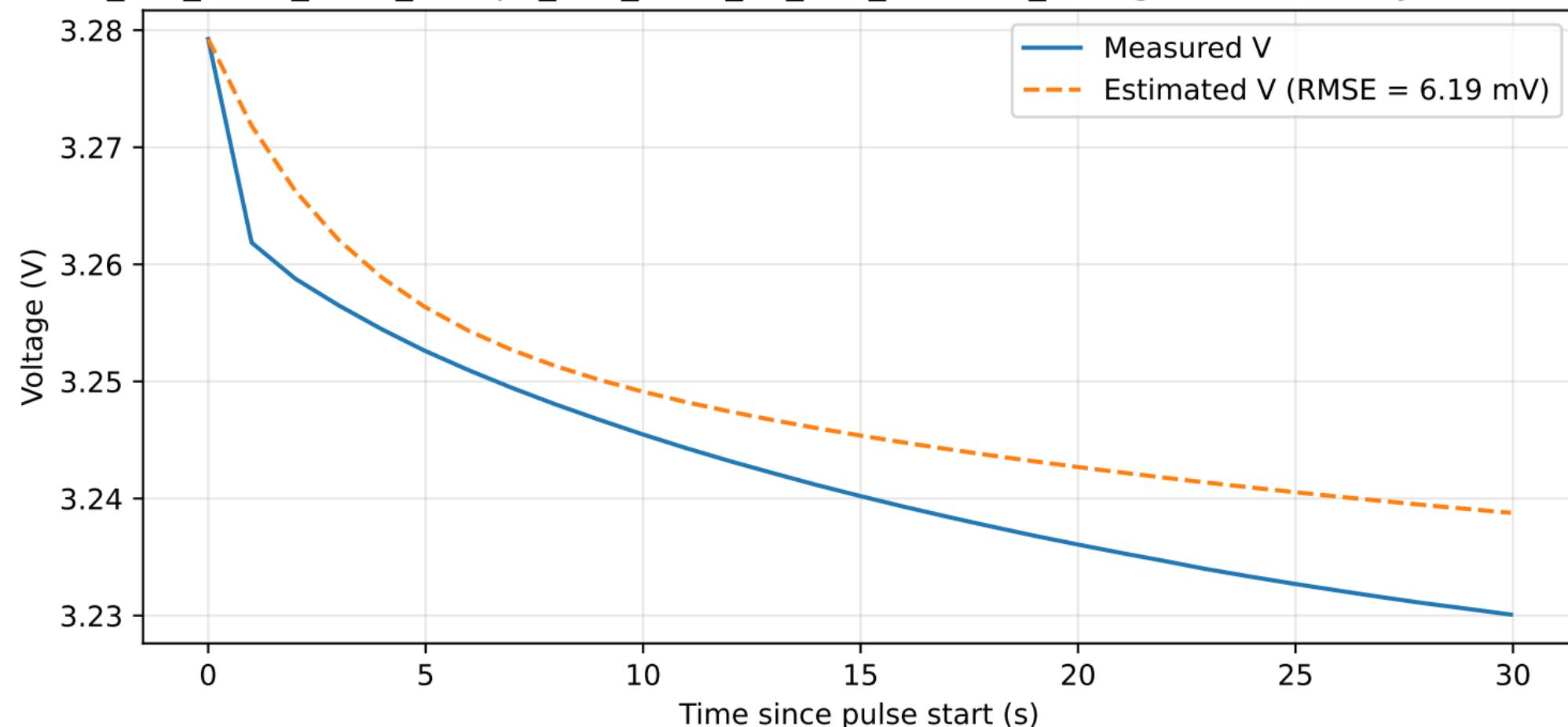
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



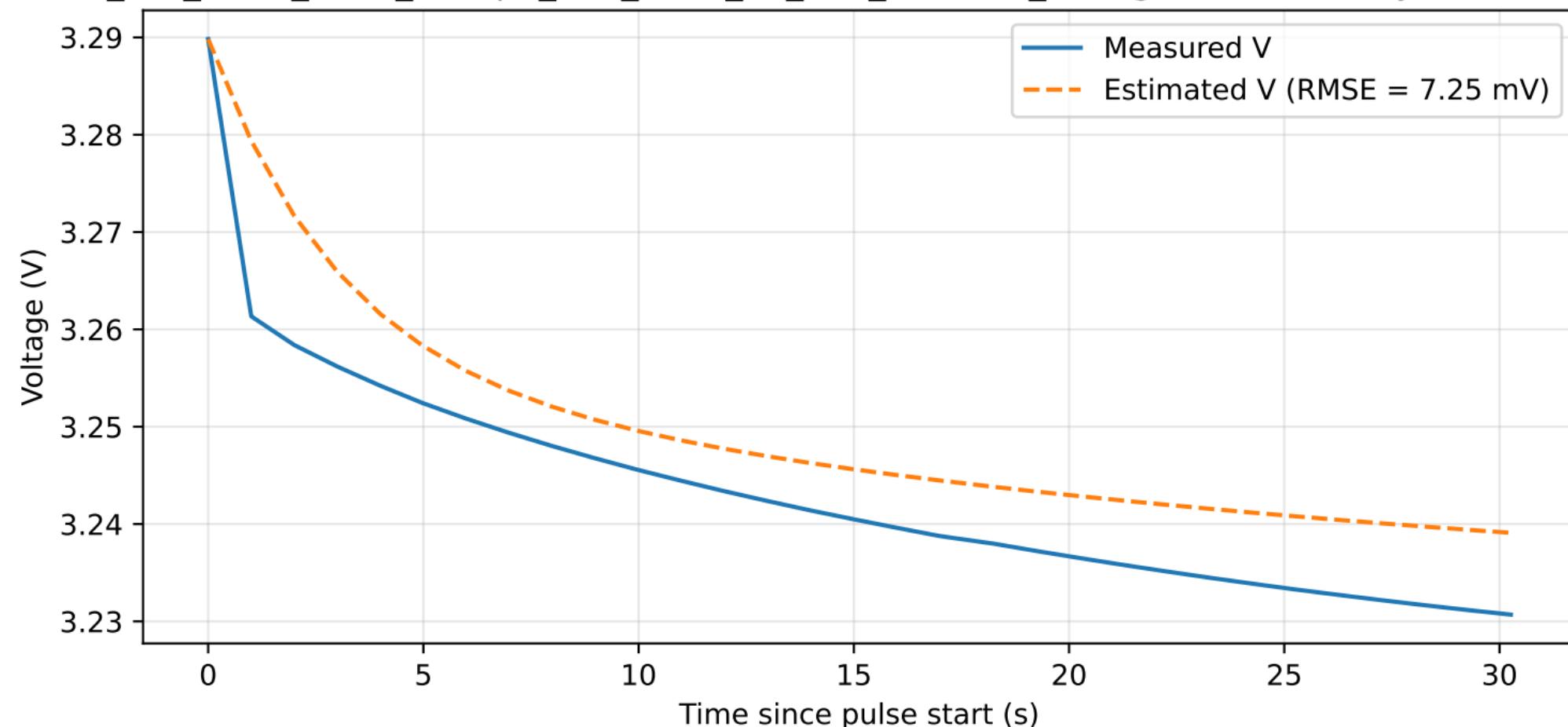
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



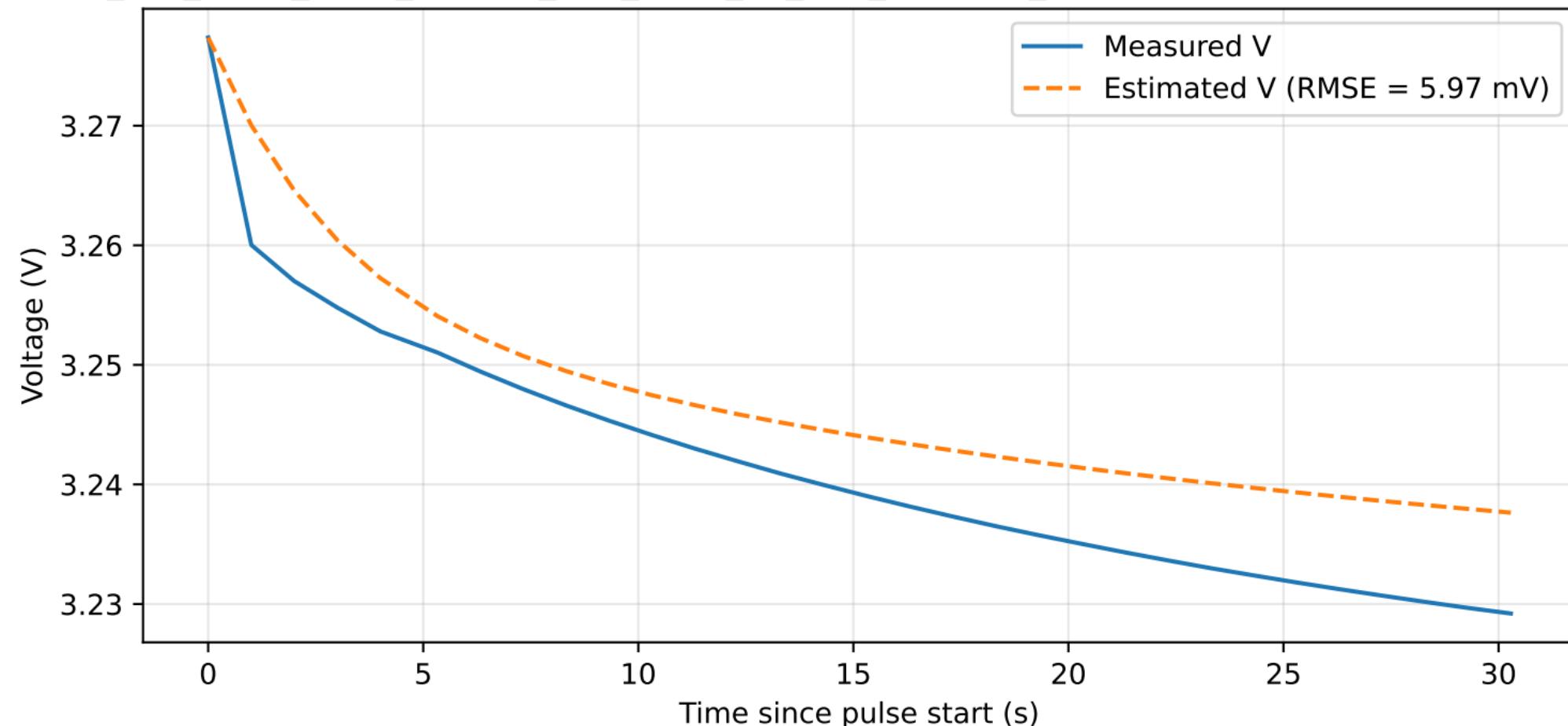
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



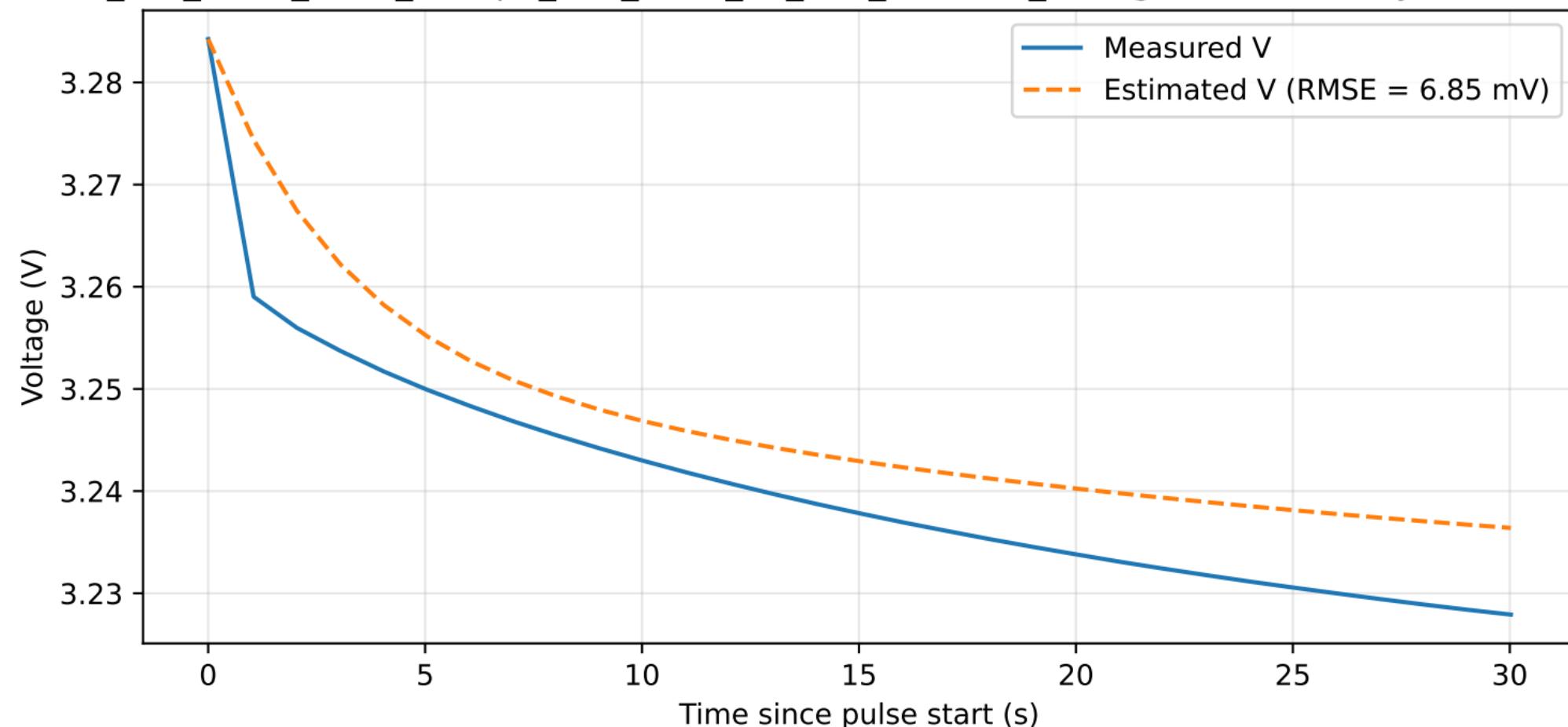
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



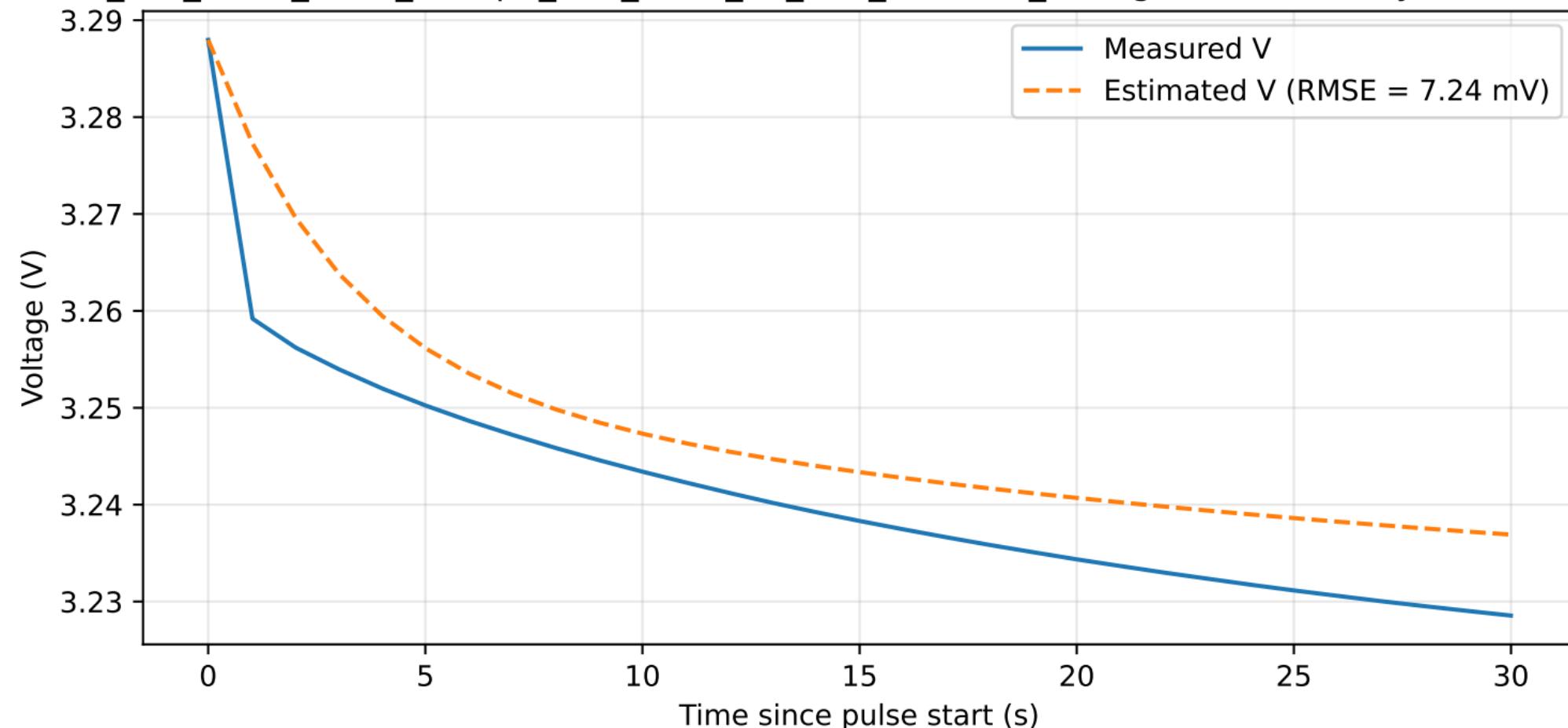
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



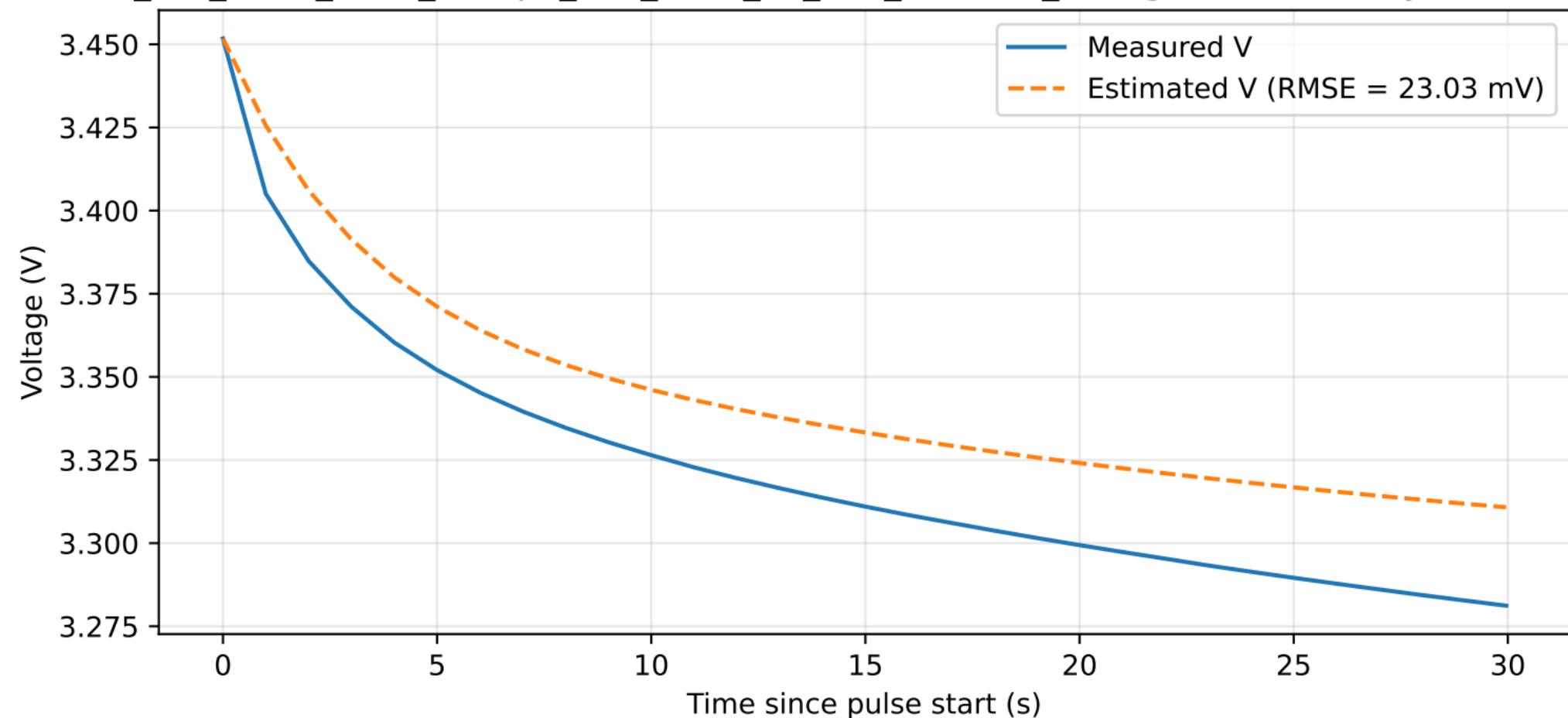
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



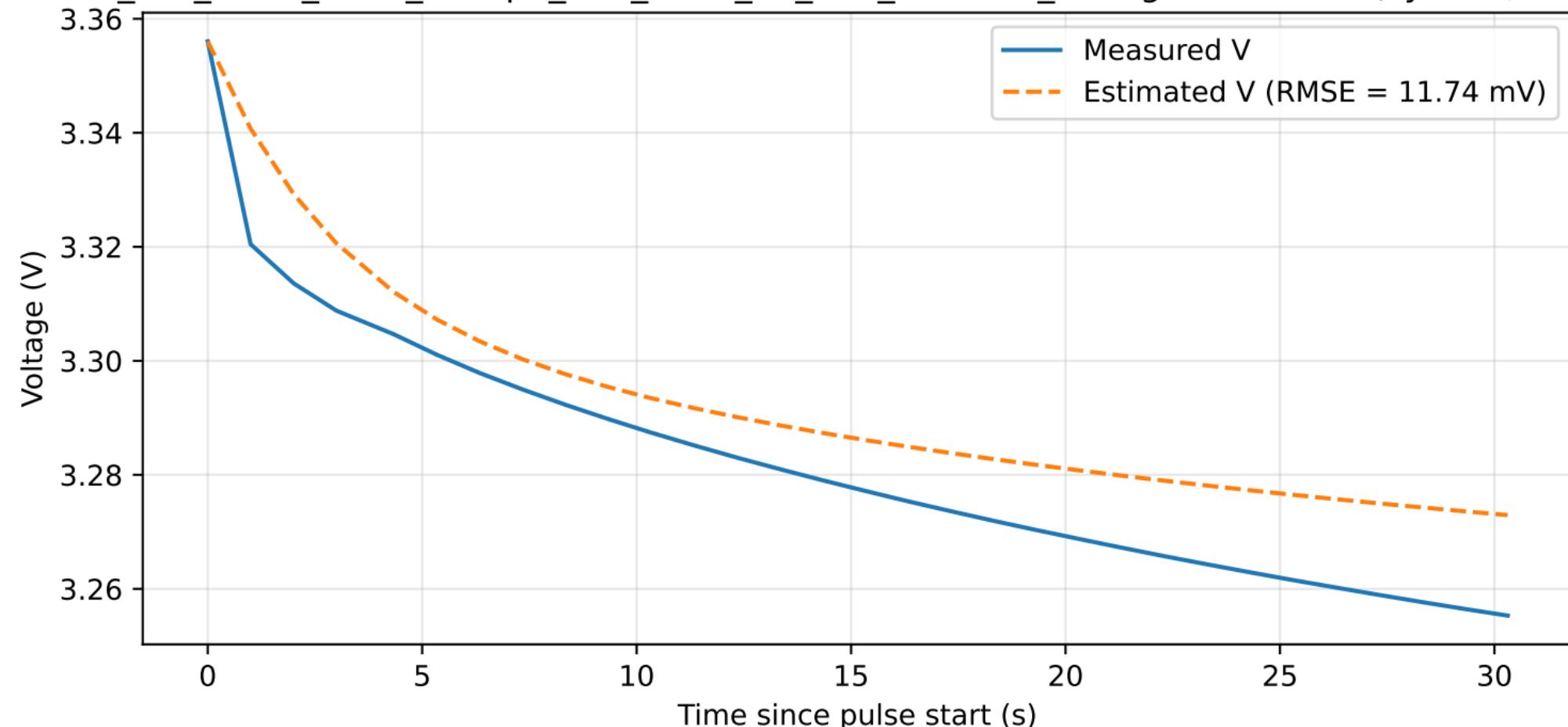
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0074\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



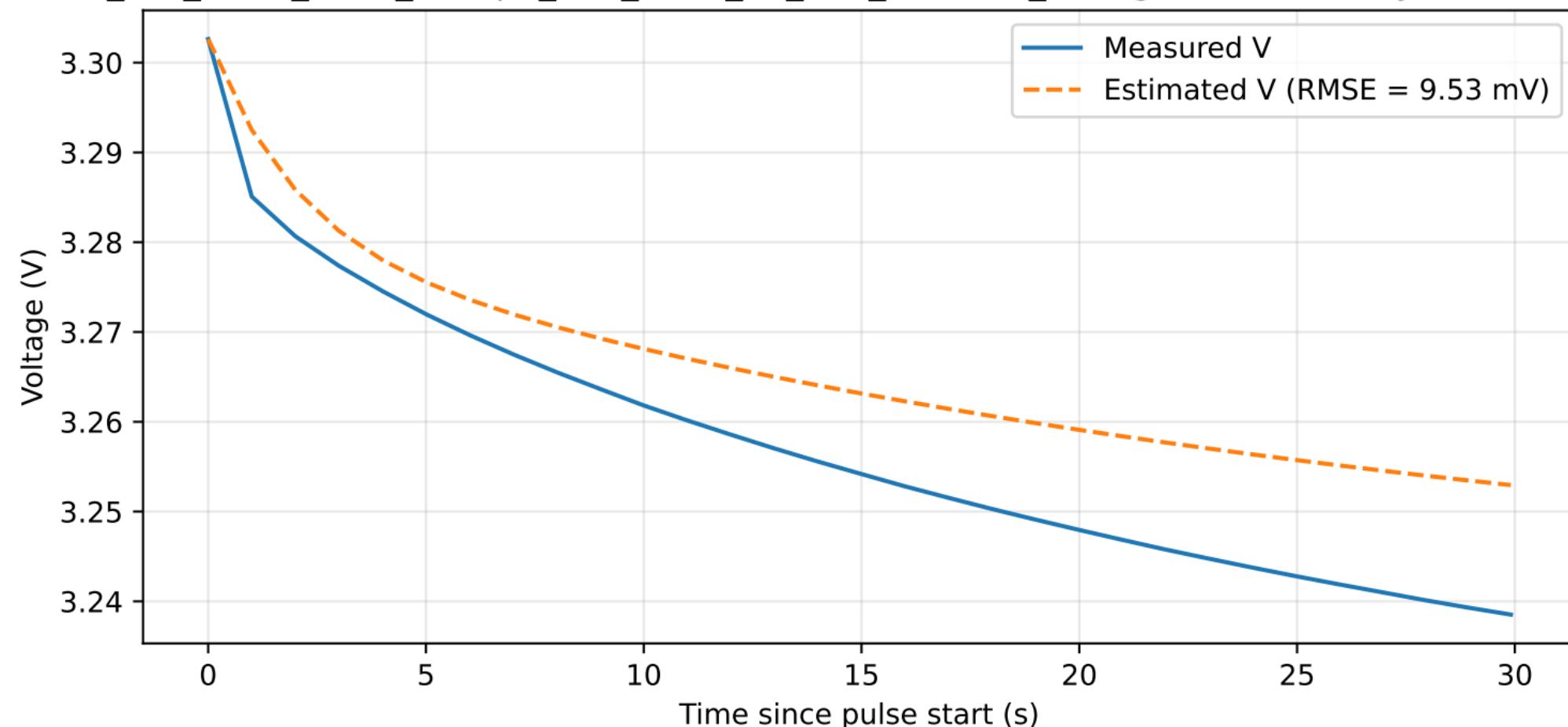
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



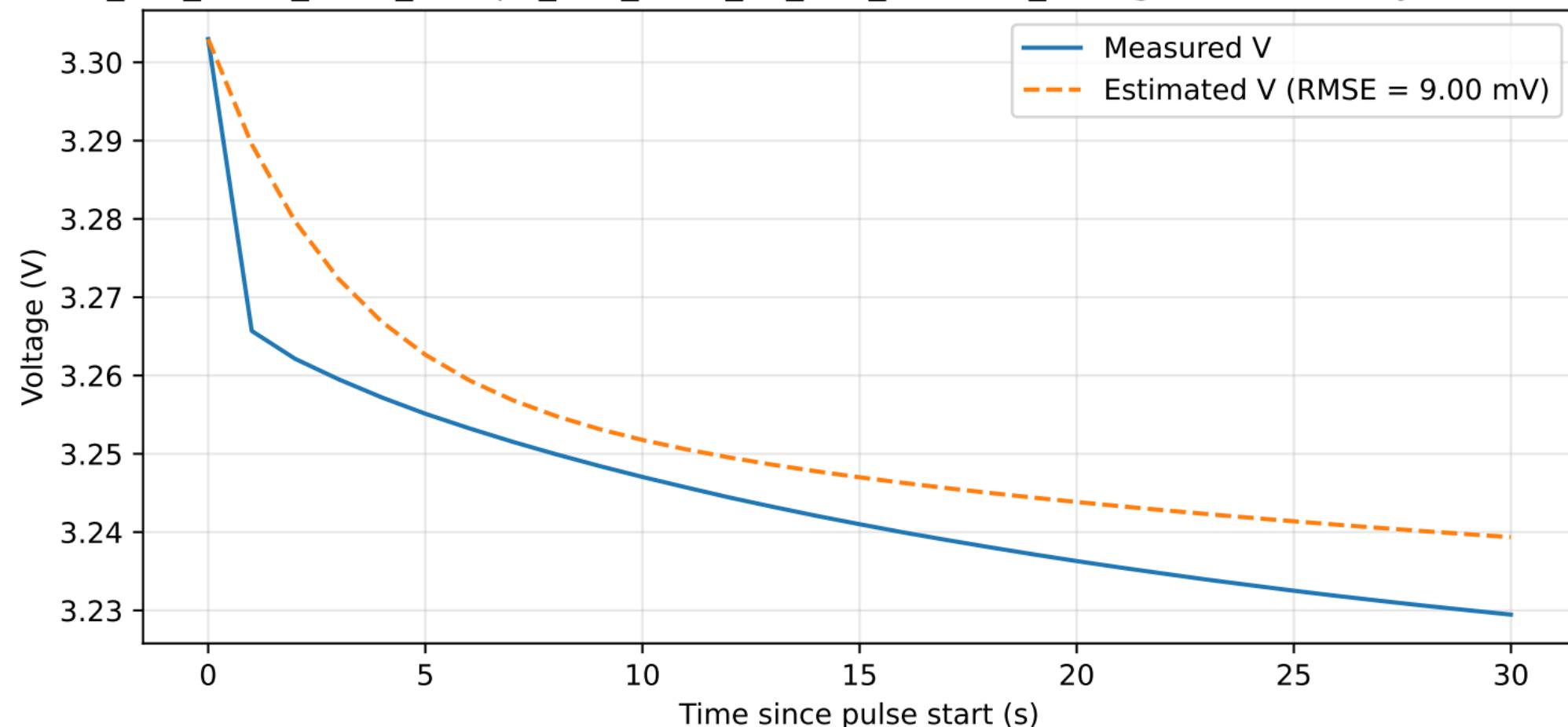
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



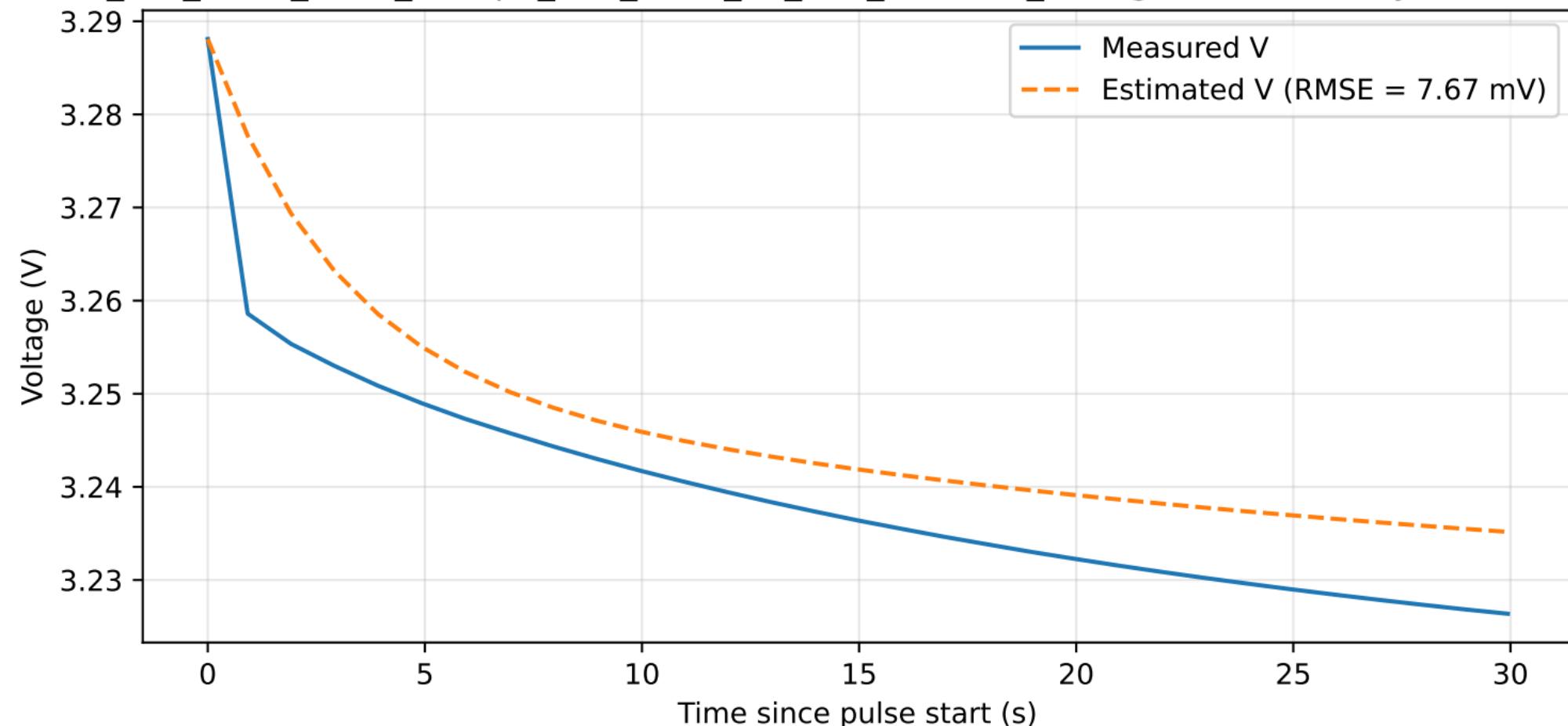
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



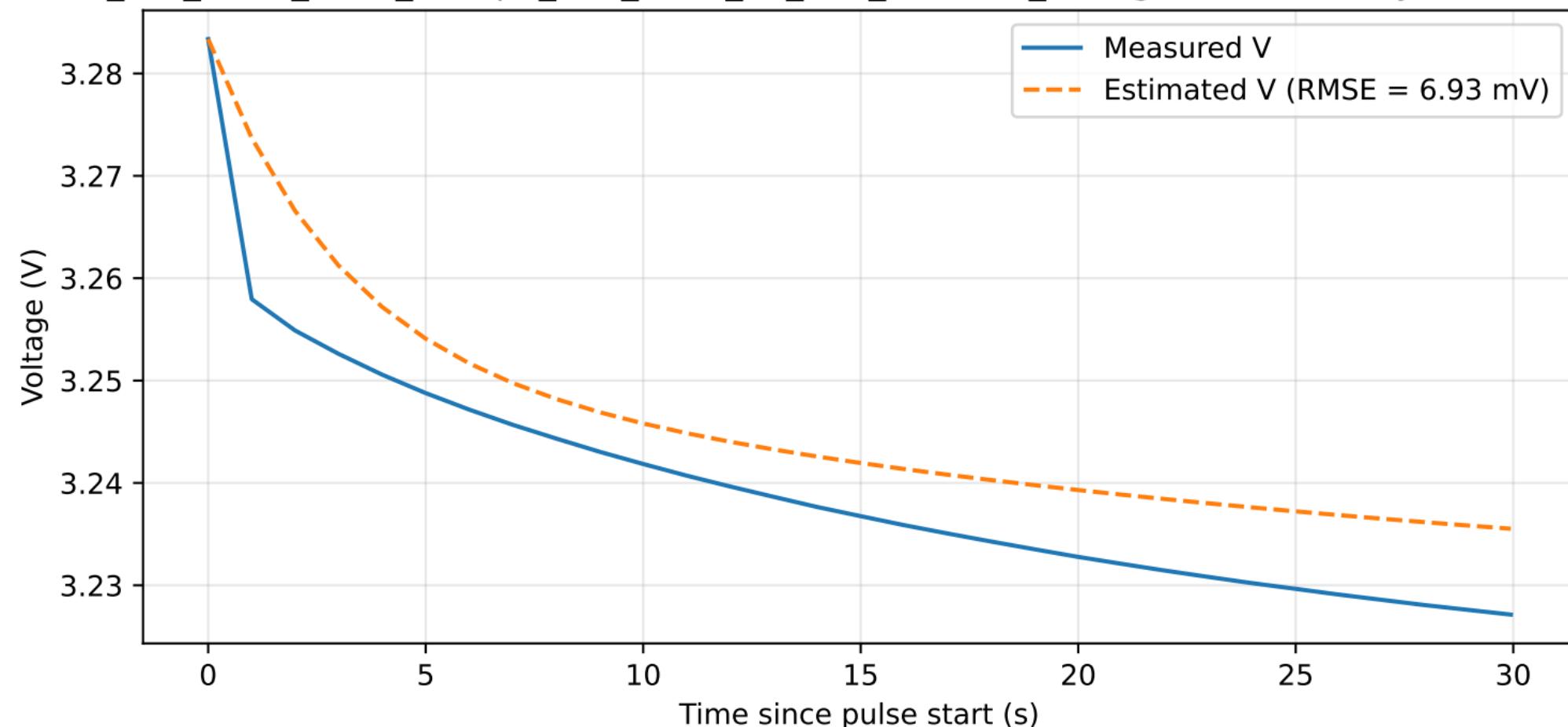
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



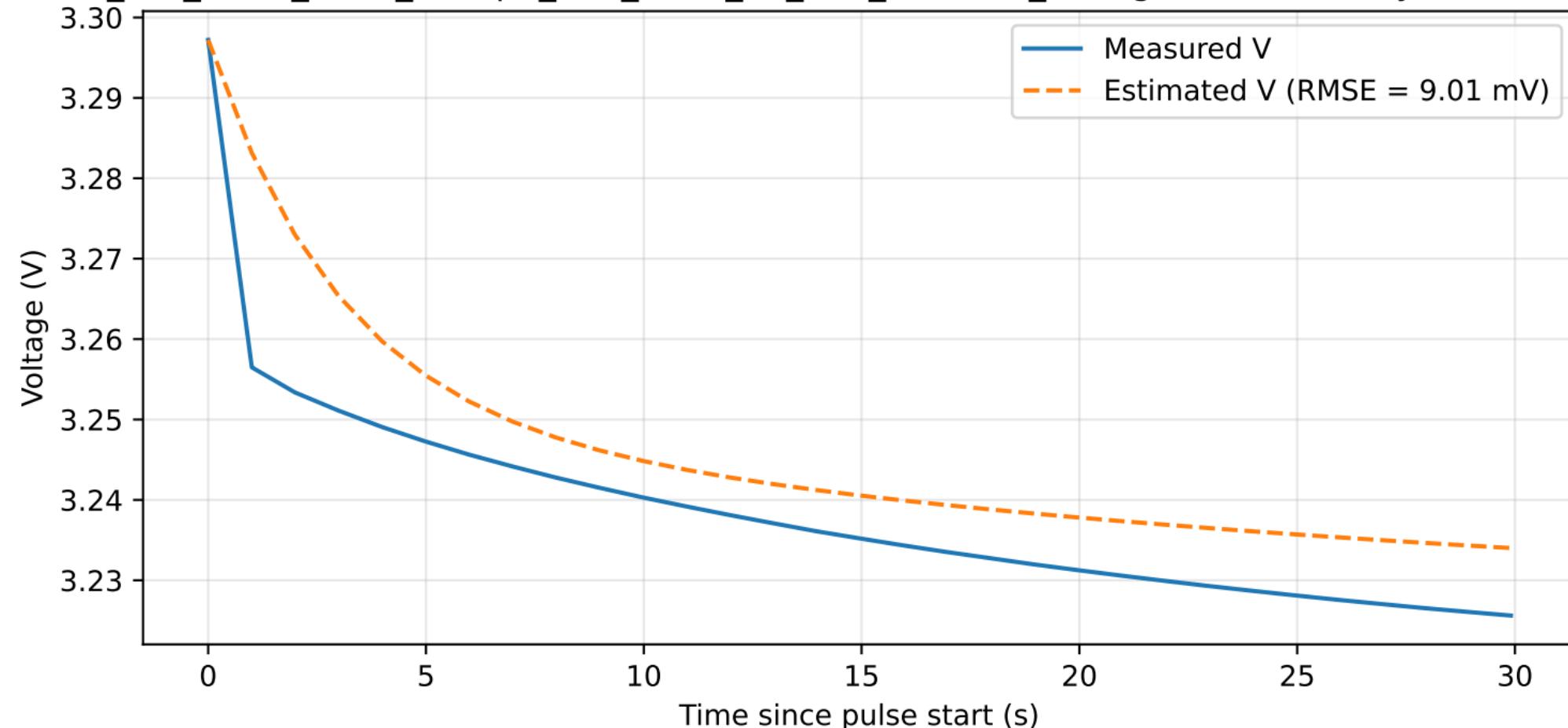
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



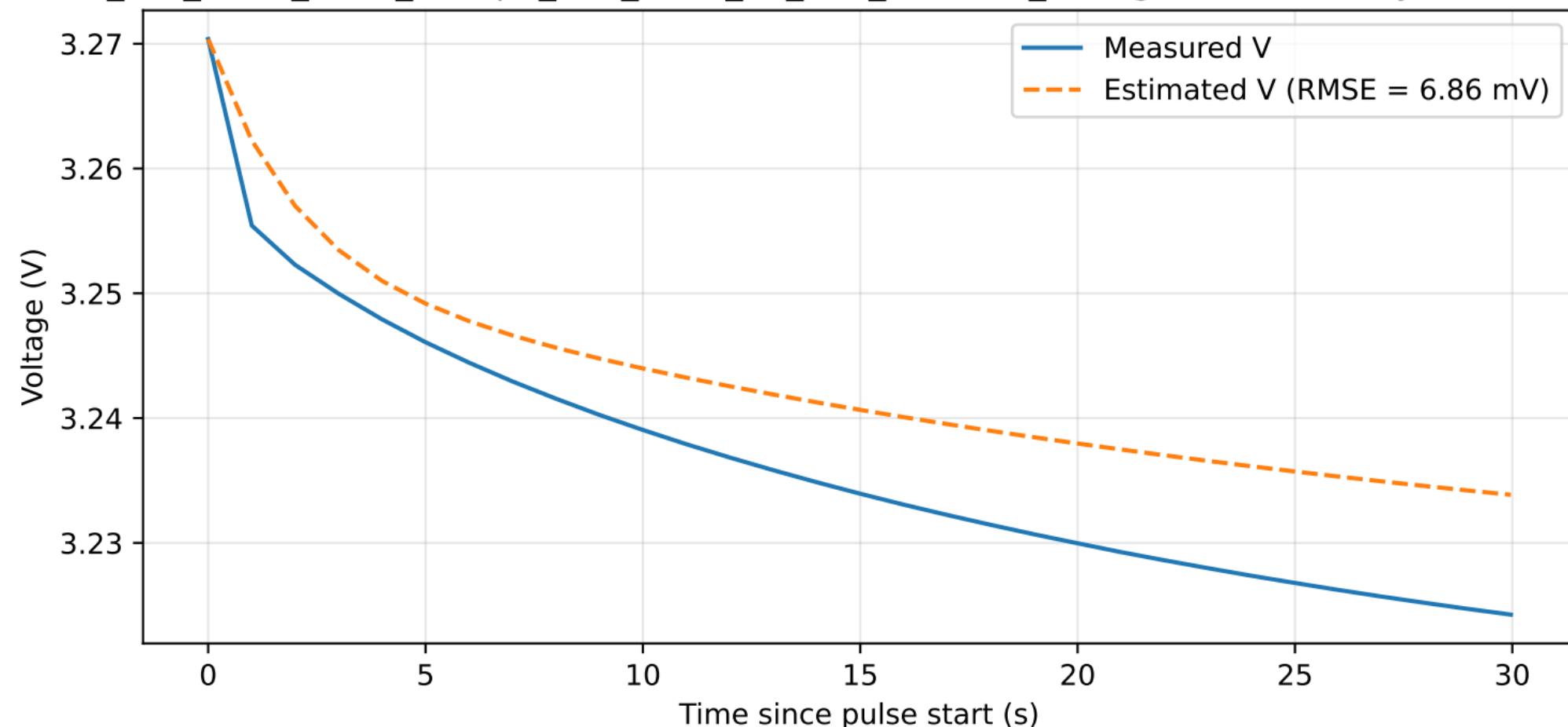
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



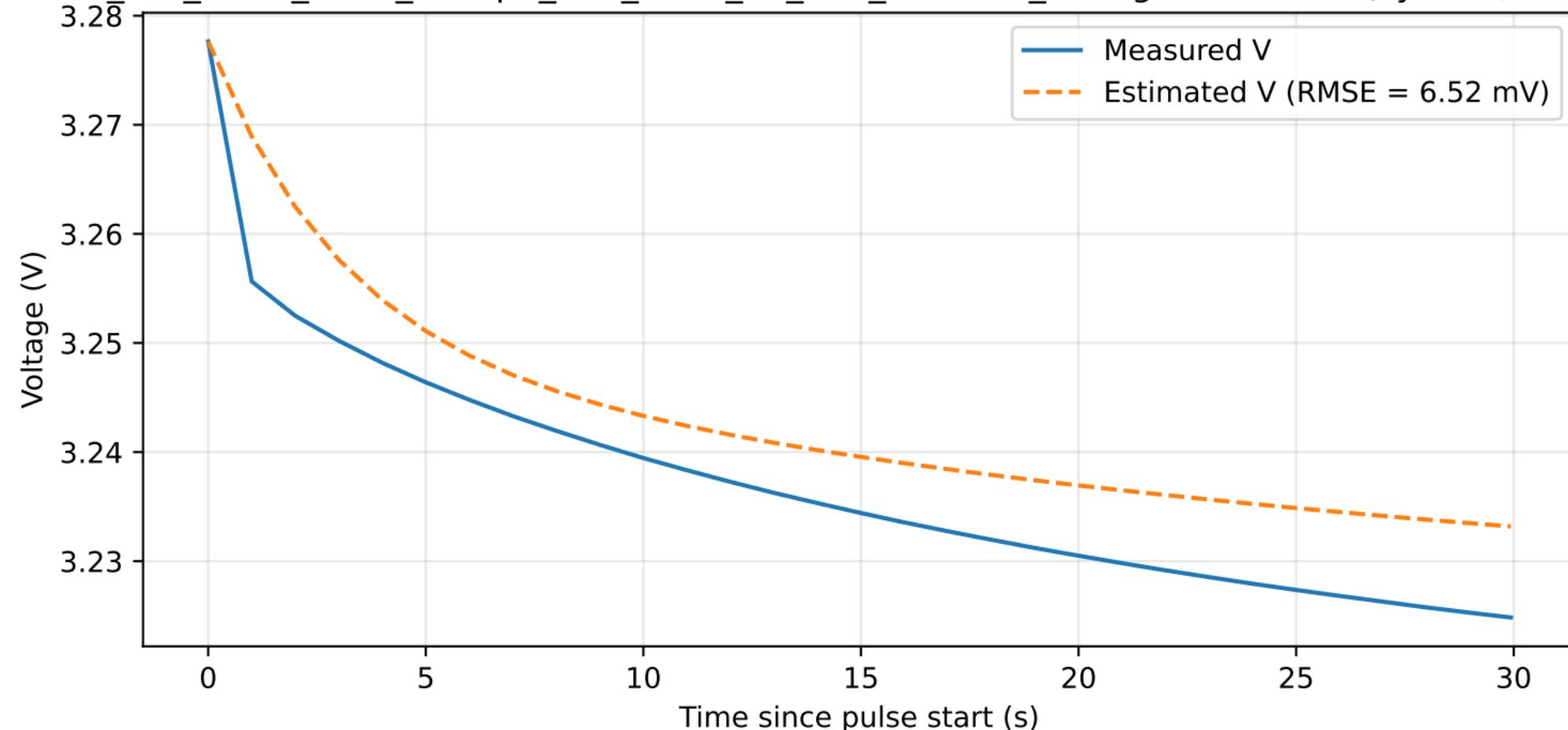
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



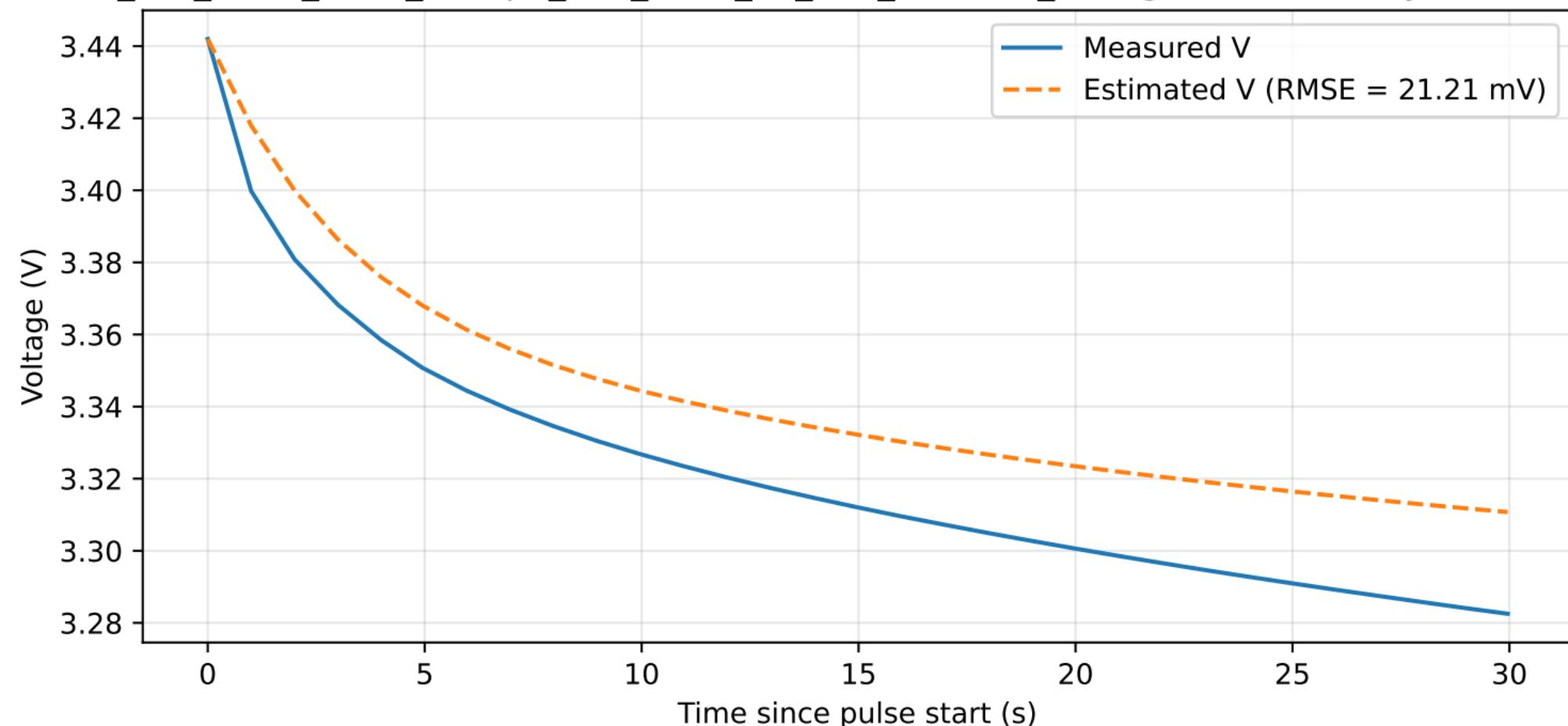
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



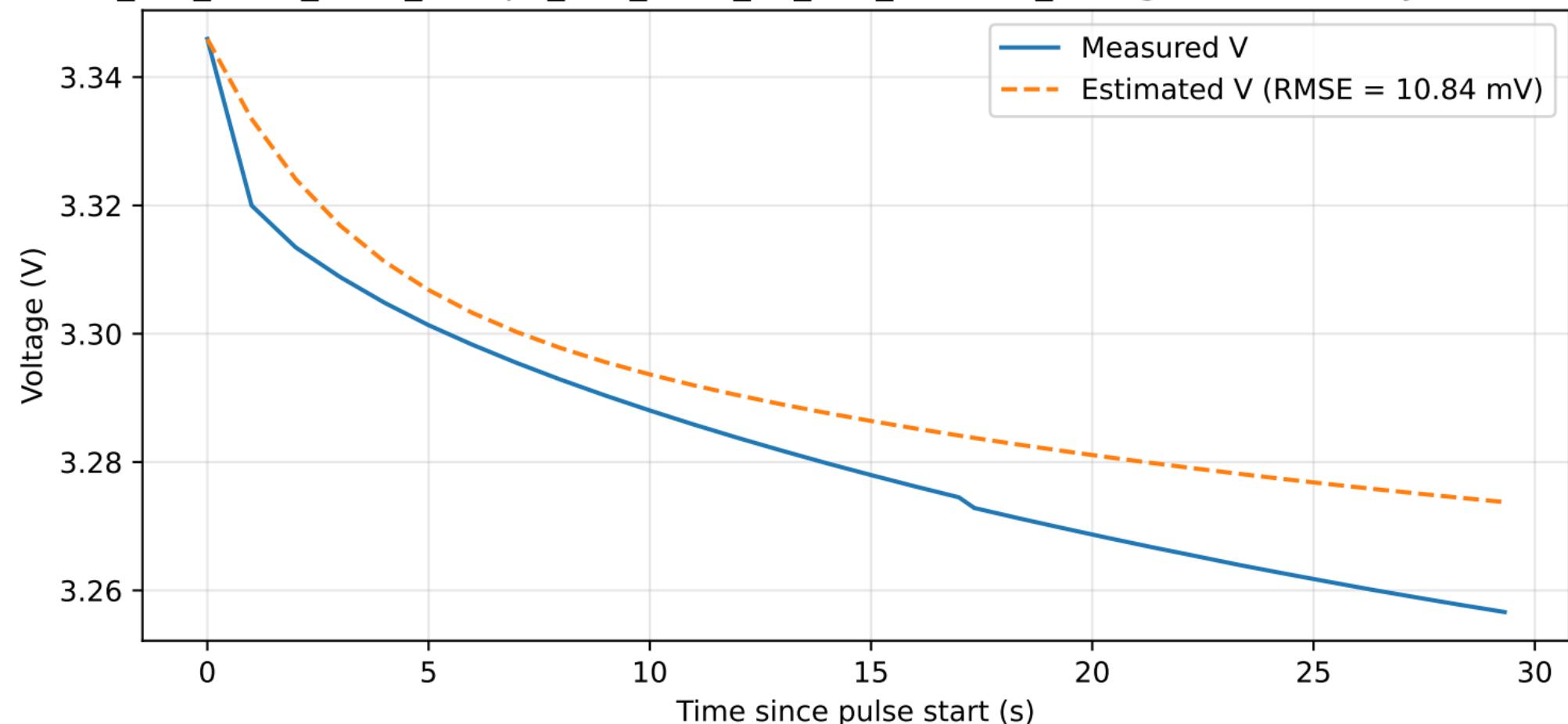
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0078\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



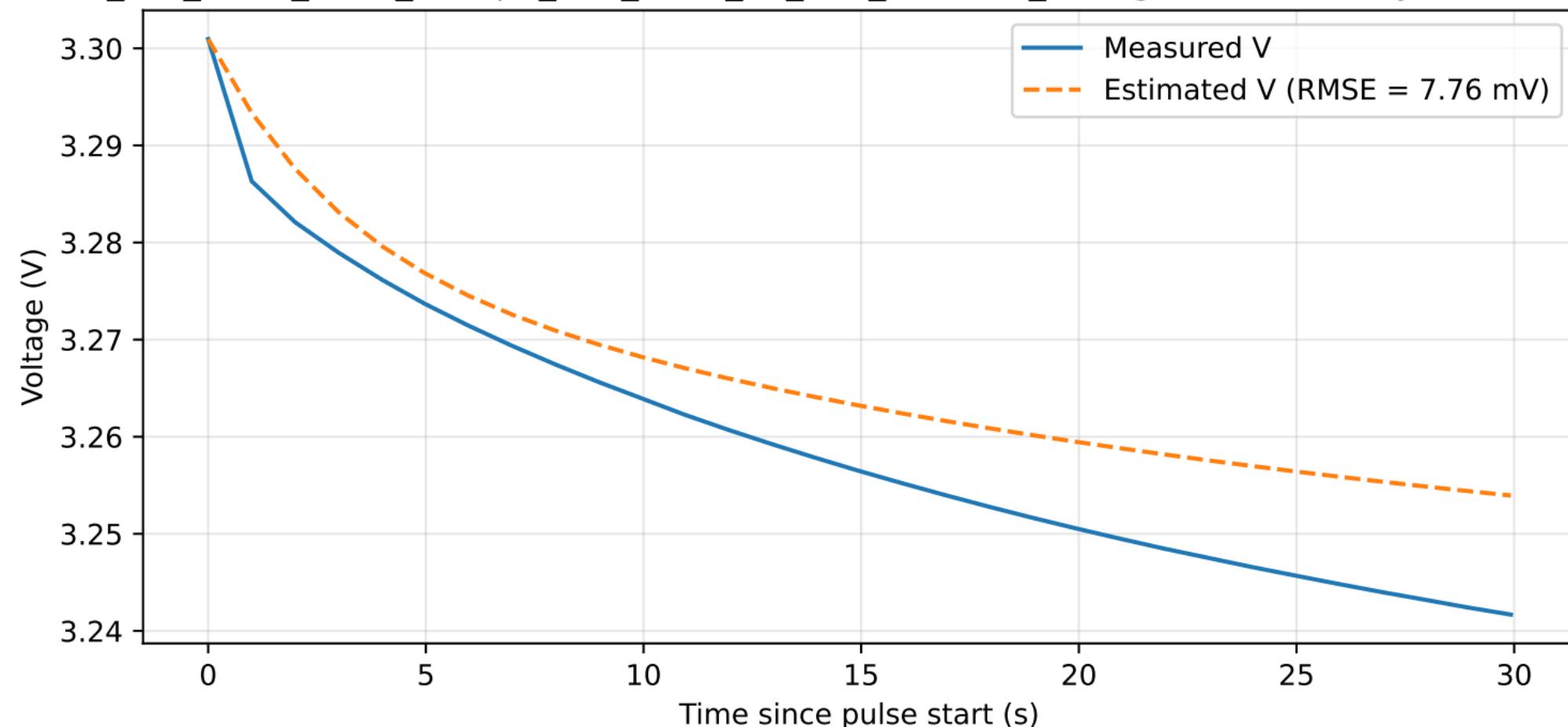
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



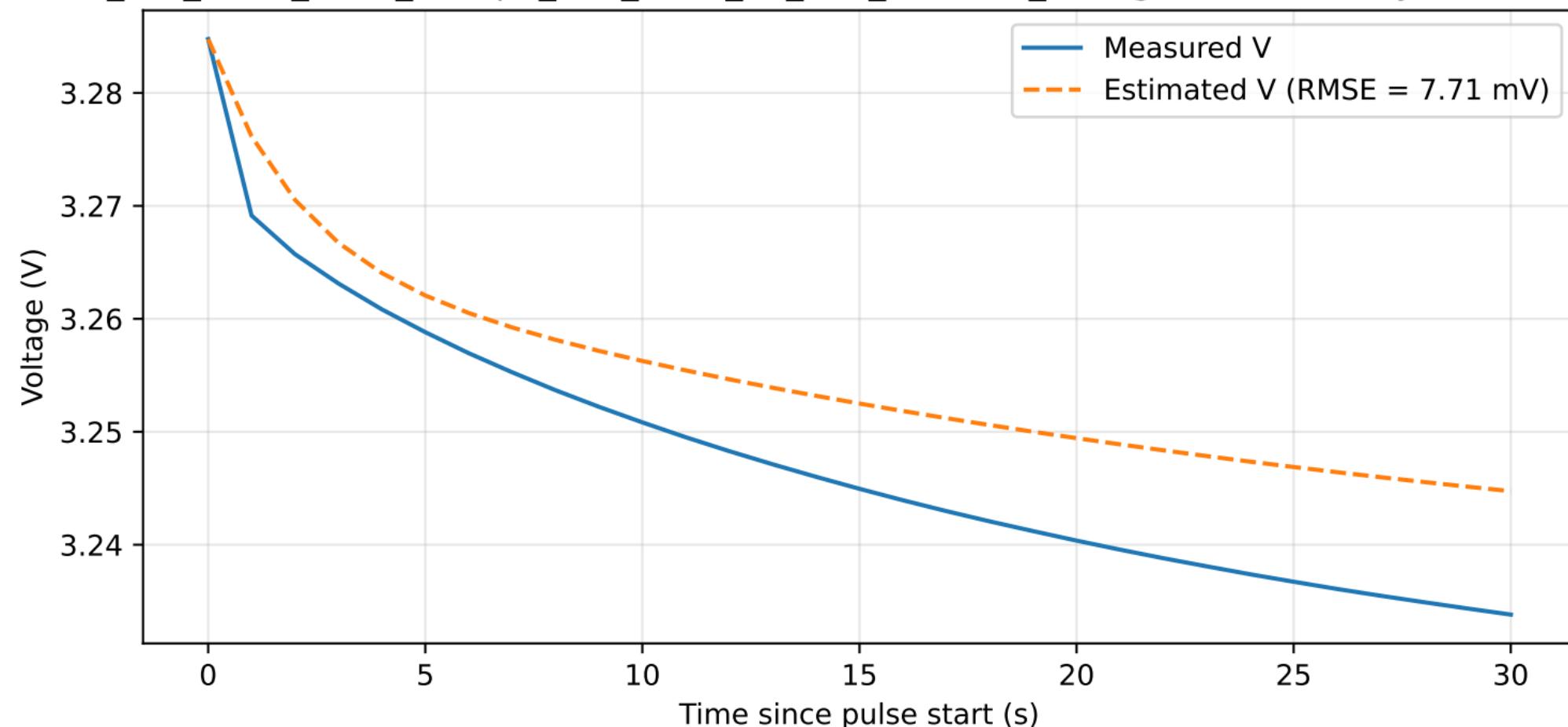
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



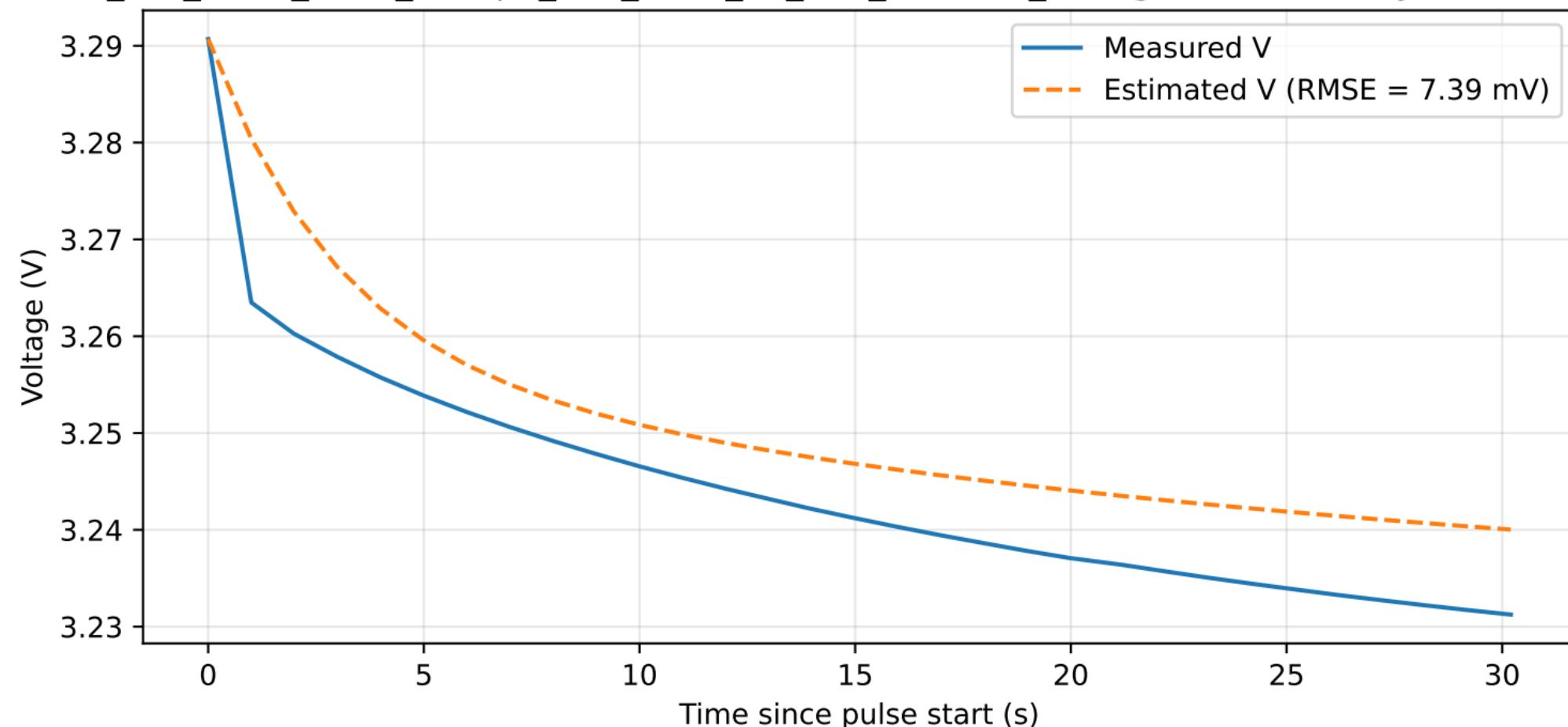
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



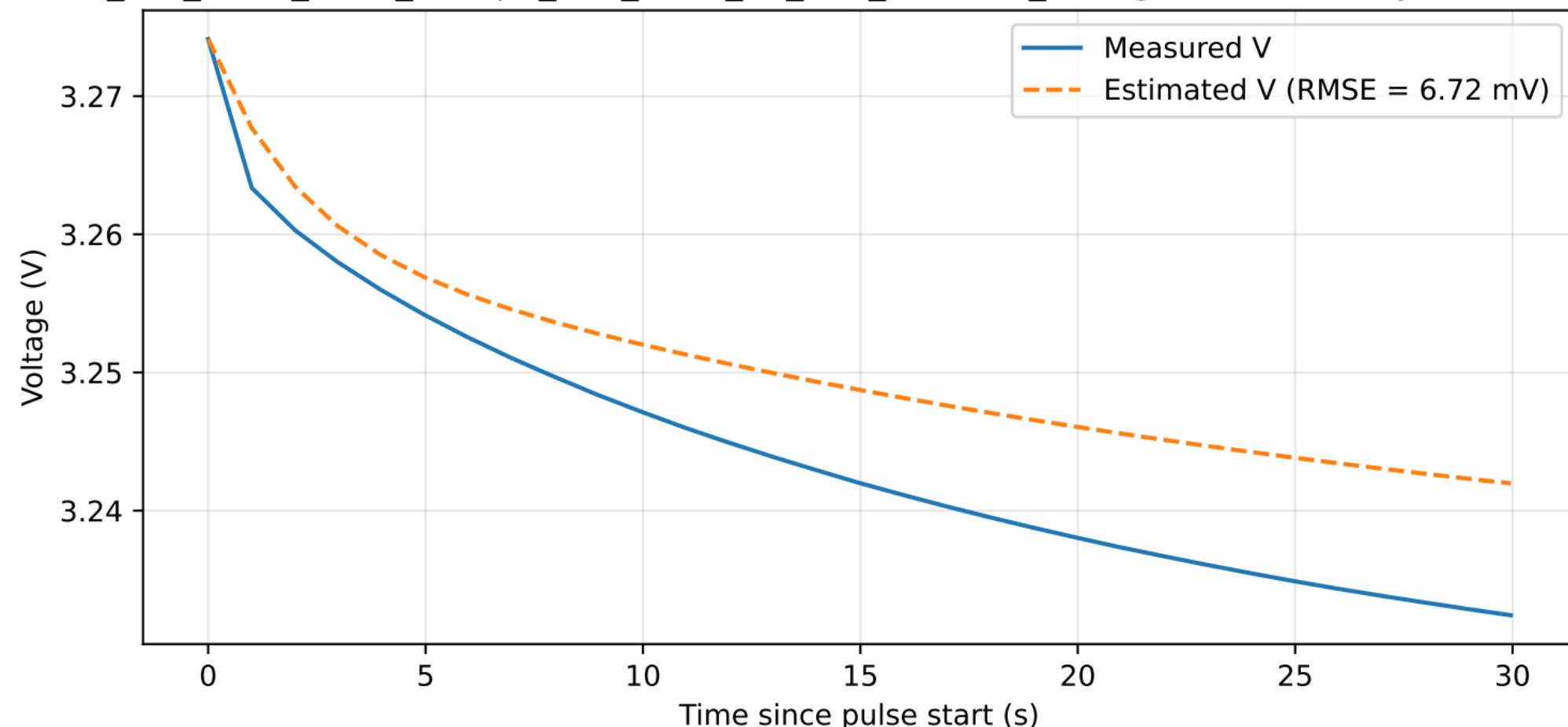
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



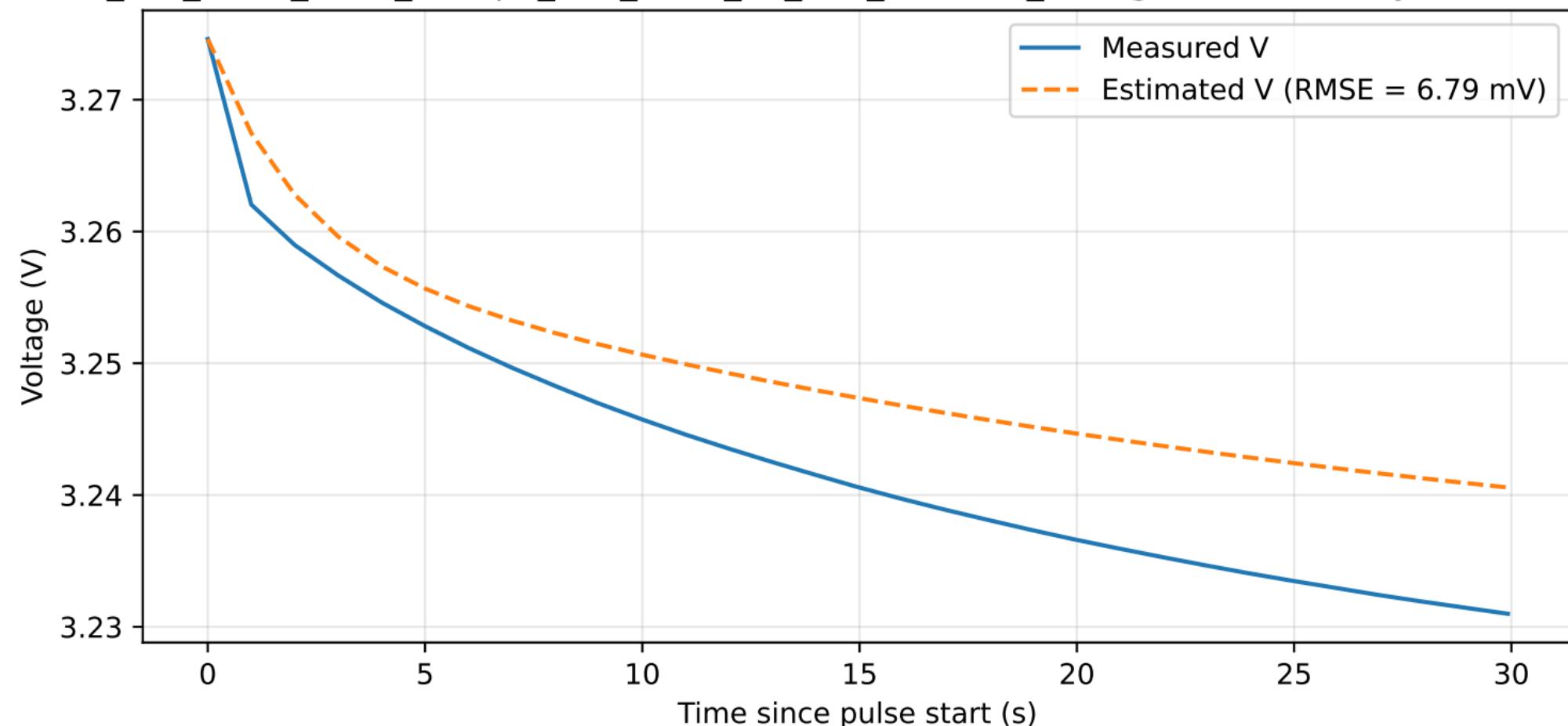
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



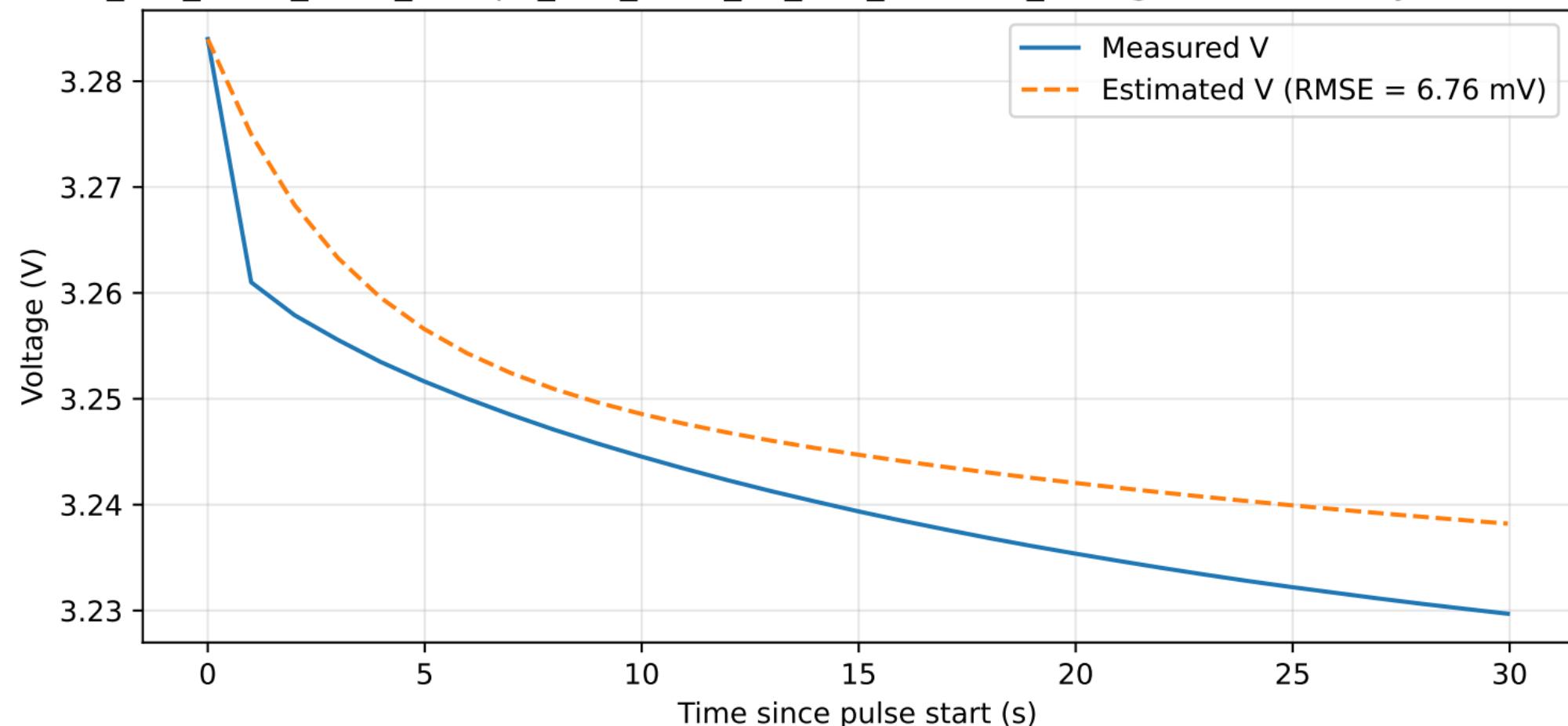
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



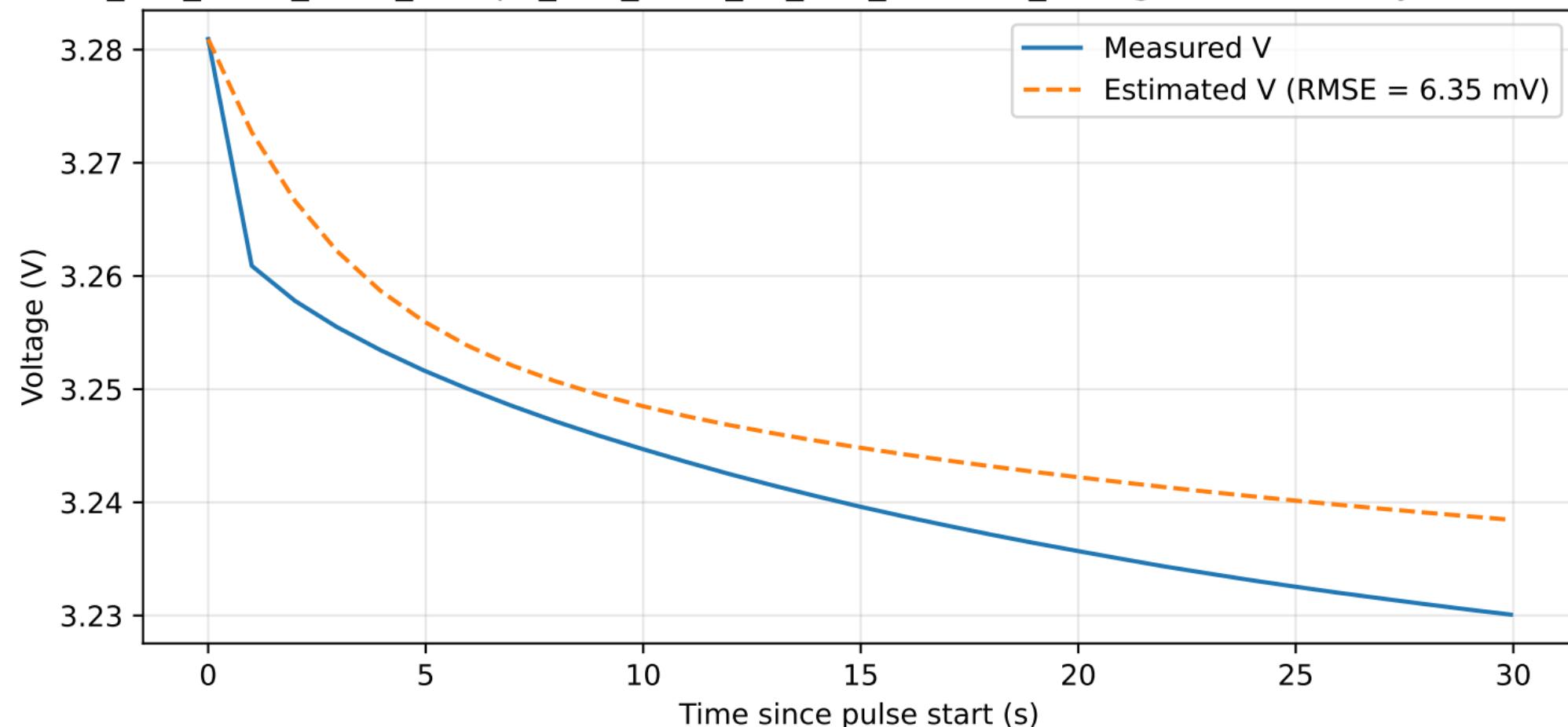
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



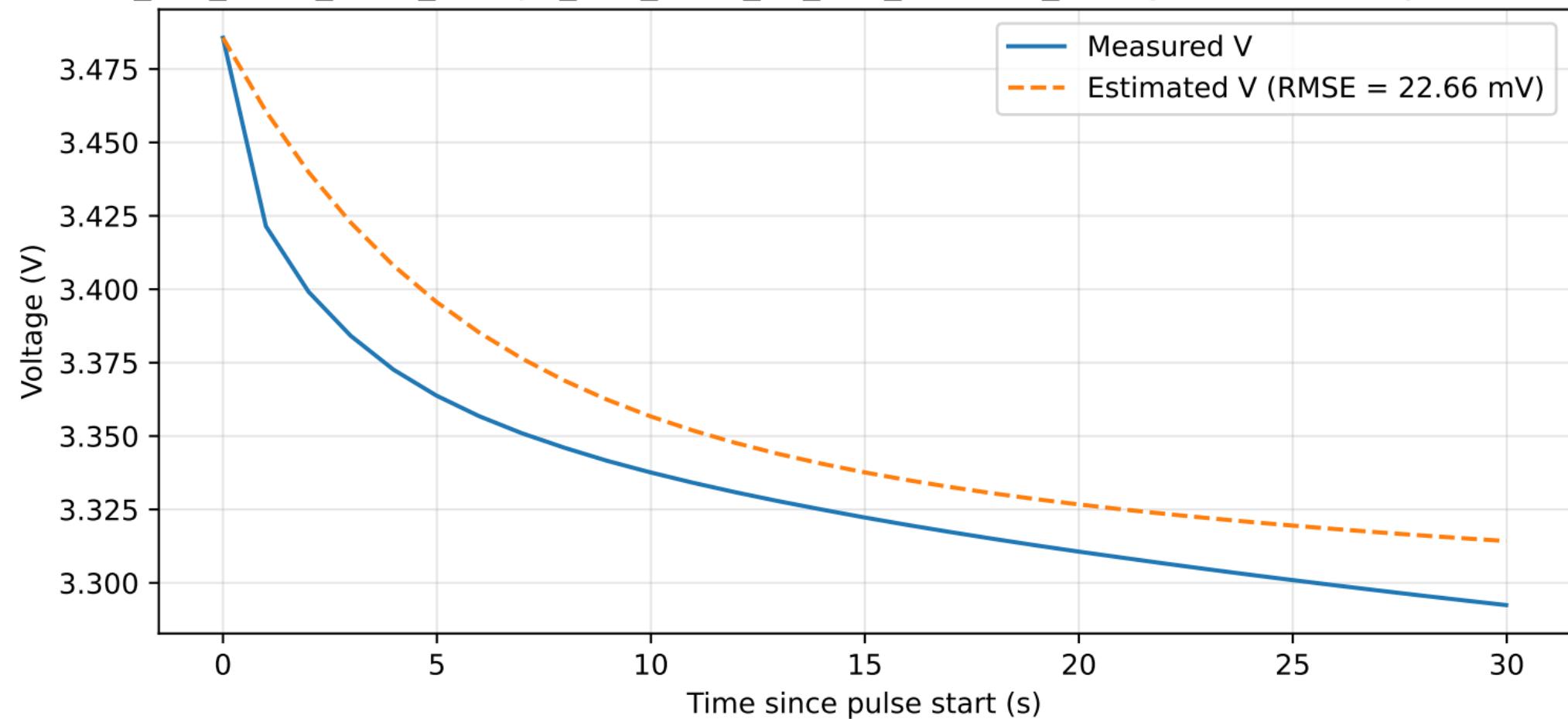
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



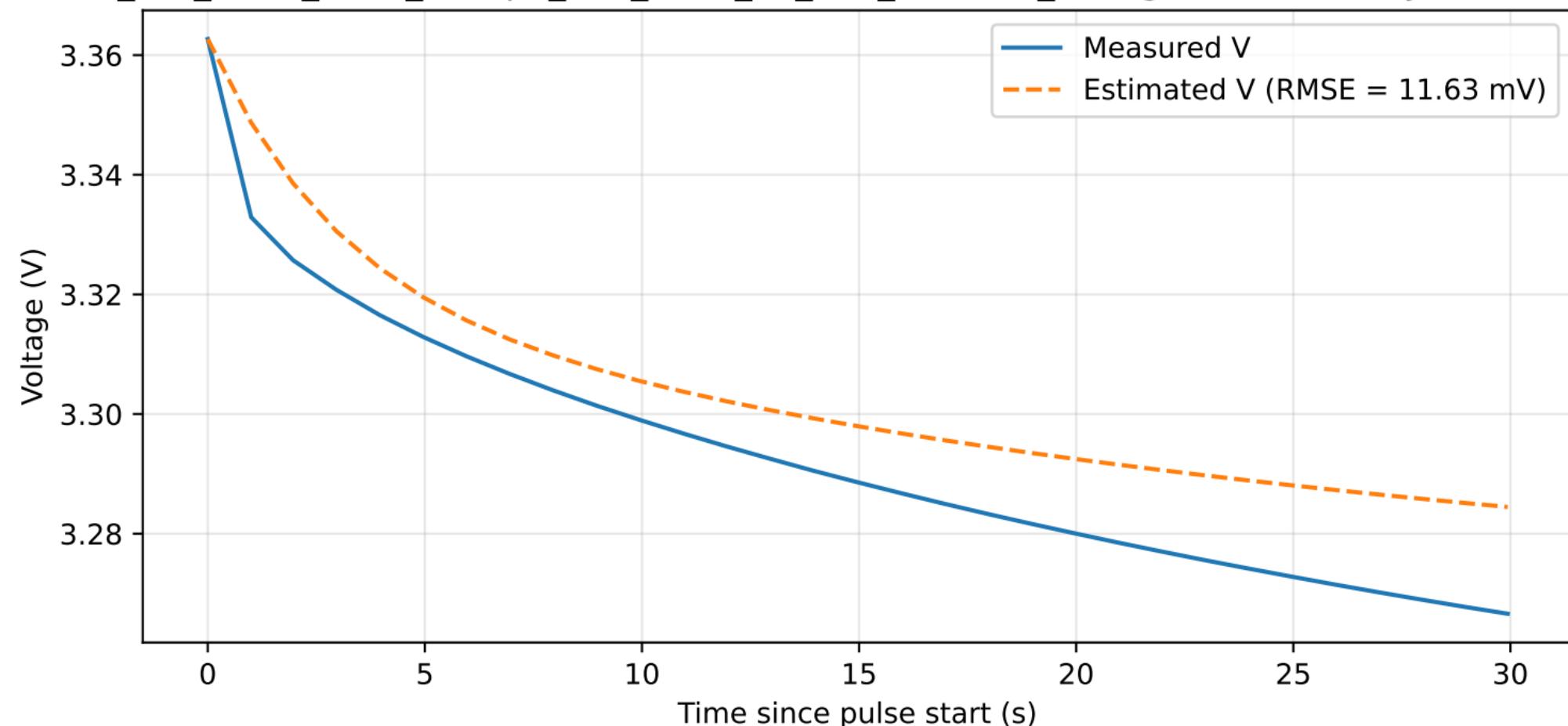
# RD\_LFP\_HPPC\_REPT\_Group1\_150\_0080\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



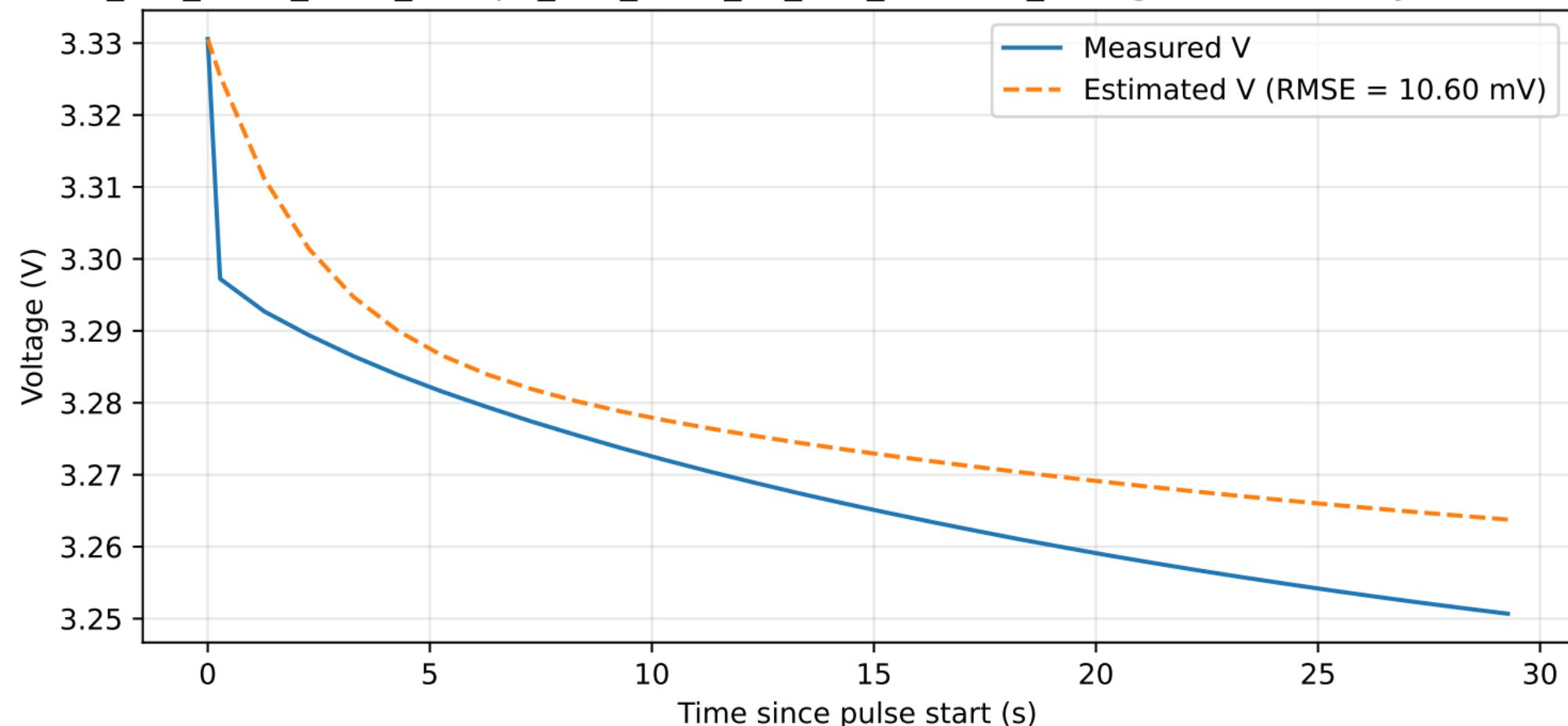
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



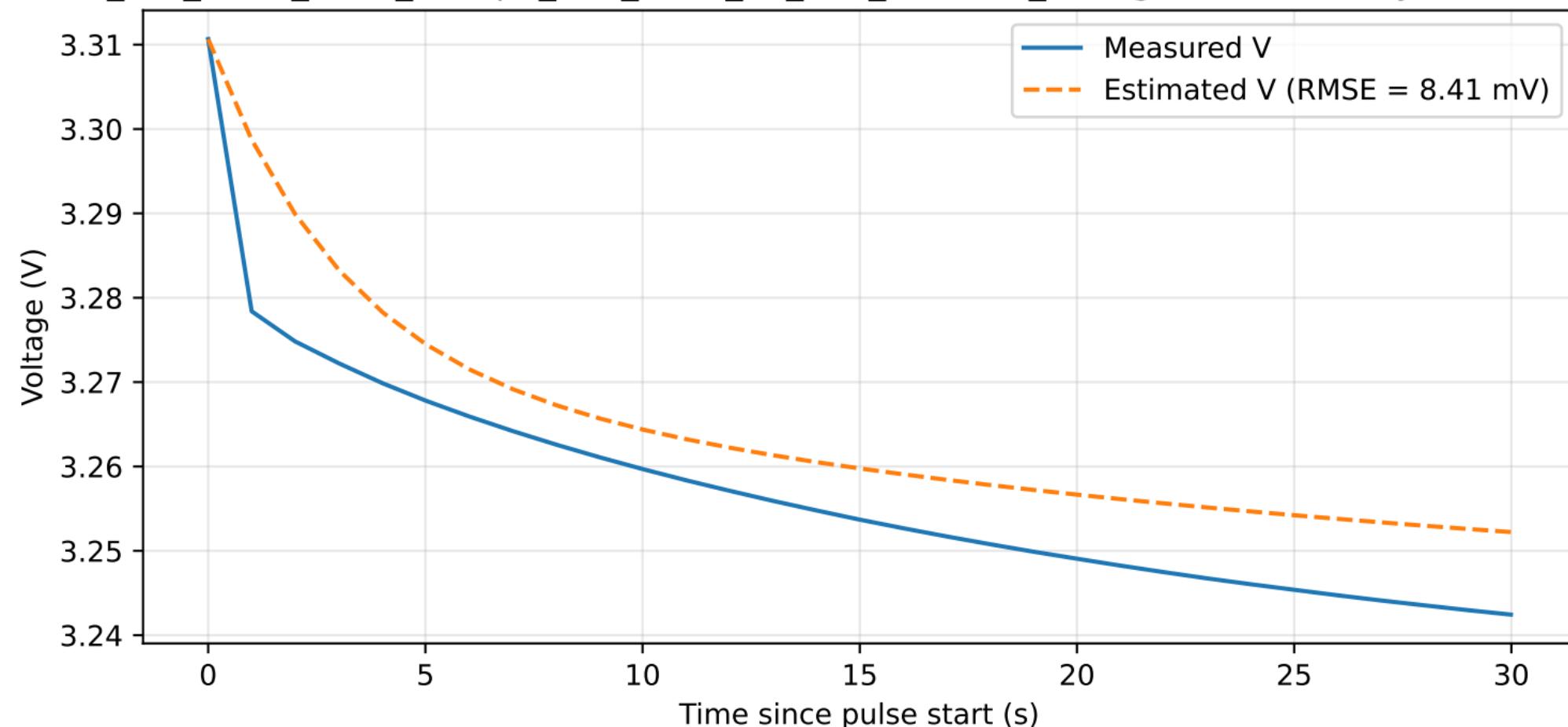
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



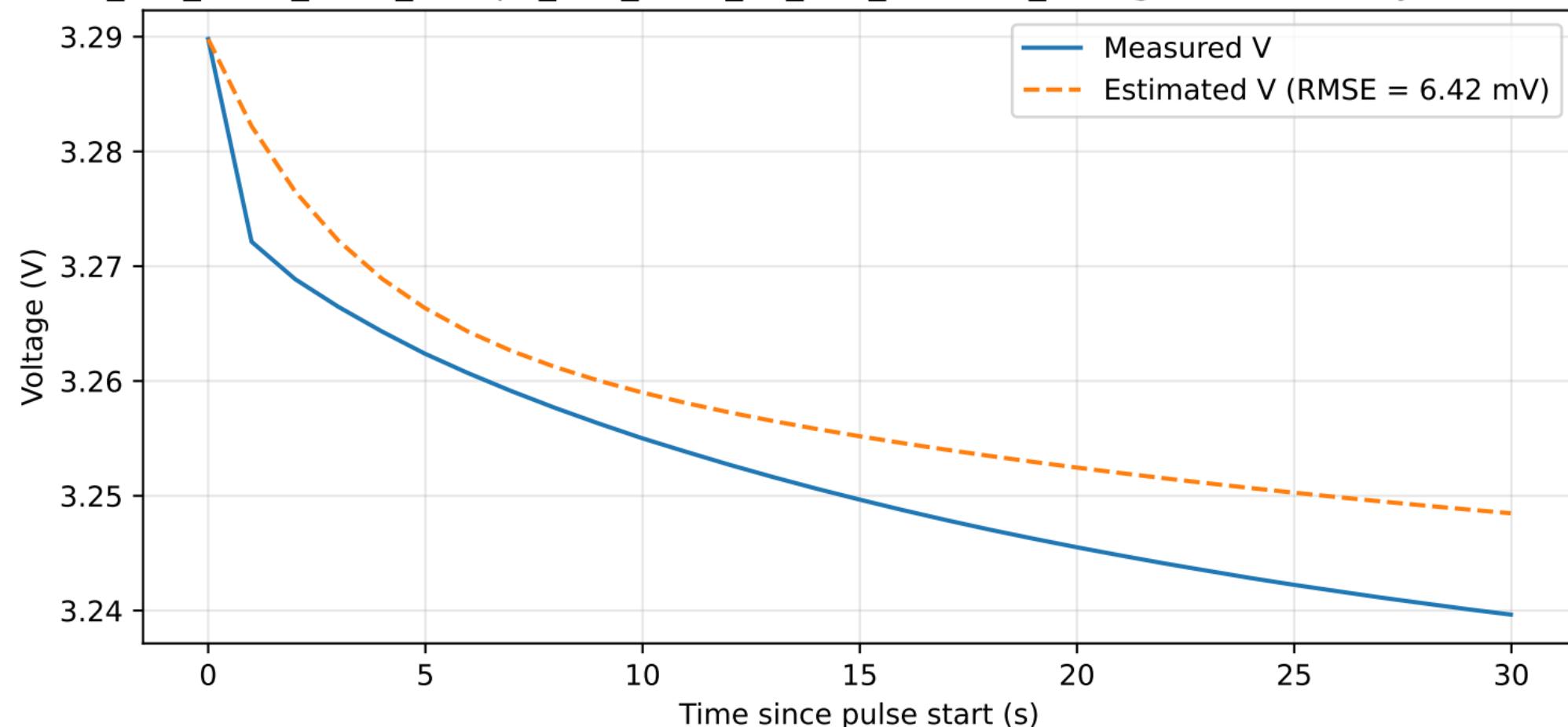
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



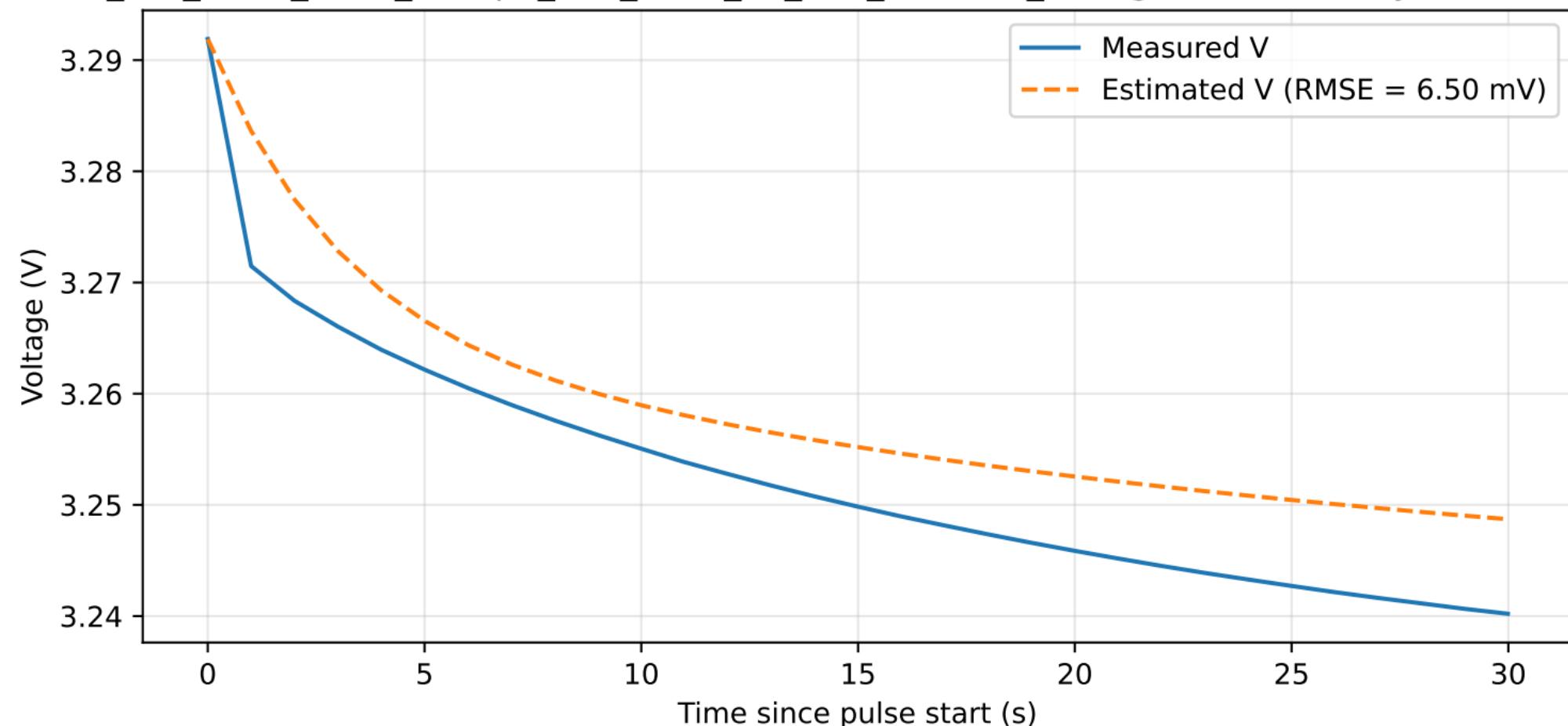
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



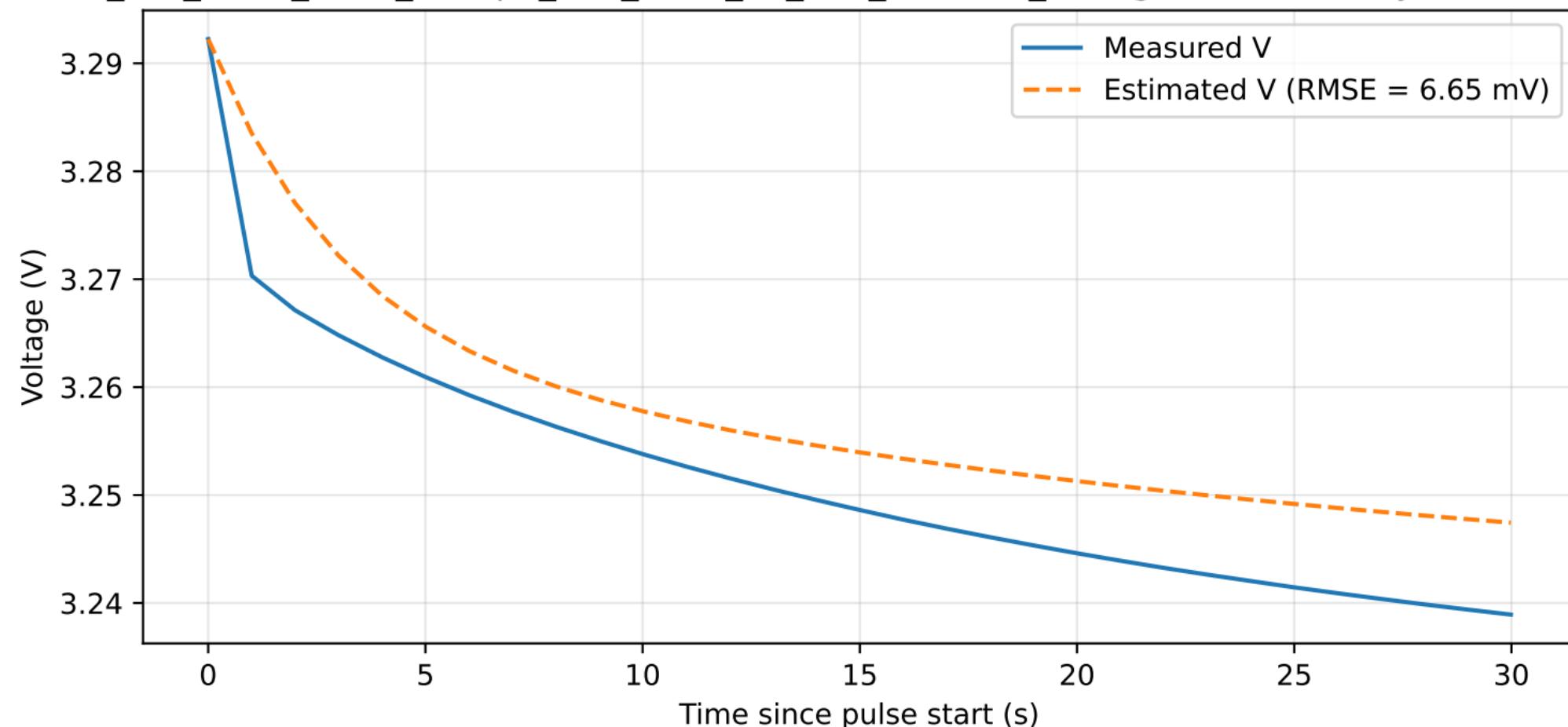
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



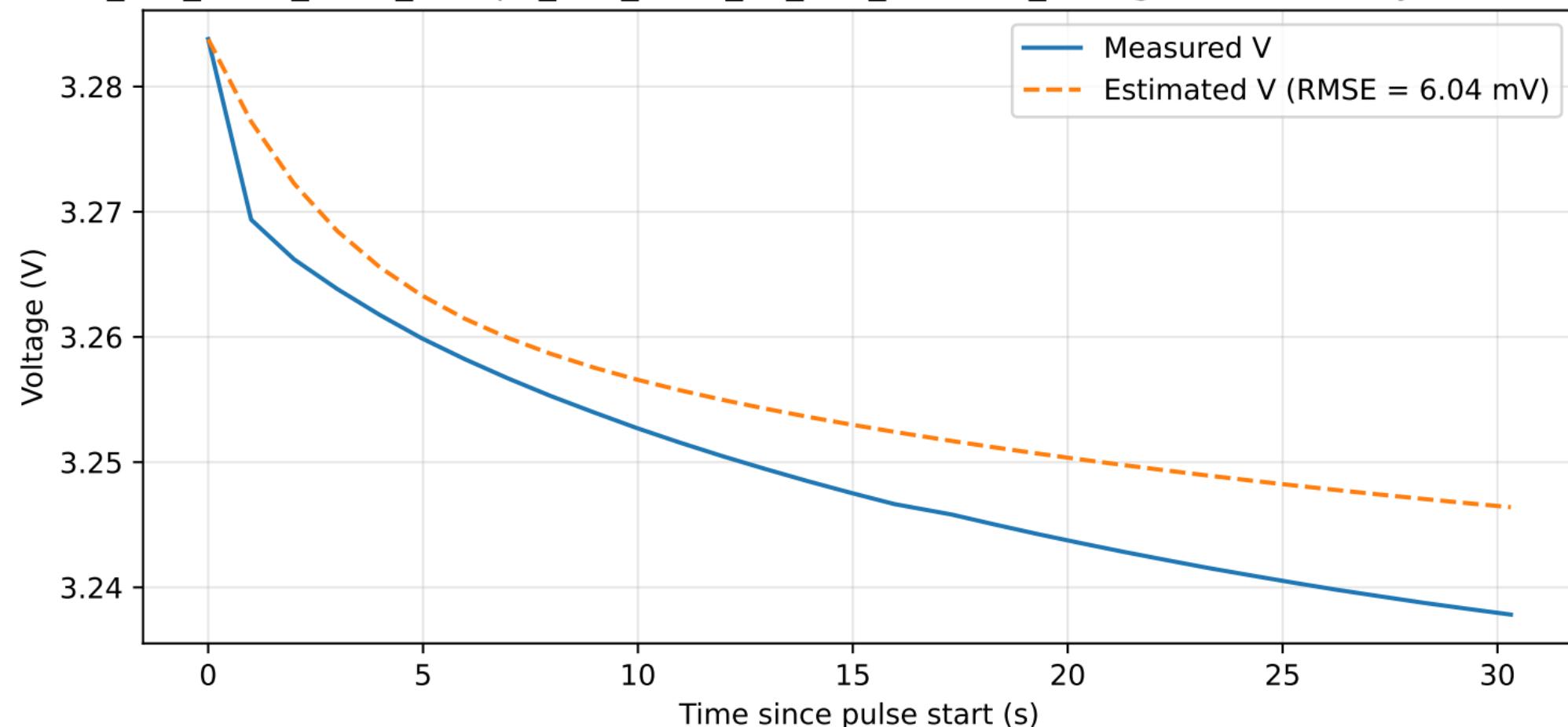
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



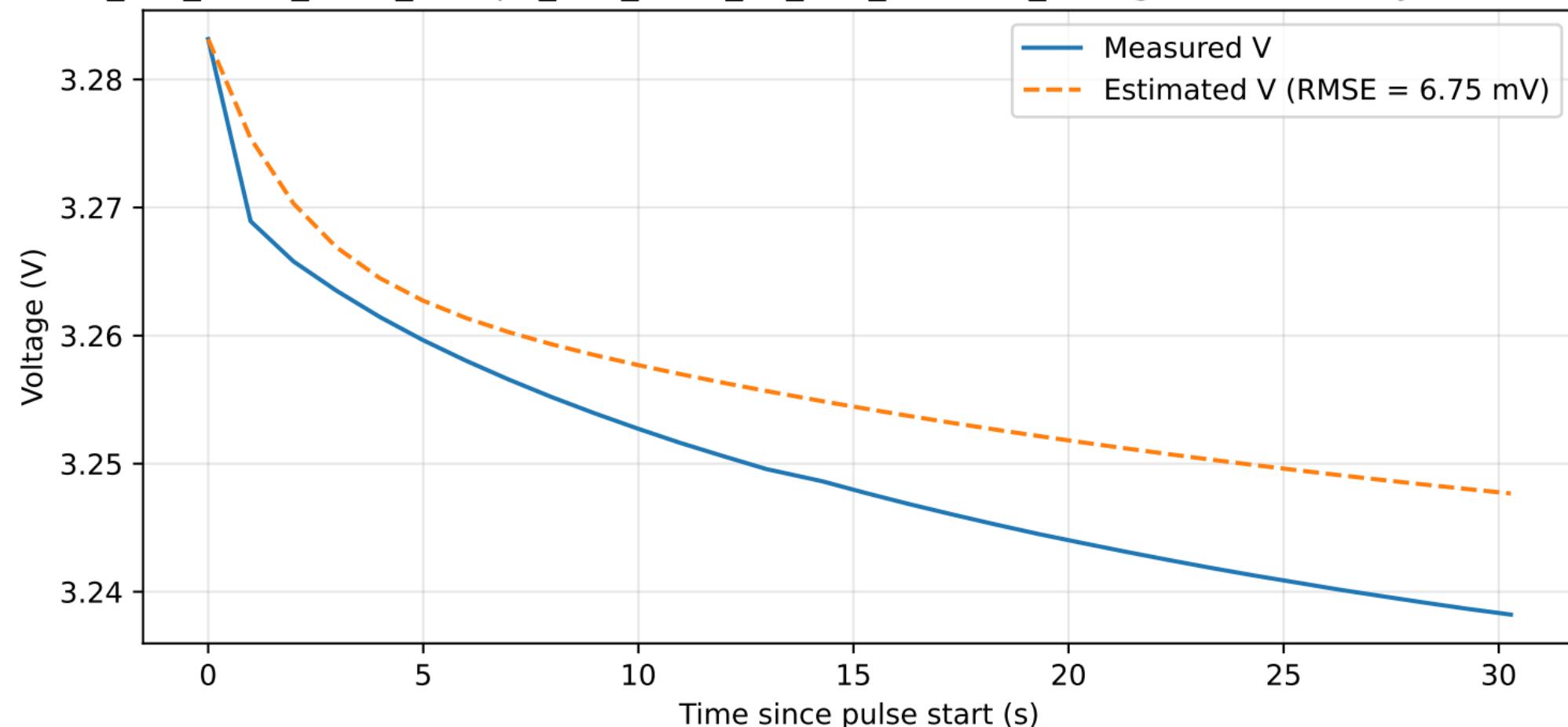
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



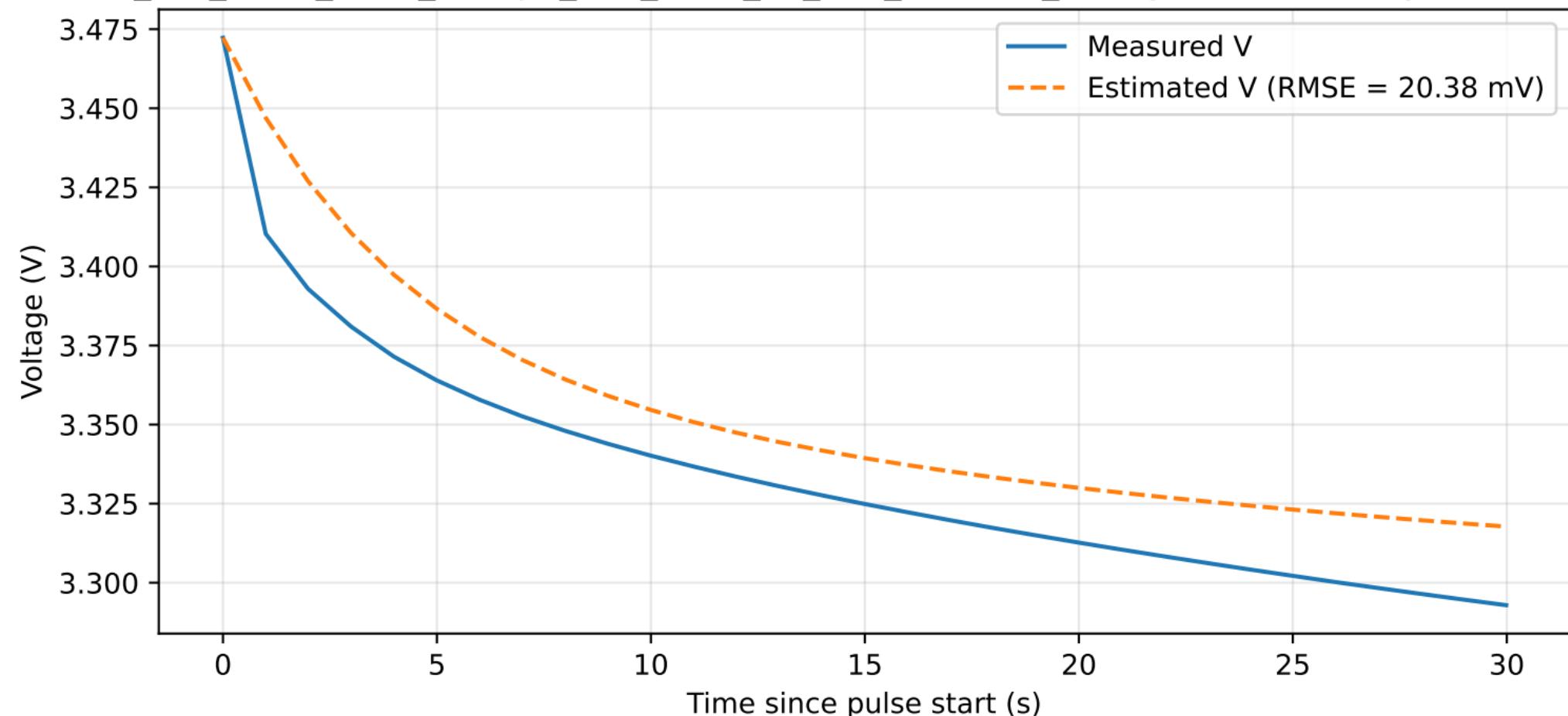
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



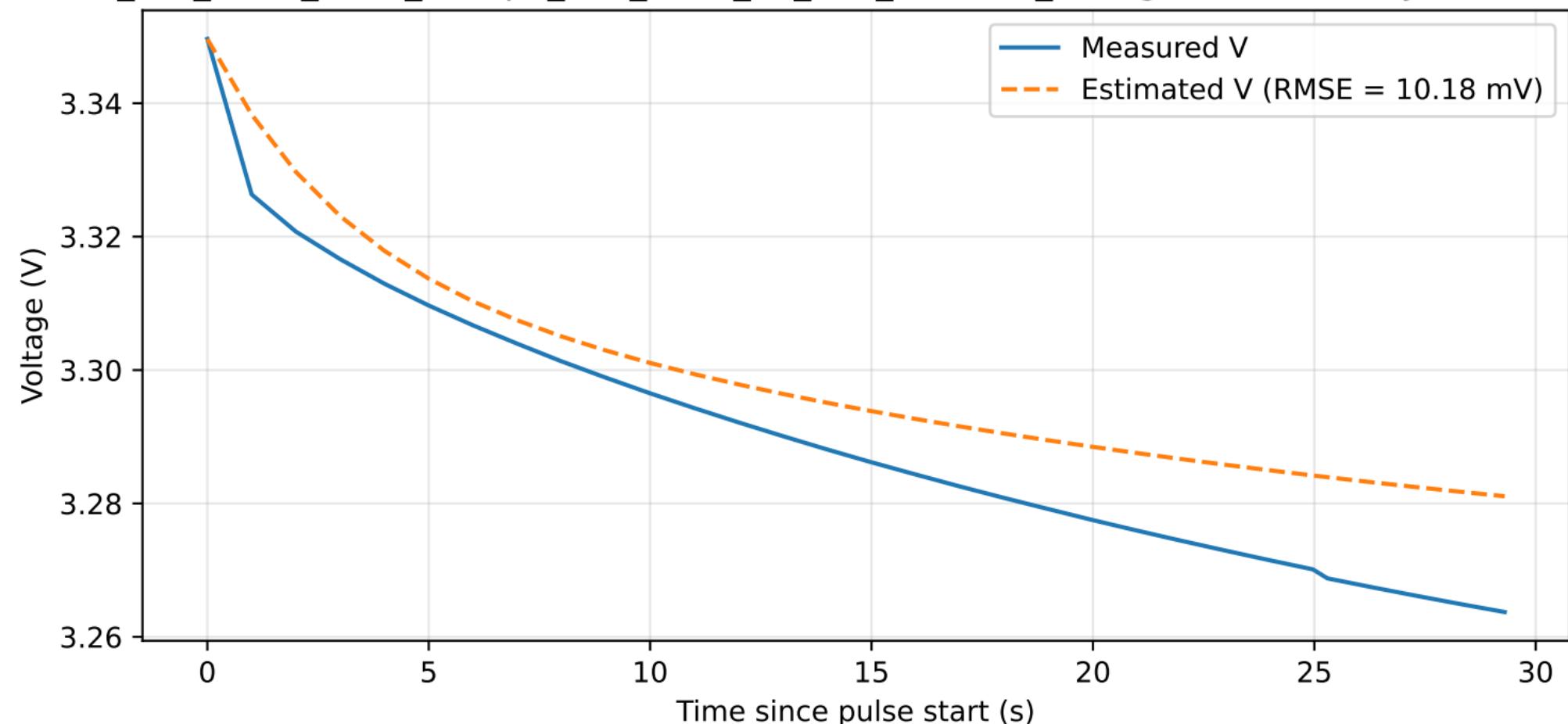
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0012\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



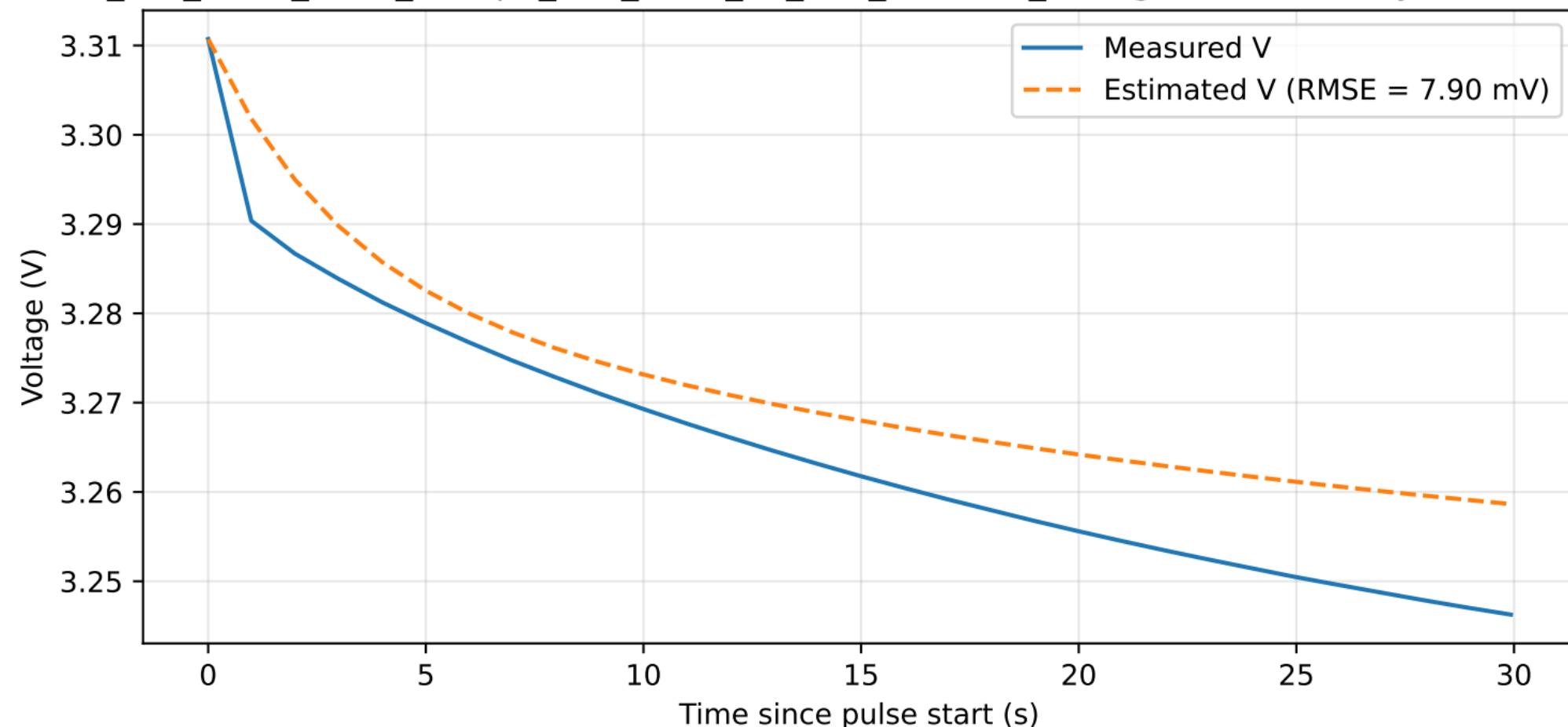
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



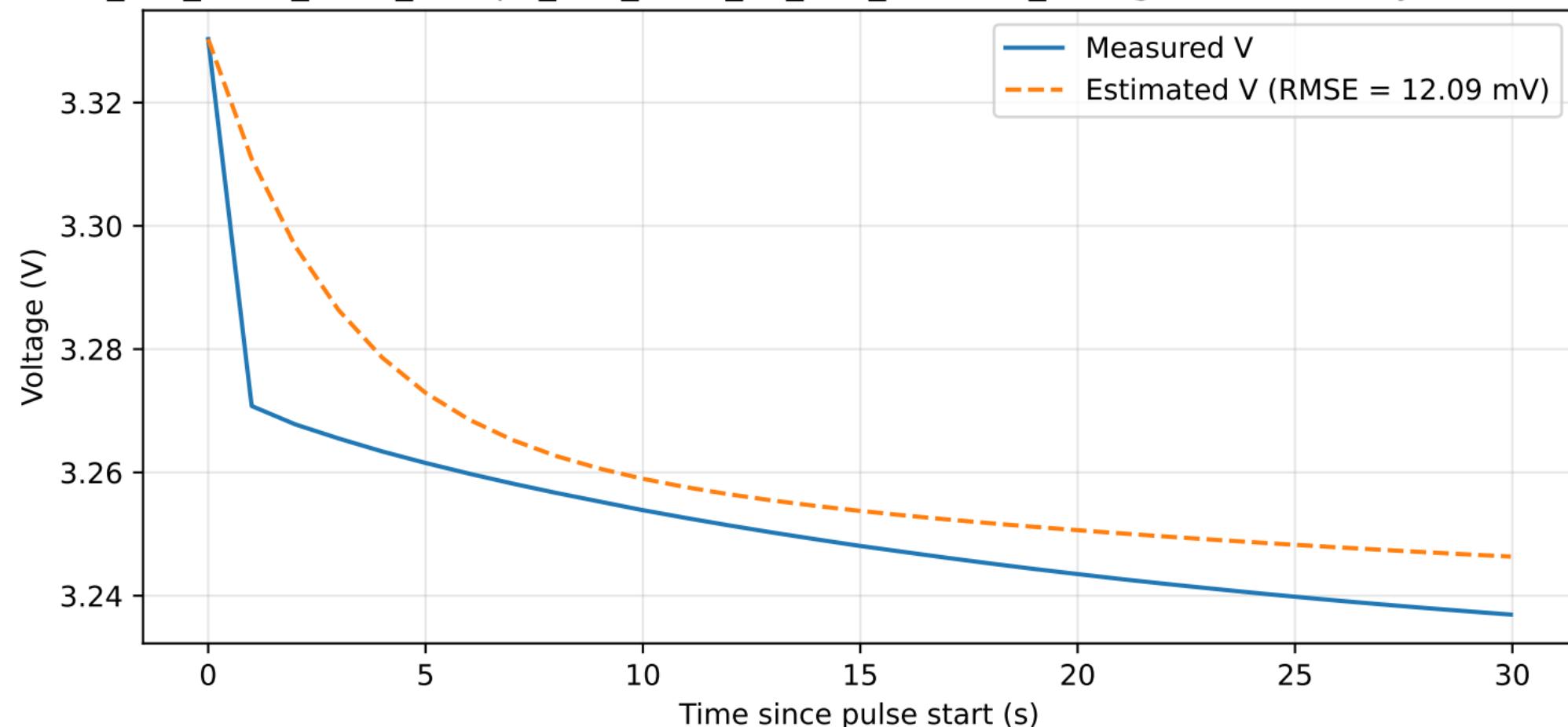
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



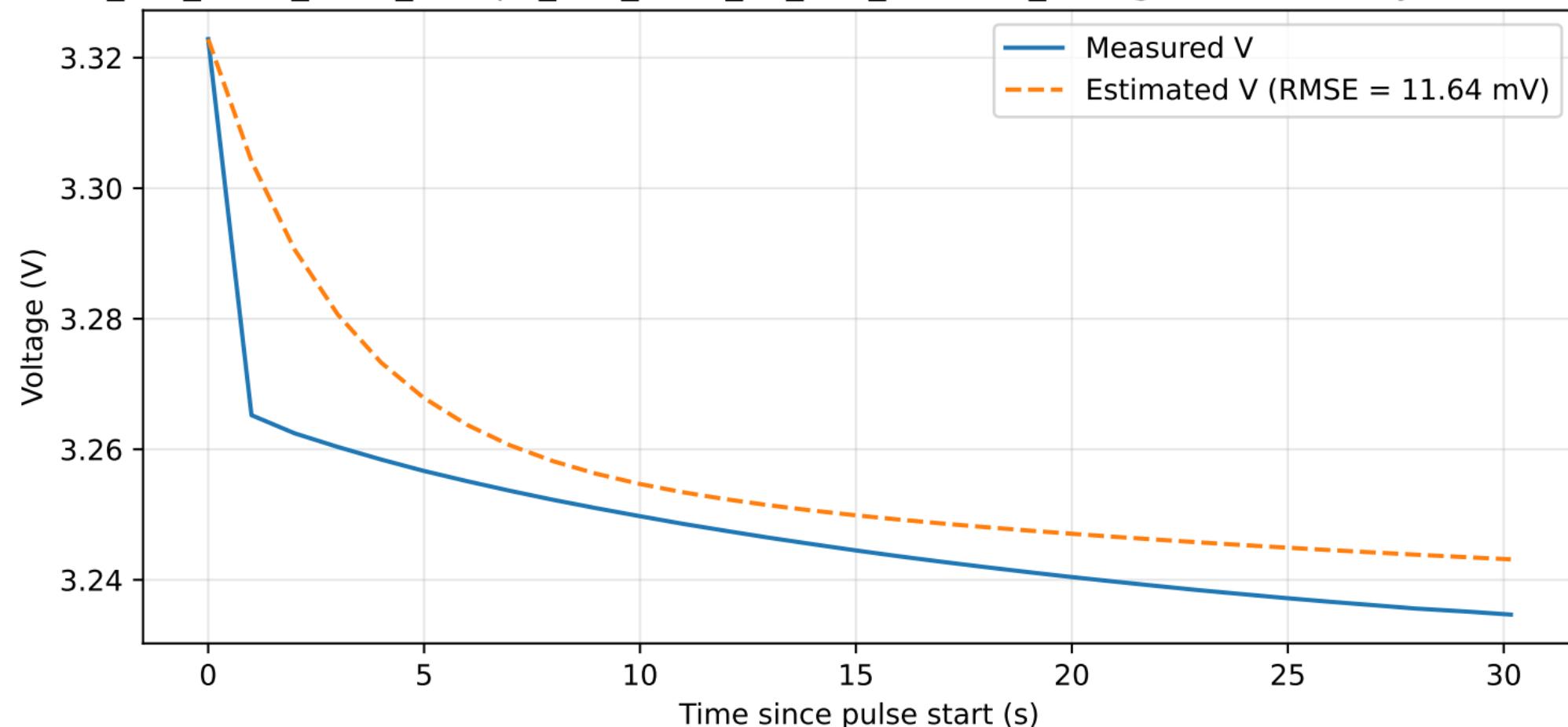
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



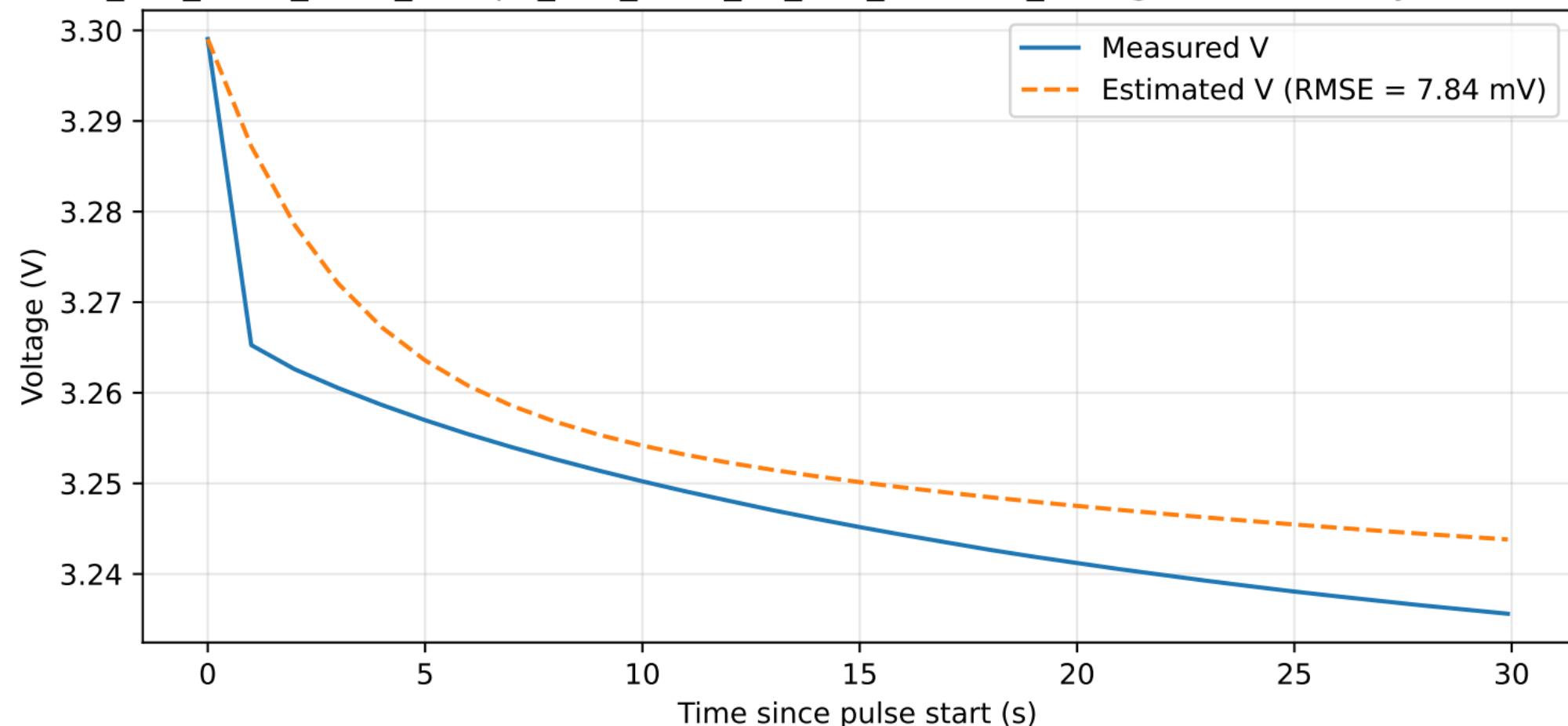
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



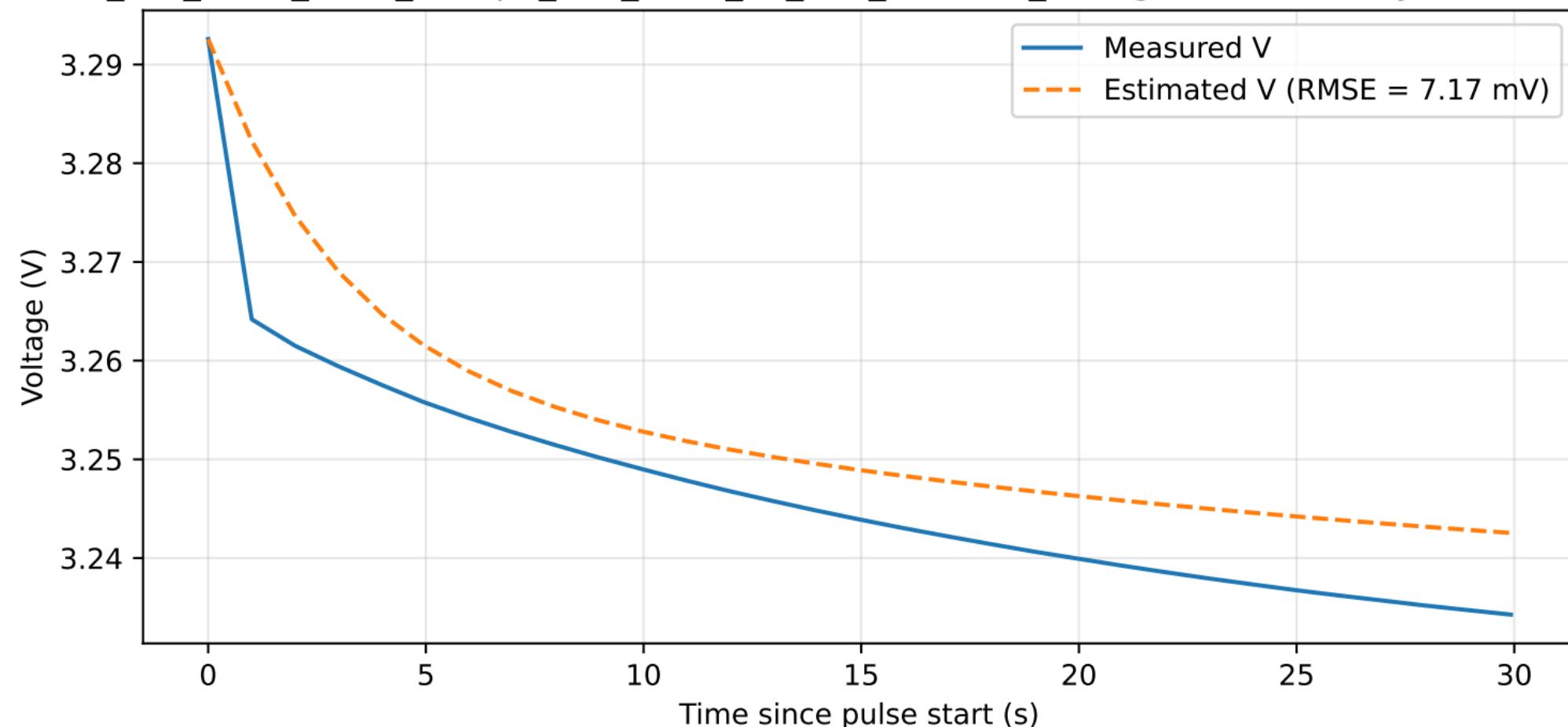
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



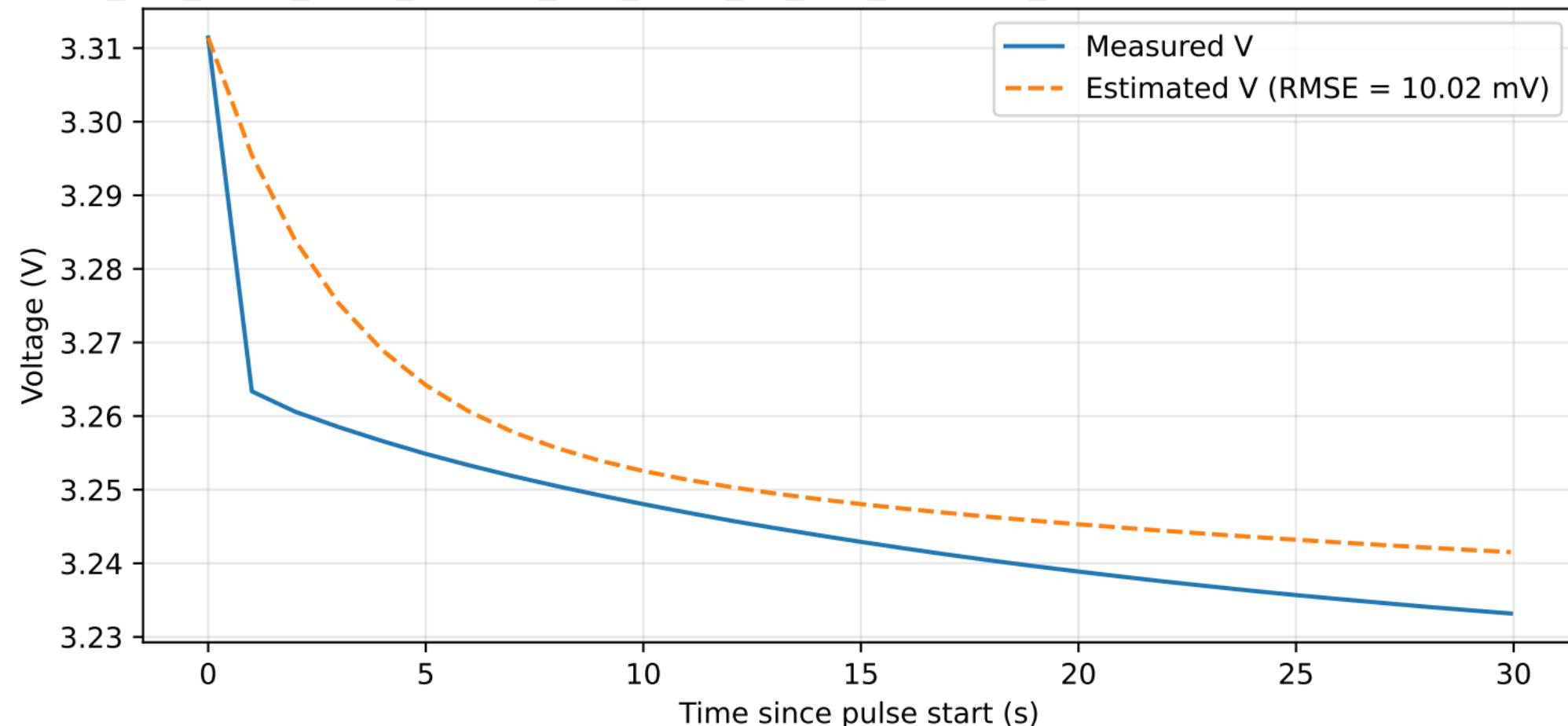
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



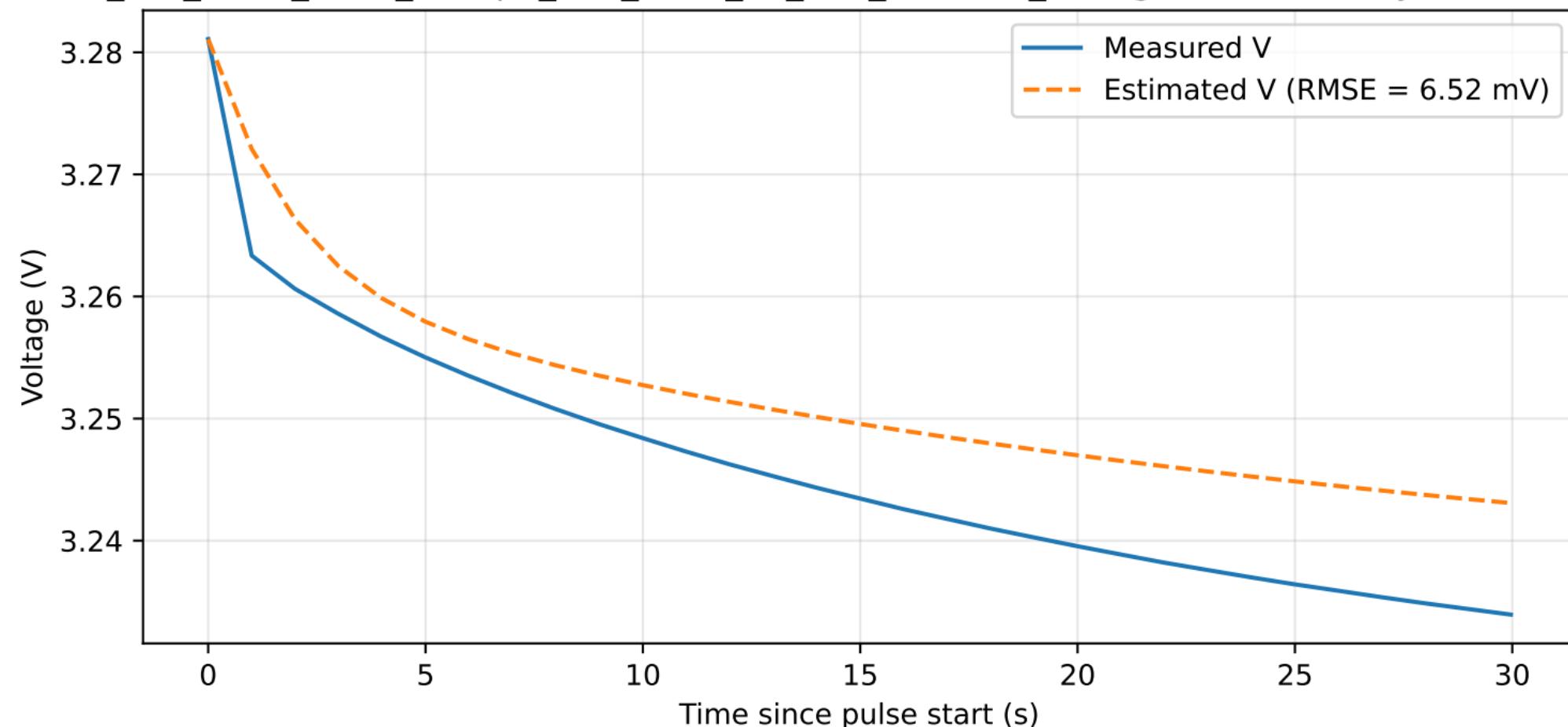
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



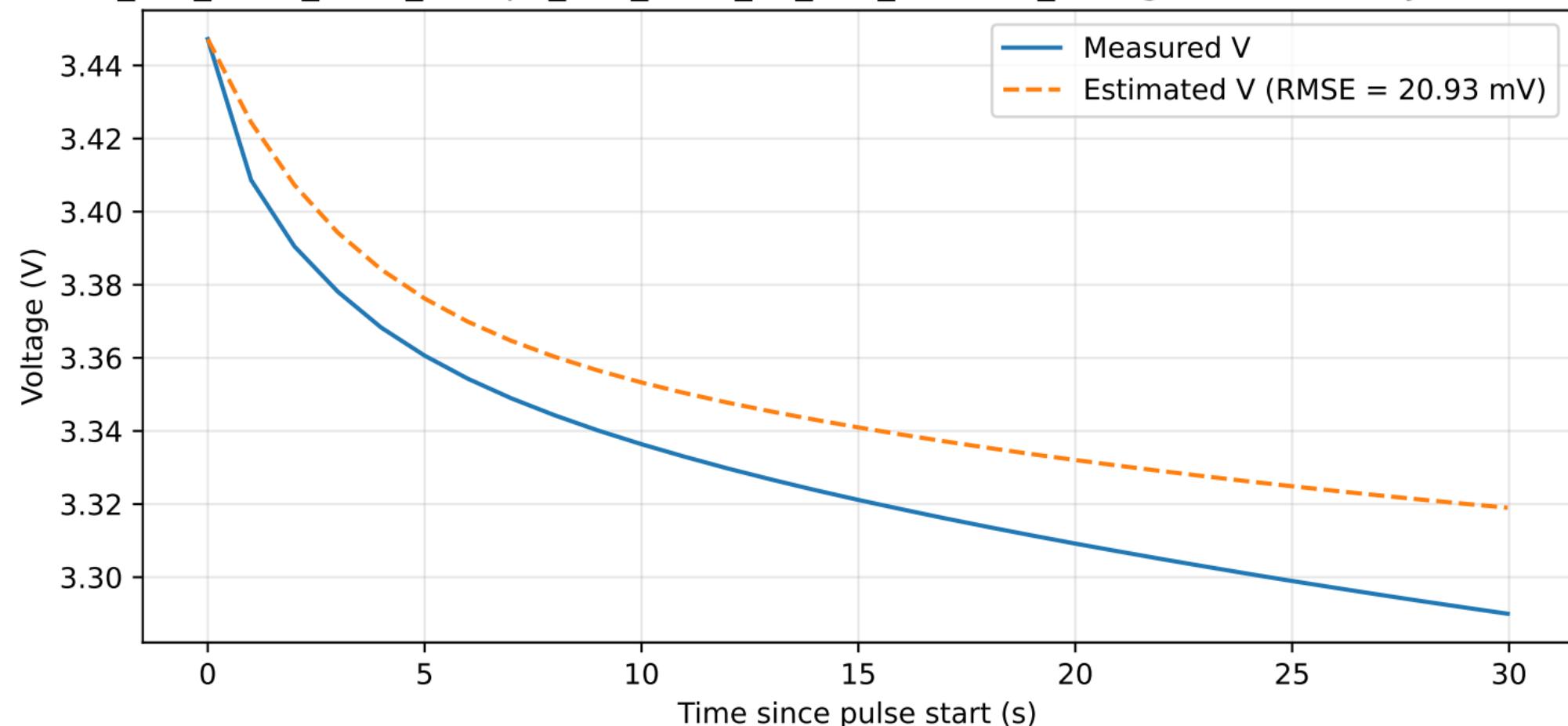
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



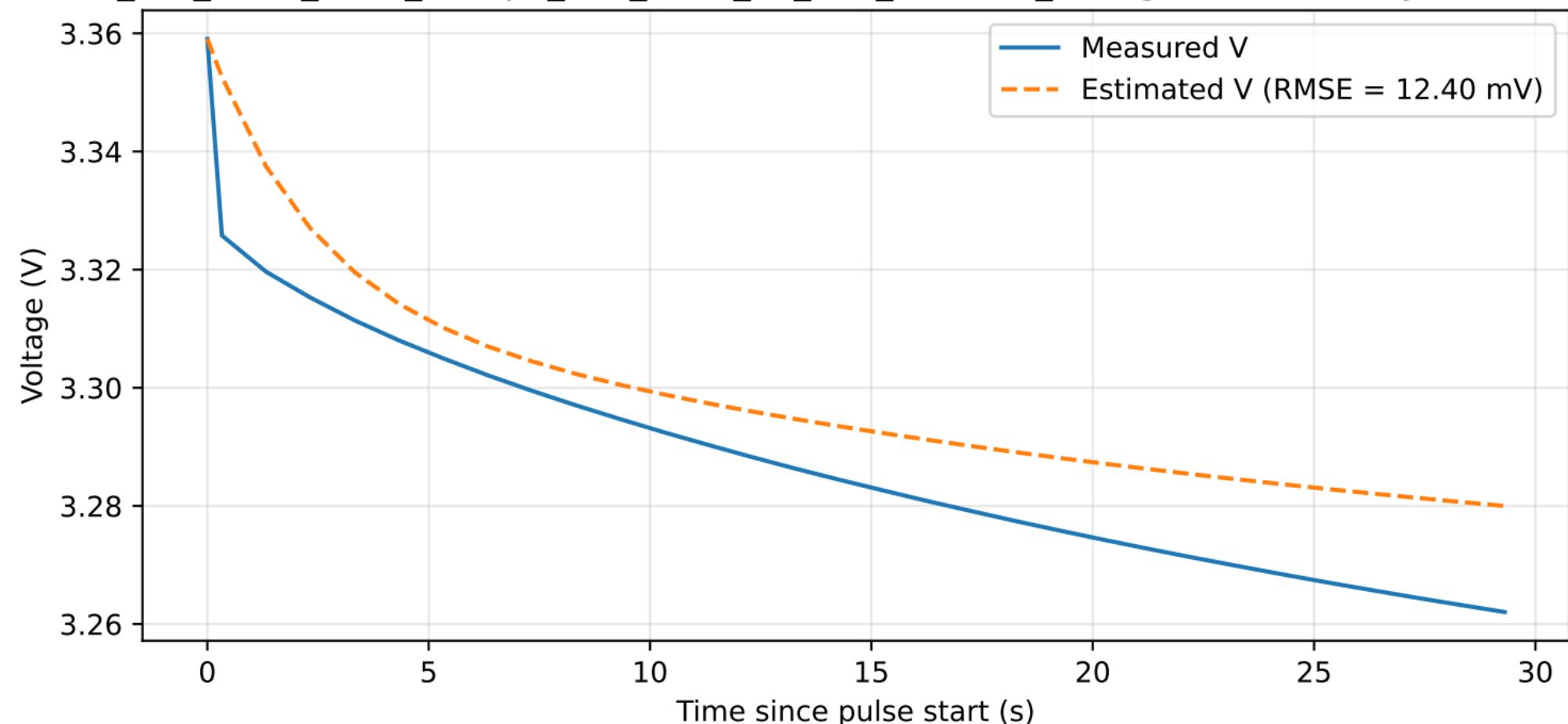
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0050\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)



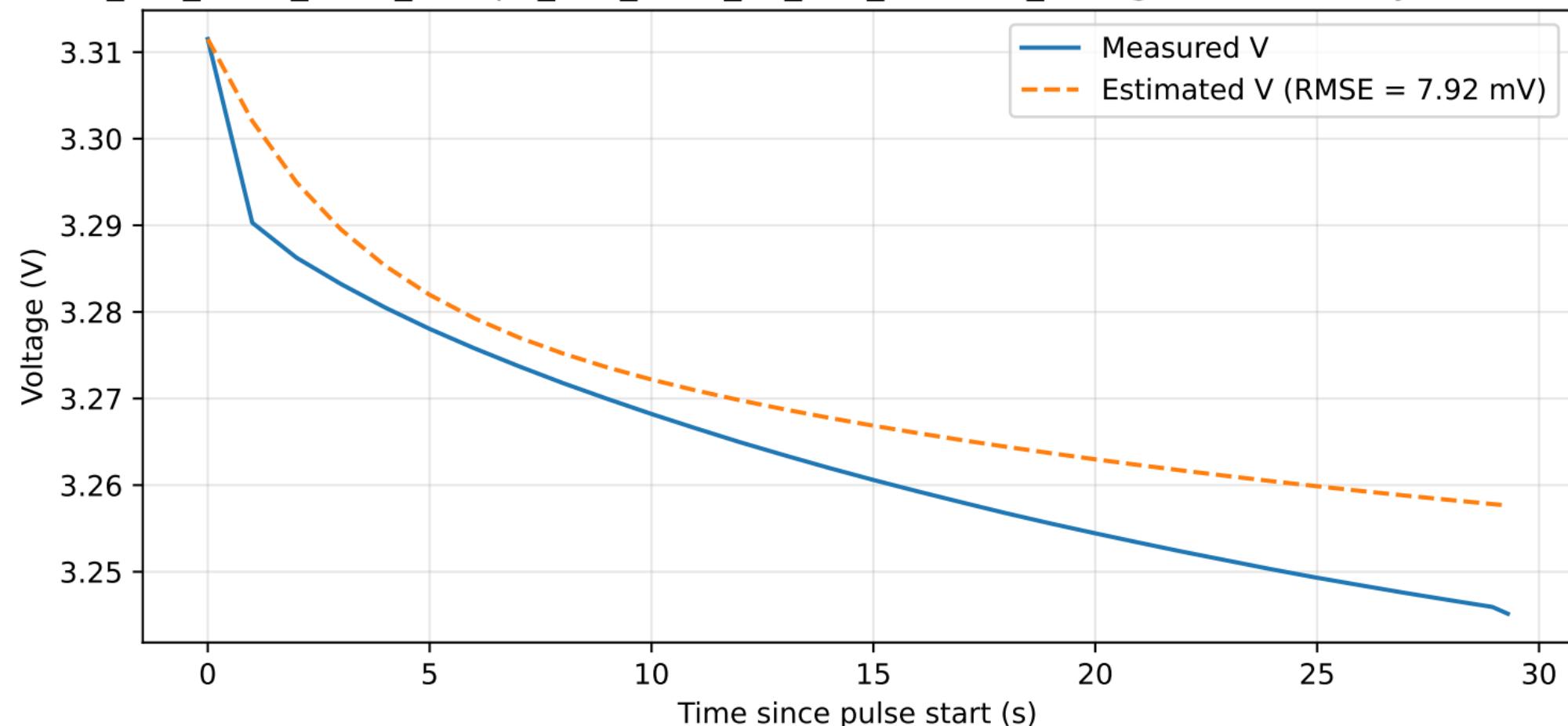
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 1 (cycle 1, step 4)



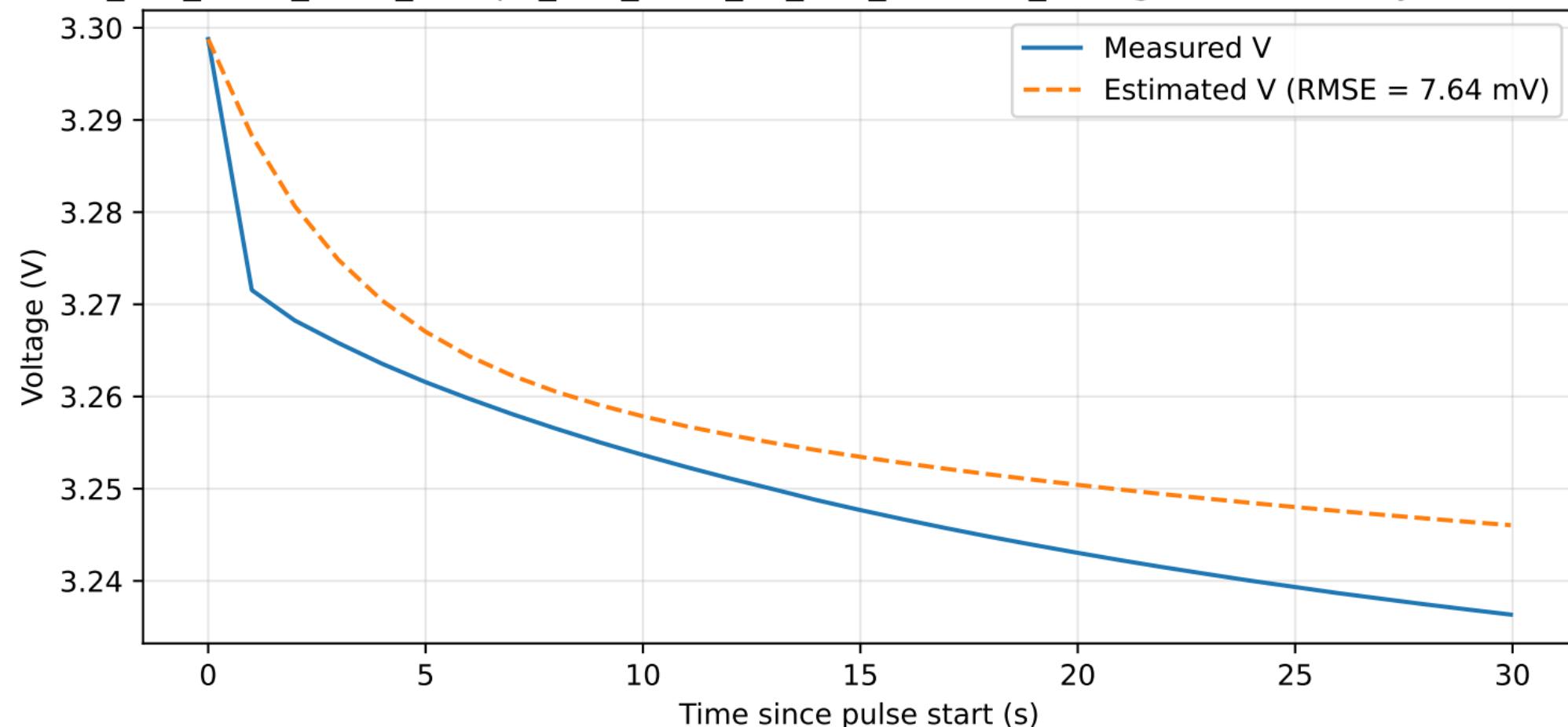
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 2 (cycle 1, step 9)



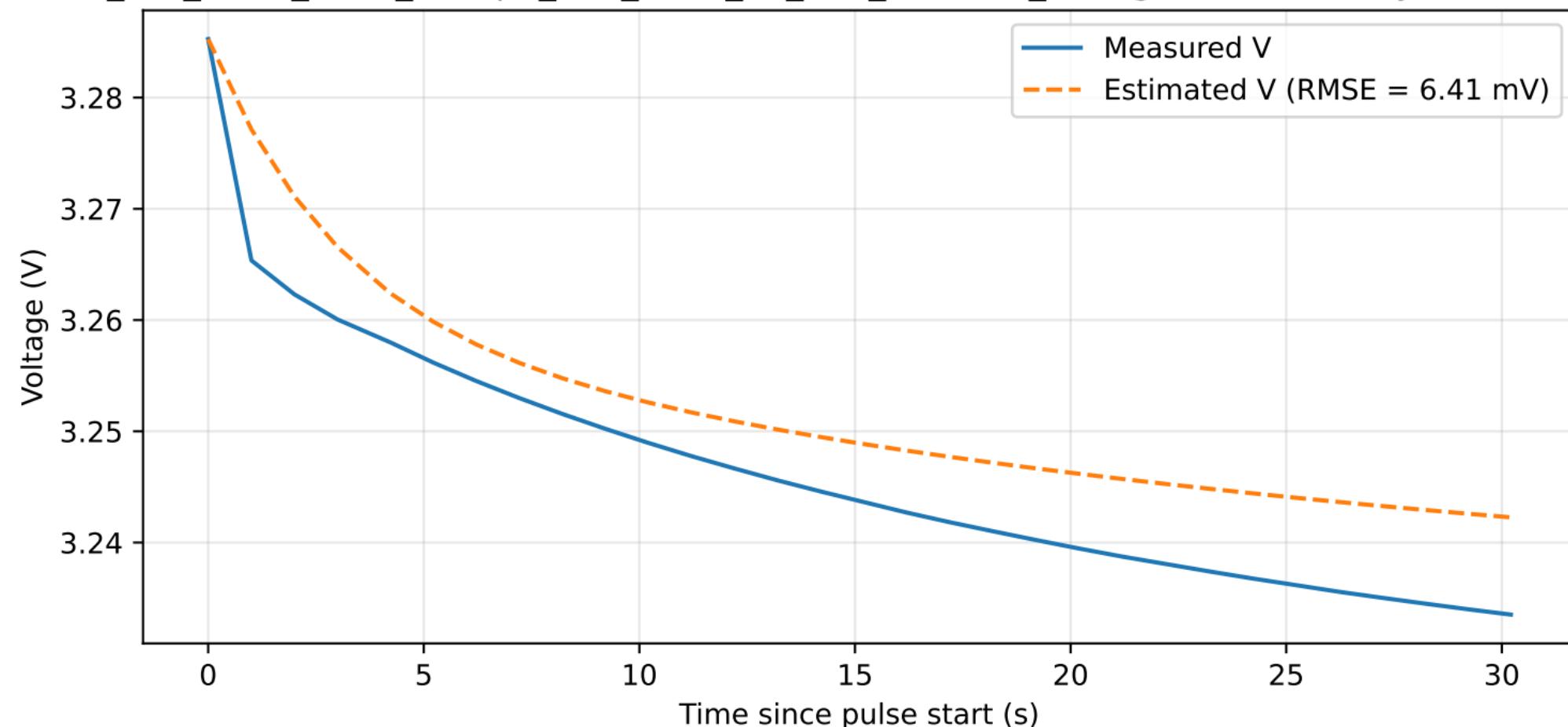
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 3 (cycle 1, step 14)



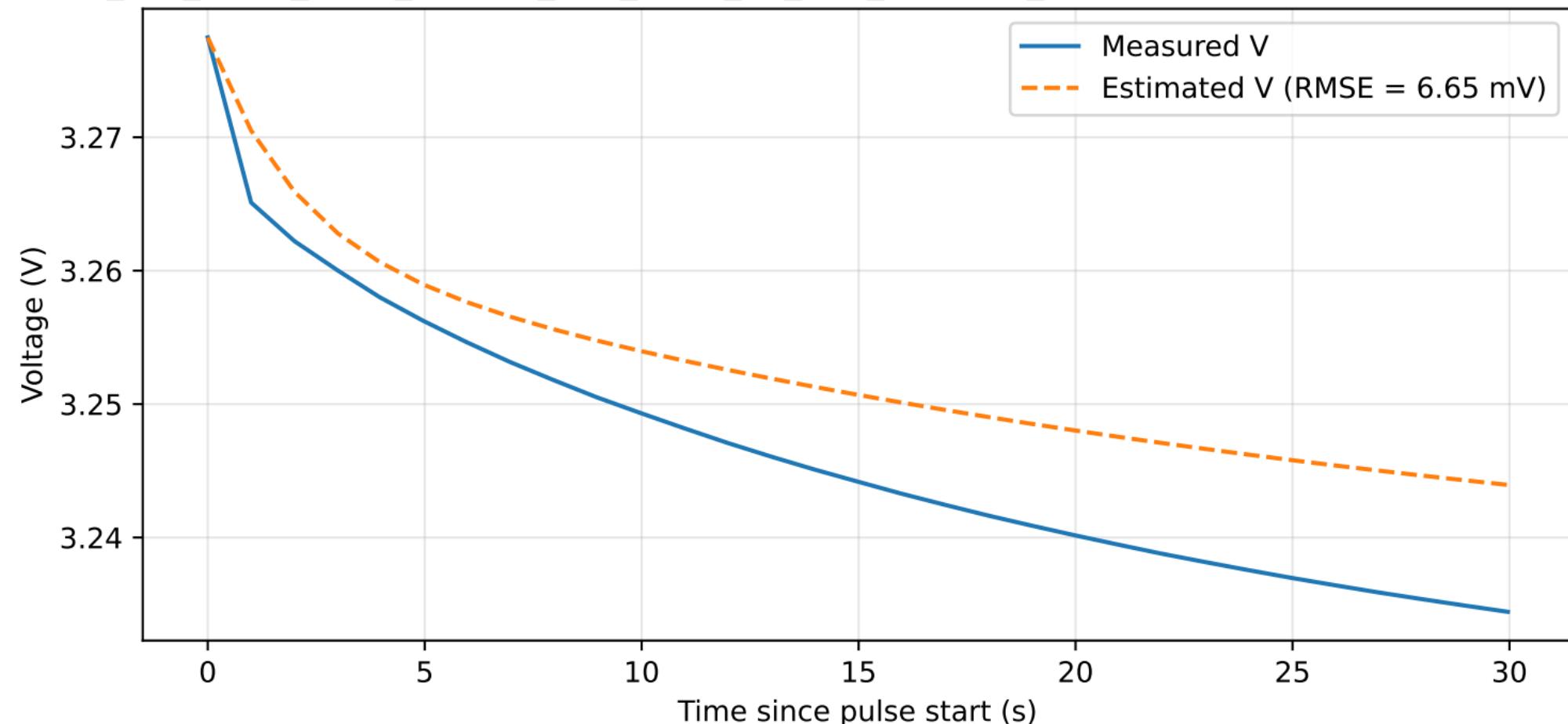
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 4 (cycle 1, step 19)



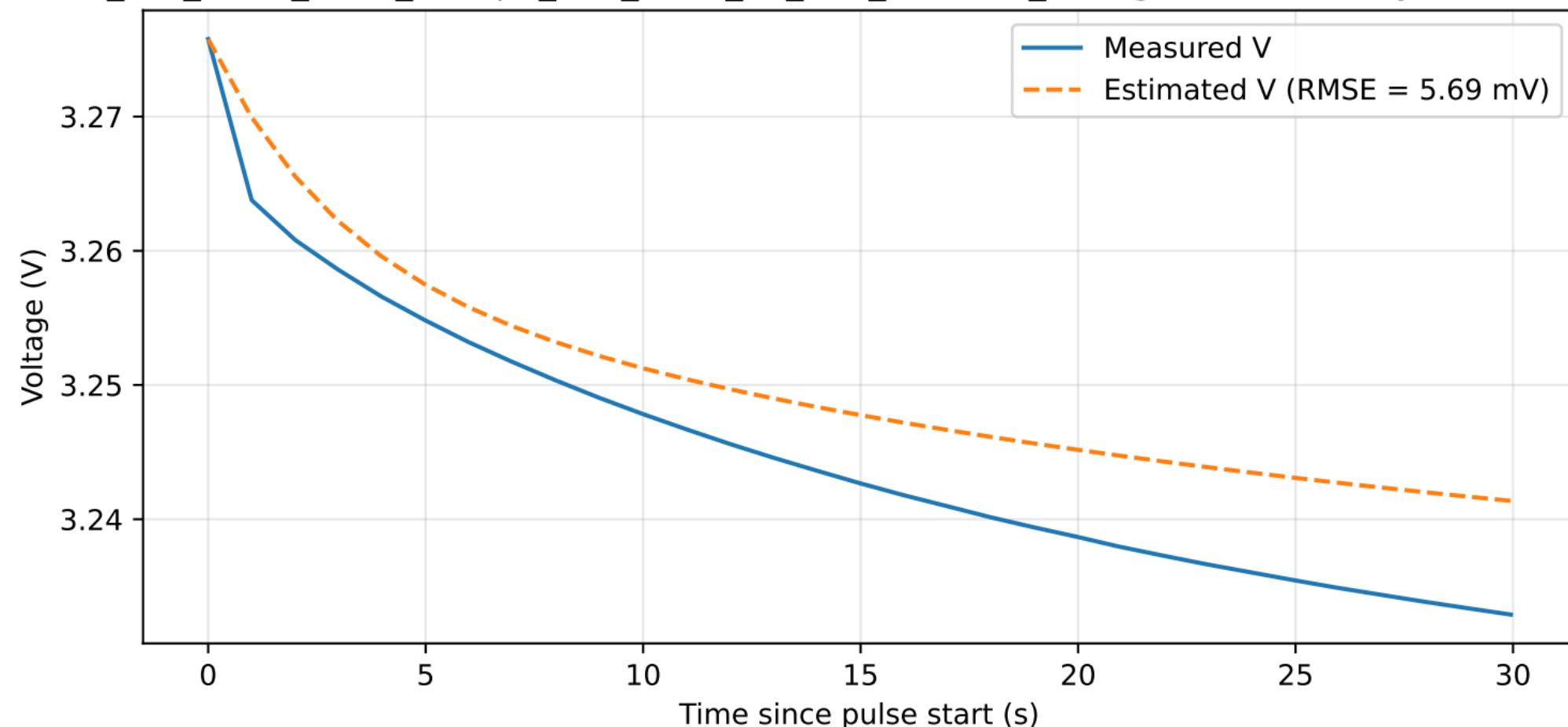
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 5 (cycle 1, step 24)



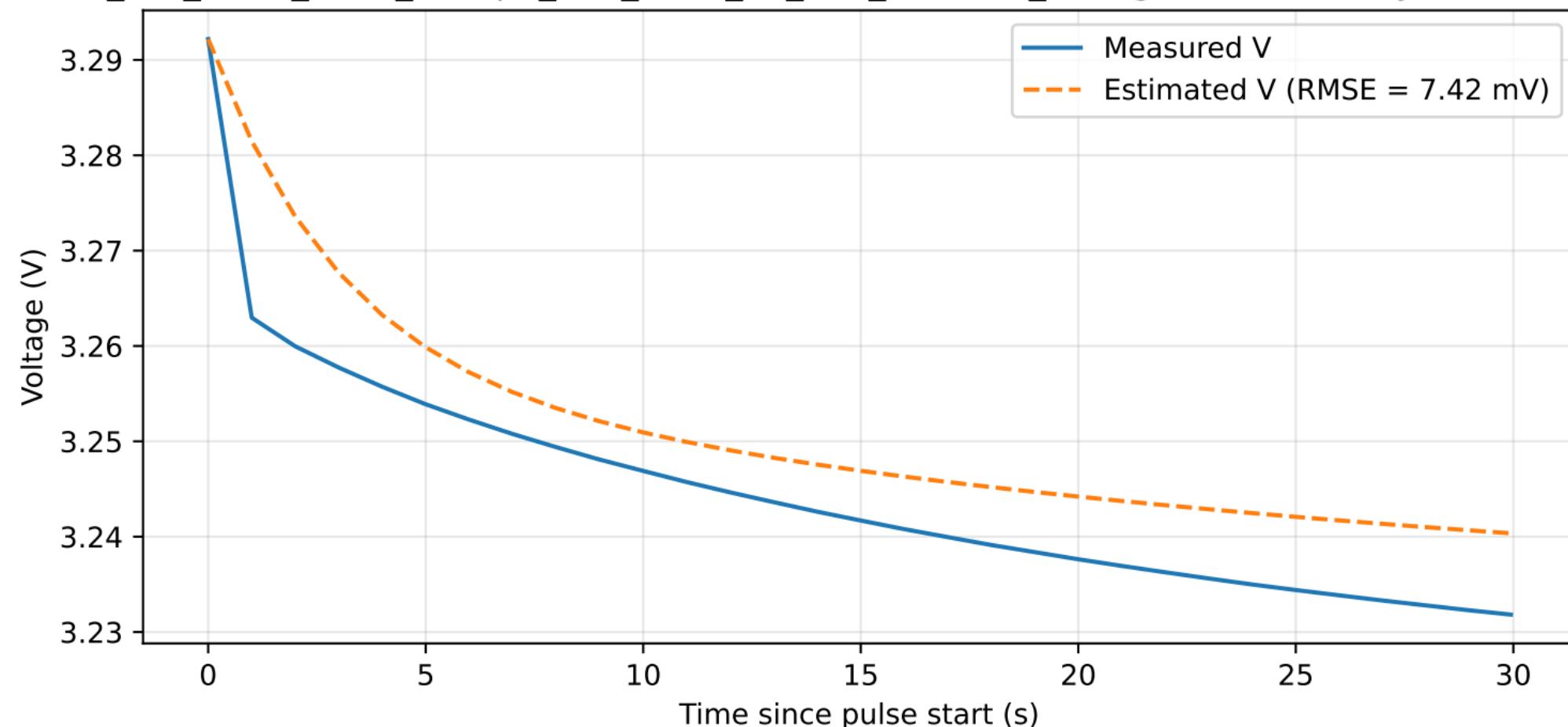
# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 6 (cycle 1, step 29)



# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 7 (cycle 1, step 34)



# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 8 (cycle 1, step 39)



# RD\_LFP\_HPPC\_REPT\_Group2\_150\_0057\_10\_100\_terminal\_voltage — Pulse 9 (cycle 1, step 44)

