



Certificate no: UC-10be9ad0-c313-4216-81ec-4a3da322088b
Certificate url: ude.my/UC-10be9ad0-c313-4216-81ec-4a3da322088b
Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Mohammed Omer

Date **March 28, 2024**

Length **12.5 total hours**