

# Seminar Details

Title: **Artificial Intelligence Verification-The Future of Chip Verification**

Short Abstract:

**Implementation of artificial intelligence (AI) in chip design verification is the futuristic vision of Truechip. The company has already automation and GUI tools which today is capable of automatic test bench generation, automatic environment creation and regression analysis along with performance measurement.**

**AI coupled with Truechip's automation tool will revolutionize the design verification. AI will bring more automation in verification. We will have superior testing and verification methods which will use the past data and use AI-ML algorithms to get robust coverage. It will also reduce time-to-market by automation and rapid impact analysis of different implementations.**

Speaker Biography:

Speaker: **Nitin Kishore**

**Over 22+ years of engineering and management experience, developing and bringing to market over 100 products. Prior to founding Truechip, Nitin was a Design Manager at Freescale Semiconductor (now NXP). Nitin worked at FSL for 10 years. Prior to that Nitin worked at ControlNet, Goa, India. Hands on experience in frontend and backend SoC design flows. Nitin holds a PGDM (Finance) from AIMA, and a BE (Electronics & Telecommunications) from Pune University, India.**

Photo:



Duration: **35 minutes**