



## 1. Description

### 1.1. Project

Project Name	myclock_keil
Board Name	custom
Generated with:	STM32CubeMX 6.7.0
Date	01/01/2023

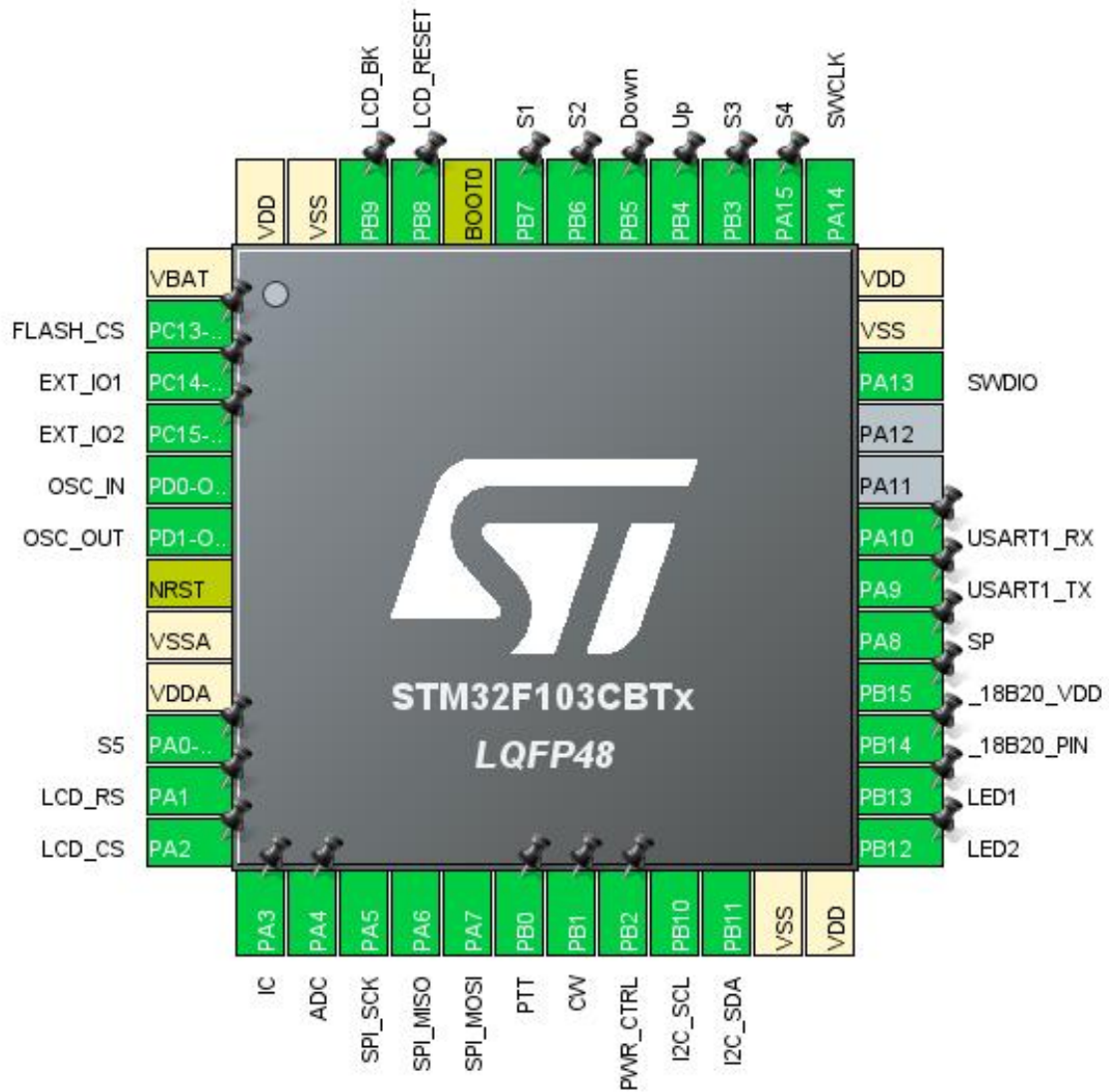
### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103CBTx
MCU Package	LQFP48
MCU Pin number	48

### 1.3. Core(s) information

Core(s)	Arm Cortex-M3
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## 2. Pinout Configuration



### 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13-TAMPER-RTC *	I/O	GPIO_Output	FLASH_CS
3	PC14-OSC32_IN *	I/O	GPIO_Input	EXT_IO1
4	PC15-OSC32_OUT *	I/O	GPIO_Output	EXT_IO2
5	PD0-OSC_IN	I/O	RCC_OSC_IN	OSC_IN
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	OSC_OUT
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP *	I/O	GPIO_Input	S5
11	PA1 *	I/O	GPIO_Output	LCD_RS
12	PA2 *	I/O	GPIO_Output	LCD_CS
13	PA3 *	I/O	GPIO_Input	IC
14	PA4	I/O	ADC1_IN4	ADC
15	PA5	I/O	SPI1_SCK	SPI_SCK
16	PA6	I/O	SPI1_MISO	SPI_MISO
17	PA7	I/O	SPI1_MOSI	SPI_MOSI
18	PB0 *	I/O	GPIO_Output	PTT
19	PB1 *	I/O	GPIO_Output	CW
20	PB2 *	I/O	GPIO_Output	PWR_CTRL
21	PB10	I/O	I2C2_SCL	I2C_SCL
22	PB11	I/O	I2C2_SDA	I2C_SDA
23	VSS	Power		
24	VDD	Power		
25	PB12 *	I/O	GPIO_Output	LED2
26	PB13 *	I/O	GPIO_Output	LED1
27	PB14 *	I/O	GPIO_Output	_18B20_PIN
28	PB15 *	I/O	GPIO_Output	_18B20_VDD
29	PA8	I/O	TIM1_CH1	SP
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
34	PA13	I/O	SYS_JTMS-SWDIO	SWDIO
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	SWCLK
38	PA15 *	I/O	GPIO_Input	S4

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
39	PB3 *	I/O	GPIO_Input	S3
40	PB4 *	I/O	GPIO_Input	Up
41	PB5 *	I/O	GPIO_Input	Down
42	PB6 *	I/O	GPIO_Input	S2
43	PB7 *	I/O	GPIO_Input	S1
44	BOOT0	Boot		
45	PB8 *	I/O	GPIO_Output	LCD_RESET
46	PB9 *	I/O	GPIO_Output	LCD_BK
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function



## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	myclock_keil
Project Folder	D:\MyCode\STM32\myclock_keil
Toolchain / IDE	MDK-ARM V5.32
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	MX_DMA_Init	DMA
3	SystemClock_Config	RCC
4	MX_I2C2_Init	I2C2
5	MX_SPI1_Init	SPI1
6	MX_TIM1_Init	TIM1
7	MX_ADC1_Init	ADC1
8	MX_USART1_UART_Init	USART1

## 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103CBTx
Datasheet	DS5319_Rev17

### 6.2. Parameter Selection

Temperature	25
Vdd	3.3

### 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1



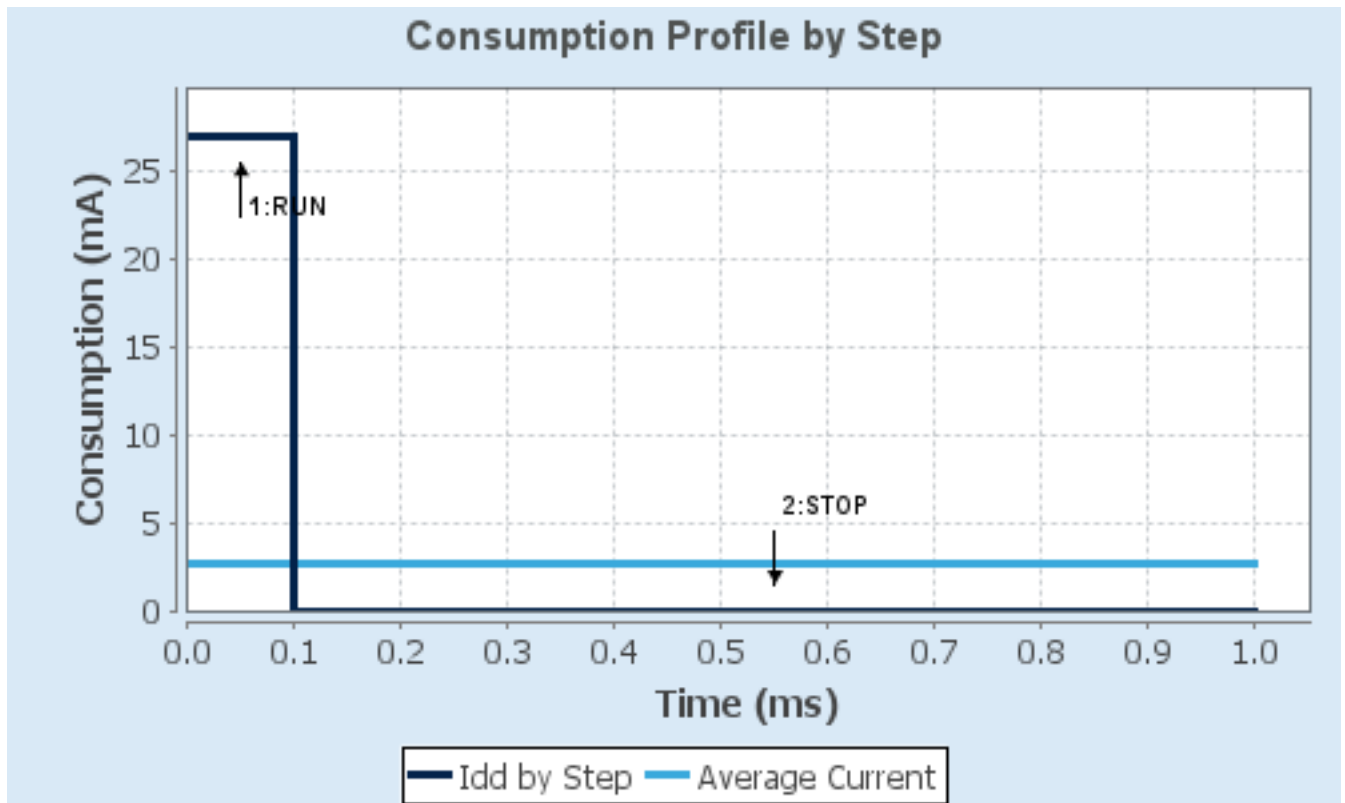
#### 6.4. Sequence

<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP
<b>Vdd</b>	3.3	3.3
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	No Scale	No Scale
<b>Fetch Type</b>	FLASH	n/a
<b>CPU Frequency</b>	72 MHz	0 Hz
<b>Clock Configuration</b>	HSE PLL	Regulator LP
<b>Clock Source Frequency</b>	8 MHz	0 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	27 mA	14 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	90.0	0.0
<b>Ta Max</b>	100.1	105
<b>Category</b>	In DS Table	In DS Table

#### 6.5. Results

Sequence Time	1 ms	Average Current	2.71 mA
Battery Life	1 month, 21 days, 17 hours	Average DMIPS	61.0 DMIPS

#### 6.6. Chart



## 7. Peripherals and Middlewares Configuration

### 7.1. ADC1

mode: IN4

mode: Temperature Sensor Channel

mode: Vrefint Channel

#### 7.1.1. Parameter Settings:

##### ADCs\_Common\_Settings:

Mode Independent mode

##### ADC\_Settings:

Data Alignment Right alignment

Scan Conversion Mode Disabled

Continuous Conversion Mode Disabled

Discontinuous Conversion Mode Disabled

##### ADC\_Regular\_ConversionMode:

Enable Regular Conversions Enable

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

Rank 1

Channel

**Channel Temperature Sensor \***

Sampling Time 1.5 Cycles

##### ADC\_Injected\_ConversionMode:

Enable Injected Conversions Disable

##### WatchDog:

Enable Analog WatchDog Mode false

### 7.2. I2C2

I2C: I2C

#### 7.2.1. Parameter Settings:

##### Master Features:

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

##### Slave Features:

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address	0
General Call address detection	Disabled

## 7.3. RCC

### High Speed Clock (HSE): Crystal/Ceramic Resonator

#### 7.3.1. Parameter Settings:

##### System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

##### RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

## 7.4. SPI1

### Mode: Full-Duplex Master

#### 7.4.1. Parameter Settings:

##### Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

##### Clock Parameters:

Prescaler (for Baud Rate)	<b>4 *</b>
Baud Rate	<b>18.0 MBits/s *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

##### Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

## 7.5. SYS

## Debug: Serial Wire

Timebase Source: SysTick

## 7.6. TIM1

**Clock Source : Internal Clock**

**Channel1: Output Compare CH1**

### 7.6.1. Parameter Settings:

#### Counter Settings:

Prescaler (PSC - 16 bits value)	<b>72-1 *</b>
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	<b>1000 *</b>
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0
auto-reload preload	Disable

#### Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

#### Break And Dead Time management - BRK Configuration:

BRK State	Disable
BRK Polarity	High

#### Break And Dead Time management - Output Configuration:

Automatic Output State	Disable
Off State Selection for Run Mode (OSSR)	Disable
Off State Selection for Idle Mode (OSSI)	Disable
Lock Configuration	Off

#### Output Compare Channel 1:

Mode	<b>Toggle on match *</b>
Pulse (16 bits value)	0
Output compare preload	Disable
CH Polarity	High
CH Idle State	Reset

## 7.7. USART1

**Mode: Asynchronous**

### 7.7.1. Parameter Settings:

**Basic Parameters:**

Baud Rate	<b>9600 *</b>
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

**Advanced Parameters:**

Data Direction	Receive and Transmit
Over Sampling	16 Samples

\* User modified value

## 8. System Configuration

### 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA4	ADC1_IN4	Analog mode	n/a	n/a	ADC
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	n/a	High *	I2C_SCL
	PB11	I2C2_SDA	Alternate Function Open Drain	n/a	High *	I2C_SDA
RCC	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	OSC_IN
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	OSC_OUT
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	n/a	High *	SPI_SCK
	PA6	SPI1_MISO	Input mode	No pull-up and no pull-down	n/a	SPI_MISO
	PA7	SPI1_MOSI	Alternate Function Push Pull	n/a	High *	SPI_MOSI
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	SWDIO
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	SWCLK
TIM1	PA8	TIM1_CH1	Alternate Function Push Pull	n/a	Low	SP
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	High *	
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	n/a	
GPIO	PC13-TAMPER-RTC	GPIO_Output	Output Push Pull	No pull-up and no pull-down	High *	FLASH_CS
	PC14-OSC32_IN	GPIO_Input	Input mode	Pull-up *	n/a	EXT_IO1
	PC15-OSC32_OUT	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	EXT_IO2
	PA0-WKUP	GPIO_Input	Input mode	Pull-down *	n/a	S5
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_RS
	PA2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_CS
	PA3	GPIO_Input	Input mode	Pull-down *	n/a	IC
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	PTT
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	CW
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	PWR_CTRL
	PB12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED2
	PB13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED1
	PB14	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	_18B20_PIN

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PB15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	_18B20_VDD
	PA15	GPIO_Input	Input mode	<b>Pull-down *</b>	n/a	S4
	PB3	GPIO_Input	Input mode	<b>Pull-down *</b>	n/a	S3
	PB4	GPIO_Input	Input mode	<b>Pull-down *</b>	n/a	Up
	PB5	GPIO_Input	Input mode	<b>Pull-down *</b>	n/a	Down
	PB6	GPIO_Input	Input mode	<b>Pull-down *</b>	n/a	S2
	PB7	GPIO_Input	Input mode	<b>Pull-down *</b>	n/a	S1
	PB8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_RESET
	PB9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_BK



## 8.2. DMA configuration

DMA request	Stream	Direction	Priority
USART1_TX	DMA1_Channel4	Memory To Peripheral	Low
USART1_RX	DMA1_Channel5	Peripheral To Memory	Low

### USART1\_TX: DMA1\_Channel4 DMA request Settings:

Mode: Normal  
Peripheral Increment: Disable  
Memory Increment: **Enable \***  
Peripheral Data Width: Byte  
Memory Data Width: Byte

### USART1\_RX: DMA1\_Channel5 DMA request Settings:

Mode: Normal  
Peripheral Increment: Disable  
Memory Increment: **Enable \***  
Peripheral Data Width: Byte  
Memory Data Width: Byte

### 8.3. NVIC configuration

#### 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
DMA1 channel4 global interrupt	true	0	0
DMA1 channel5 global interrupt	true	0	0
USART1 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
ADC1 and ADC2 global interrupts	unused		
TIM1 break interrupt	unused		
TIM1 update interrupt	unused		
TIM1 trigger and commutation interrupts	unused		
TIM1 capture compare interrupt	unused		
I2C2 event interrupt	unused		
I2C2 error interrupt	unused		
SPI1 global interrupt	unused		

#### 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
DMA1 channel4 global interrupt	false	true	true

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
DMA1 channel5 global interrupt	false	true	true
USART1 global interrupt	false	true	true

\* User modified value

## 9. System Views

### 9.1. Category view

#### 9.1.1. Current

## 10. Docs & Resources

Type	Link
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip">https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32ibis.zip">https://www.st.com/resource/en/ibis_model/stm32ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f1_svd.zip">https://www.st.com/resource/en/svd/stm32f1_svd.zip</a>
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip">https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32ibis.zip">https://www.st.com/resource/en/ibis_model/stm32ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f1_svd.zip">https://www.st.com/resource/en/svd/stm32f1_svd.zip</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf">https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf</a>
Training Material	<a href="https://www.st.com/resource/en/sales_guide/sg_sc2155.pdf">https://www.st.com/resource/en/sales_guide/sg_sc2155.pdf</a>
Brochures	<a href="https://www.st.com/resource/en/brochure/breveco0518.pdf">https://www.st.com/resource/en/brochure/breveco0518.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstmcsuite.pdf">https://www.st.com/resource/en/flyer/flstmcsuite.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/fldpstpf11120.pdf">https://www.st.com/resource/en/flyer/fldpstpf11120.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/1239988349.pdf">https://www.st.com/resource/en/certification_document/1239988349.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1709-emc-design-">https://www.st.com/resource/en/application_note/an1709-emc-design-</a>

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes [https://www.st.com/resource/en/application\\_note/an2548-using-the-stm32f0f1f3gxl-series-dma-controller-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2548-using-the-stm32f0f1f3gxl-series-dma-controller-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3128-stm32-embedded-graphic-objectstouchscreen-library-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3128-stm32-embedded-graphic-objectstouchscreen-library-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3156-usb-dfu-](https://www.st.com/resource/en/application_note/an3156-usb-dfu-)

protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

- Application Notes [https://www.st.com/resource/en/application\\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf)
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- Application Notes [https://www.st.com/resource/en/application\\_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf)
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