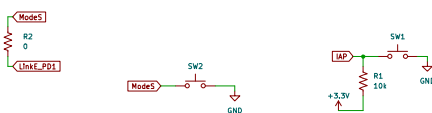


U2
CH32V305FBP6

RT → 1 RSTB
Line_P25 → 2 PA5/UART1_CTS/PAL_in(sv)
Line_P26 → 3 PA7/UART1_TX/PC2_SCL
UART_TX → 4 PA8/UART1_RX/PC2_SDA
UART_RX → 5 PB12/SPI2_NSS/CAN2_RX
Line_P12 → 6 PB13/SPI2_SCK/UART1_CTS/CAN2_TX
Line_P13 → 7 PB14/SPI2_MISO/UART1_BTS
Line_P14 → 8 PB15/SPI2_MOSI/UART1_TX
Line_P15 → 9 PC6
Line_P16 → 10 PC7
I2P → 18

OSC_OUT/PA0 → 10
OSC_IN/PA1 → 11
VSS/GND → 12
PA7/USB2P0/UART1_TX → 17
PA8/USB2P0/UART1_RX → 18
USB+ → 19
USB- → 20
PA14/SWCLK → 21
VDD3V3 → 22
PA9/UART1_BTS/PA13/SWIO → 23
PC9/PA8/UART1_RX → 24
PC8 → 25
PA14/SWCLK → 26
VSS/GND → 27
EEPROM → 28
RST → 29

+3.3V
C5 0.1uF



Designed By: Yatharth Agarwal		
VLSI System Design		
Sheet: /		
File: SquadronMini_28.kicad_sch		
Title: VSDSquadronMini		
Size: A3	Date: 2024-02-23	Rev: V3
KiCad E.D.A. 8.0.5		Id: 1/1