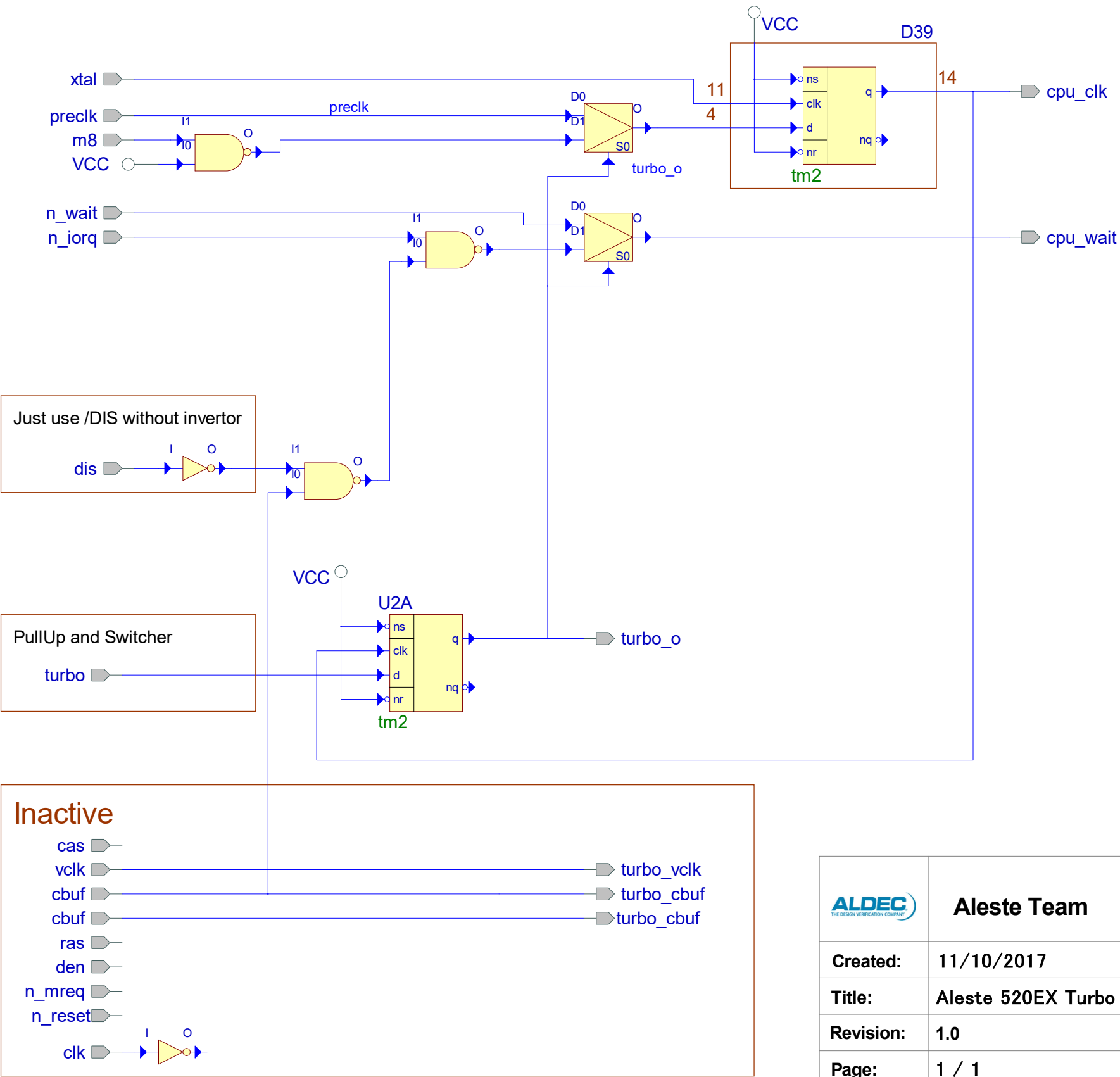



```
input n_reset,
input turbo,
input preclk,
input clk,
input den,
input m8,
input xtal,
input vclk,
input ras,
input cas,
input dis,
input n_iorq,
input n_mreq,
input n_wait,
output turbo_o,
output cpu_clk,
output cpu_wait,
output turbo_vclk,
output turbo_cbuf
```



	Aleste Team
Created:	11/10/2017
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