

Declarations

Ports:

```

wire cas;
wire clk;
wire hx;
wire m8;
wire n_rst;
wire p_rclk;
wire ras;
wire turbo;
wire vclk;
wire xtal;
wire clk_cpu;
wire n_wait_cpu;
wire turbo_o;

```

Diagram Signals:

```
wire dout;  
wire dout1;  
wire dout2;  
wire dout3;  
wire dout4;  
wire dout5;  
wire dout6;  
wire dout7;  
wire ena;  
wire sel;
```

Compiler Directives

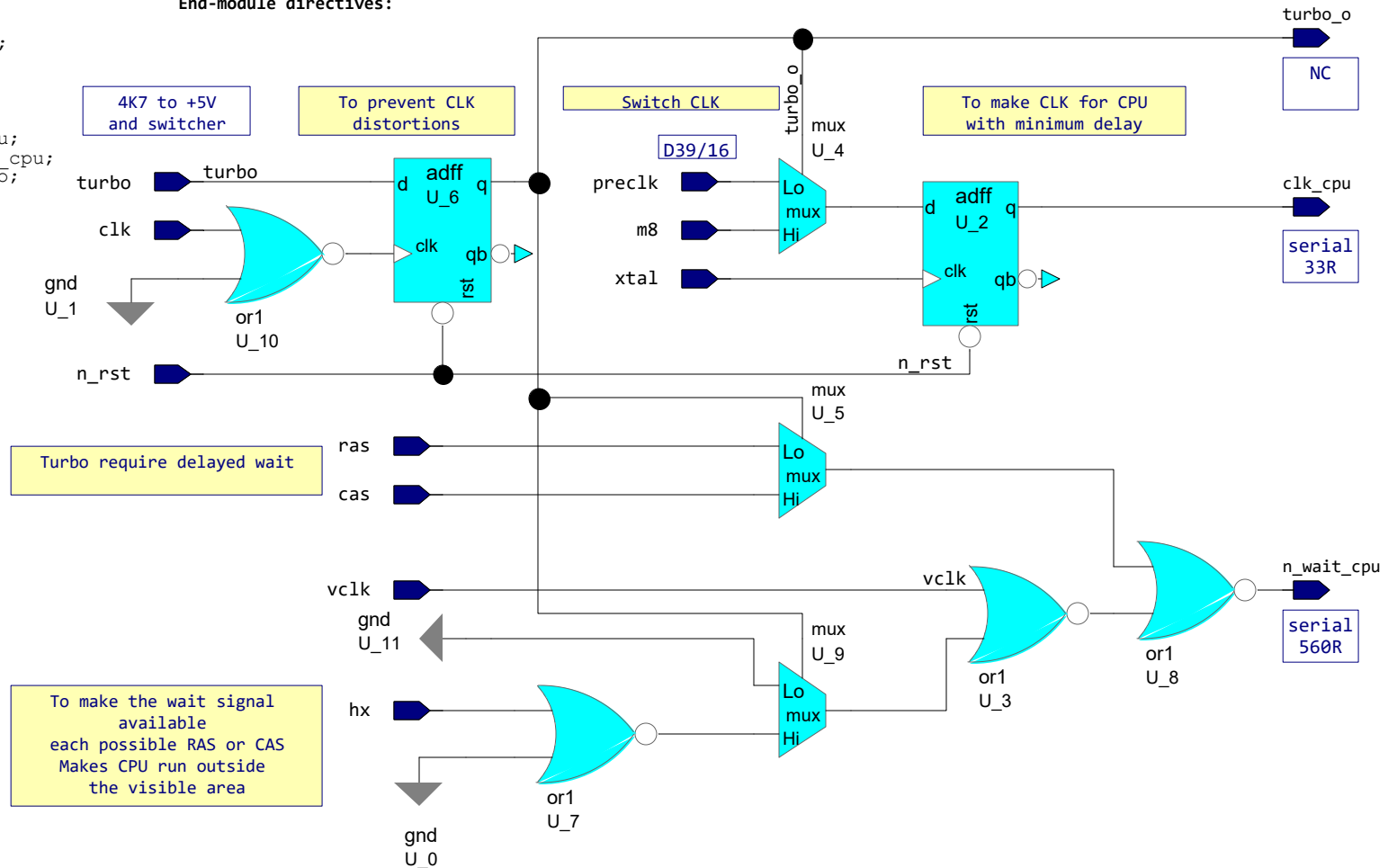
Pre-module directives:

```
`resetall
```

```
`timescale 1ns/10ps
```

Post-module directives:

End-module directives:



Aleste Team		Project: AlesteOrig	VAP	VVT	VMB
Title:	Turbo Mode Board Design (Simple)	Simple version. Use switcher with pullup resistor to enable or disable turbo mode.			
Path:	AlesteOrig_lib/turbo_simple/struct				
Edited:	by valery on 11 Nov 2017				
			SAP		AVM
					VPB