DECEMBER 1983 - REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

#### description

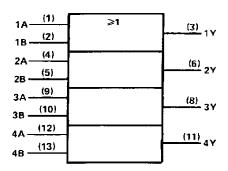
These devices contain four independent 2-input OR gates.

The SN5432, SN54LS32 and SN54S32 are characterized for operation over the full military range of  $-55\,^{\circ}\text{C}$  to  $125\,^{\circ}\text{C}$ . The SN7432, SN74LS32 and SN74S32 are characterized for operation from  $0\,^{\circ}\text{C}$  to  $70\,^{\circ}\text{C}$ .

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT
Α	В	Y
Н	х	н
Х	н	H
L	L	L

#### logic symbol†



<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D. J. N. or W packages.

SN5432, SN54LS32, SN54S32 . . . J OR W PACKAGE SN7432 . . . N PACKAGE SN74LS32, SN74S32 . . . D OR N PACKAGE (TOP VIEW)

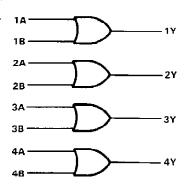
1A 🗀	ī	U14 VCC
1B 🗀	2	13 <b>□ 4B</b>
1Y 🗀	3	12 🗀 4A
2A 🗌	4	11 🗖 4Y
2B 🗀	5	10 <b>□</b> 3B
2Y 🗀	6	9∐-3A
GND 🗀	7	8 3Y
1	_	

SN54LS32, SN54S32 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

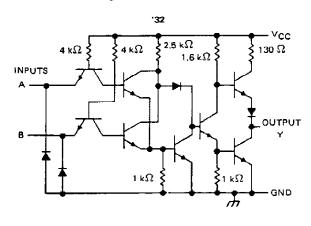
## logic diagram

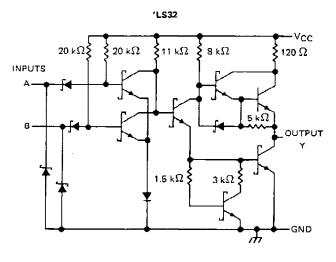


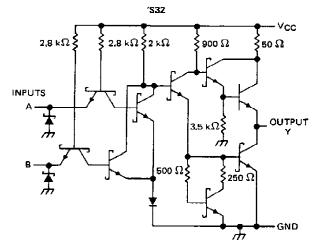
#### positive logic

 $Y = A + B \text{ or } Y = \overline{\overline{A} \cdot \overline{B}}$ 

#### schematics (each gate)







Resistor values shown are nominal.

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	7 V
Input voltage: '32, 'S32	5.5 V
'L\$32	7 V
Operating free-air temperature: SN54'	. –55°C to 125°C
SN74′	0°C to 70°C
Storage temperature range	, -65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.

# recommended operating conditions

		SN5432	?	SN7432			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	ONT
VCC Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH Hgh-level input voltage	2			2			V
VIL Low-level imput voltage			8.0			8,0	V
OH High-level output current			- 0.8			<b>- 0.8</b>	mA
IOL Low-level output current			16			16	mΑ
TA Operating free-air temperature	- 55		125	0		70	°C

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETER		TEST CONDIT	TONG +		SN5432			SN7432		
PARAMETER		TEST COMDIT	TONS (	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	VCC = MIN.	lj = - 12 mA				- 1.5			<b>— 1</b> ,5	V
V <sub>QH</sub>	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	l <sub>OH</sub> = − 0,8 mA	2.4	3.4		2.4	3.4		V
VOL	V <sub>CC</sub> = MIN,	_V <sub>1</sub> L ≈ 0.8 V,	IOL = 16 mA		0,2	0.4		0.2	0.4	V
l <sub>l</sub>	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 5.5 V				1			1	mΑ
Чн	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 2.4 V				40			40	μА
lin.	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0.4 V				1.6			- 1.6	mΑ
los§	VCC = MAX	·		- 20		<b>– 55</b>	- 18		- 55	mΑ
ГССН	V <sub>CC</sub> = MAX,	See Note 2			15	22		15	22	mA
<sup> </sup> CCL	V <sub>CC</sub> = MAX,	V1 = 0 V			23	38		23	38	mΑ

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: One input at 4.5 V, all others at GND.

## switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	MAX	UNIT	
tPLH !	A or B	>	B 400 O	C - 15 - 5		10	15	ns
†PHL	A 01 B	<u> </u>	$R_L = 400 \Omega$ ,	C <sub>L</sub> = 15 pF		14	22	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at  $V_{\rm CC}$  = 5 V,  $T_{\rm A}$  = 25°C. § Not more than one output should be shorted at a time.

# SN54LS32, SN74LS32 QUADRUPLE 2-INPUT POSITIVE OR GATES

#### recommended operating conditions

		SN54LS	32		SN74LS	32	
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH Hgh-level input voltage	2			2			٧
VIL Low-level input voltage			0.7			8.0	V
IOH High-level output current		<del></del>	- 0.4			- 0.4	mA
IOL Low-level output current			4			8	mΑ
TA Opertating free-air temperature	- 55		125	0		70	°C

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	·	**************************************			SN54LS	32		SN74LS	32	
PARAMETER		TEST CONDIT	TONST	MIN	TYP‡	MAX	MIN	TYP ‡	MAX	UNIT
V <sub>IK</sub>	V <sub>CC</sub> - MIN,	I <sub>1</sub> = 18 mA			·	- 1.5			- 1.5	V
∨он	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OH</sub> = - 0.4 mA	2,5	3.4	•	2.7	3.4		V
	VCC = MIN,	VIL = MAX,	IOL = 4 mA		0.25	0.4		0.25	0.4	v
VOL	V <sub>CC</sub> = MIN,	VIL = MAX,	ioL = 8 mA	i			[	0.35	0.5	\ \
Ιι	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 7 V		1		0.1			0.1	mA
IH	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.7 V	<u> </u>		•	20			20	μА
HL	VCC = MAX,	V1 = 0.4 V		ļ		0.4			- 0.4	mΑ
105§	VCC = MAX			- 20		- 100	<b>– 20</b>		<b>- 100</b>	mΑ
іссн	V <sub>CC</sub> = MAX,	See Note 2			3,1	6.2	Ü	3.1	6.2	mA
ICCL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0 V			4.9	9.8		4.9	9.8	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: One input at 4.5 V, all others at GND.

## switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDIT	MIN	ТҮР	МАХ	UNIT	
<sup>t</sup> PLH	A or B	V	D - 11.0	C = 15 ==		14	22	пѕ
†PHL	AOLD	•	$R_{\perp} = 2 k\Omega$ ,	C <sub>L</sub> = 15 p <sub>F</sub>		14	22	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

<sup>§</sup> Not more than one output should be shorted at a time and the duration of the short-circuit should not exceed one second.

#### recommended operating conditions

			SN54S3	2		SN74S3	2	UNIT
		MIN	MOM	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
ViH	High-level input voltage	2			2			V
VIL	Low-level input voltage			8.0			0.8	V
Іон	High-level output current			1			_ 1	mA
lOL	Low-level output current			20			20	mA
TA	Operating free-air temperature	<b>– 55</b>		125	0		70	°C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	, ,	TEST CONDIT	rions t		SN54S3	2		SN74S3	2	UNIT
PARAMETER		rest combittons:	MIN	TYP \$	MAX	MIN	TYP #	MAX	UNII	
V <sub>IK</sub>	VCC = MIN,	lj = _ 18 mA				- 1.2			- 1.2	V
Voн	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OH</sub> = - 1 mA	2.5	3.4		2.7	3.4		V
VOL	VCC = MIN,	V <sub>IL</sub> = 0.8 V,	I <sub>OL</sub> = 20 mA			0.5	1		0.5	V
11	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 5.5 V				1	Ī		1	mA
ЧН	VCC = MAX,	V <sub>1</sub> = 2.7 V				50			50	μА
<sup>†</sup> IL	VCC = MAX,	V <sub>1</sub> = 0.5 V				- 2			- 2	mA
los§	V <sub>CC</sub> = MAX			- 40		<b>- 100</b>	- 40		<b>–</b> 100	mA
Гссн	V <sub>CC</sub> = MAX,	See Note 2			18	32		18	32	mA
<sup>I</sup> CCL	VCC = MAX,	V <sub>1</sub> = 0 V			38	68	1	38	68	mA

- † For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
- ‡ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C. § Not more than one output should be shorted at a time and the duration of the short-circuit should not exceed one second.
- NOTE 2: One input at 4.5 V, all others at GND.

## switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN T	ΥP	MAX	UNIT	
tPLH .	АогВ	· ·	D - 370 C	C <sub>1</sub> = 15 pF		4	7	ns
tPHL	AOFB		R <sub>L</sub> = 280 Ω,	C[ = 15 pr		4	7	ns
tPLH	A or 8		$R_1 = 280 \Omega$ ,	C <sub>I</sub> = 50 pF		5		пş
tPHL	A019	'	71_ 200 32,	J 30 M		5		ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

#### IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

Copyright © 1996, Texas Instruments Incorporated

#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated

# This datasheet has been downloaded from:

www. Data sheet Catalog.com

Datasheets for electronic components.