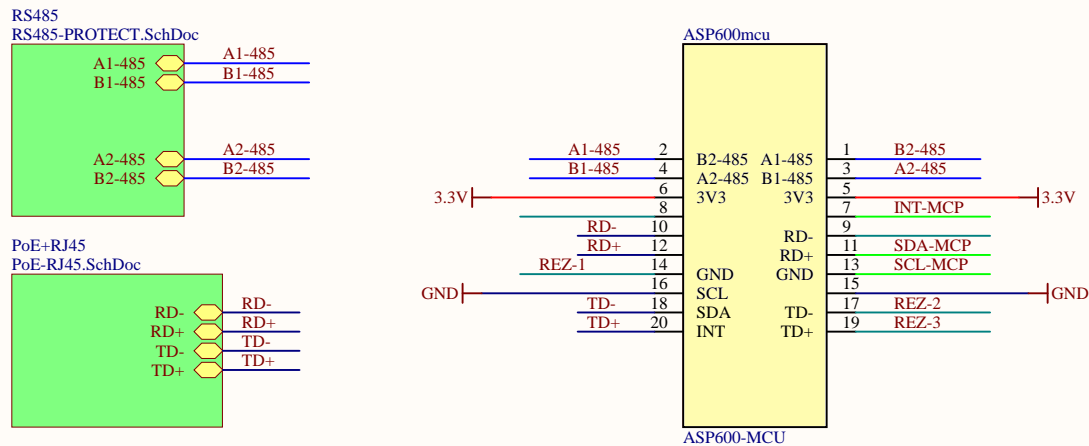


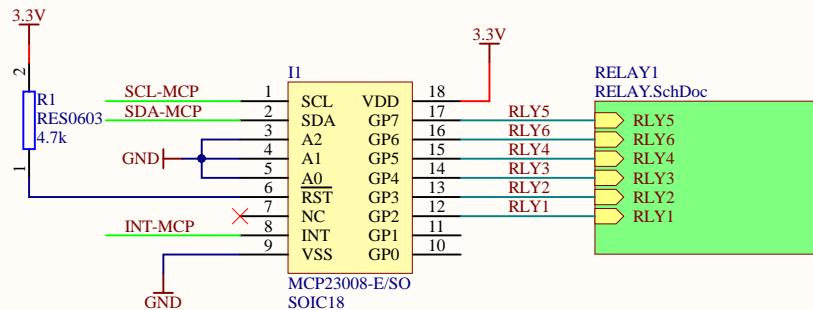
A

A



B

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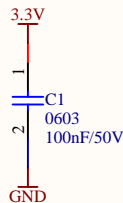
C

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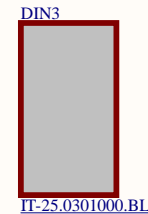
Fiducials - TOP LAYER



Fiducials - BOTTOM LAYER



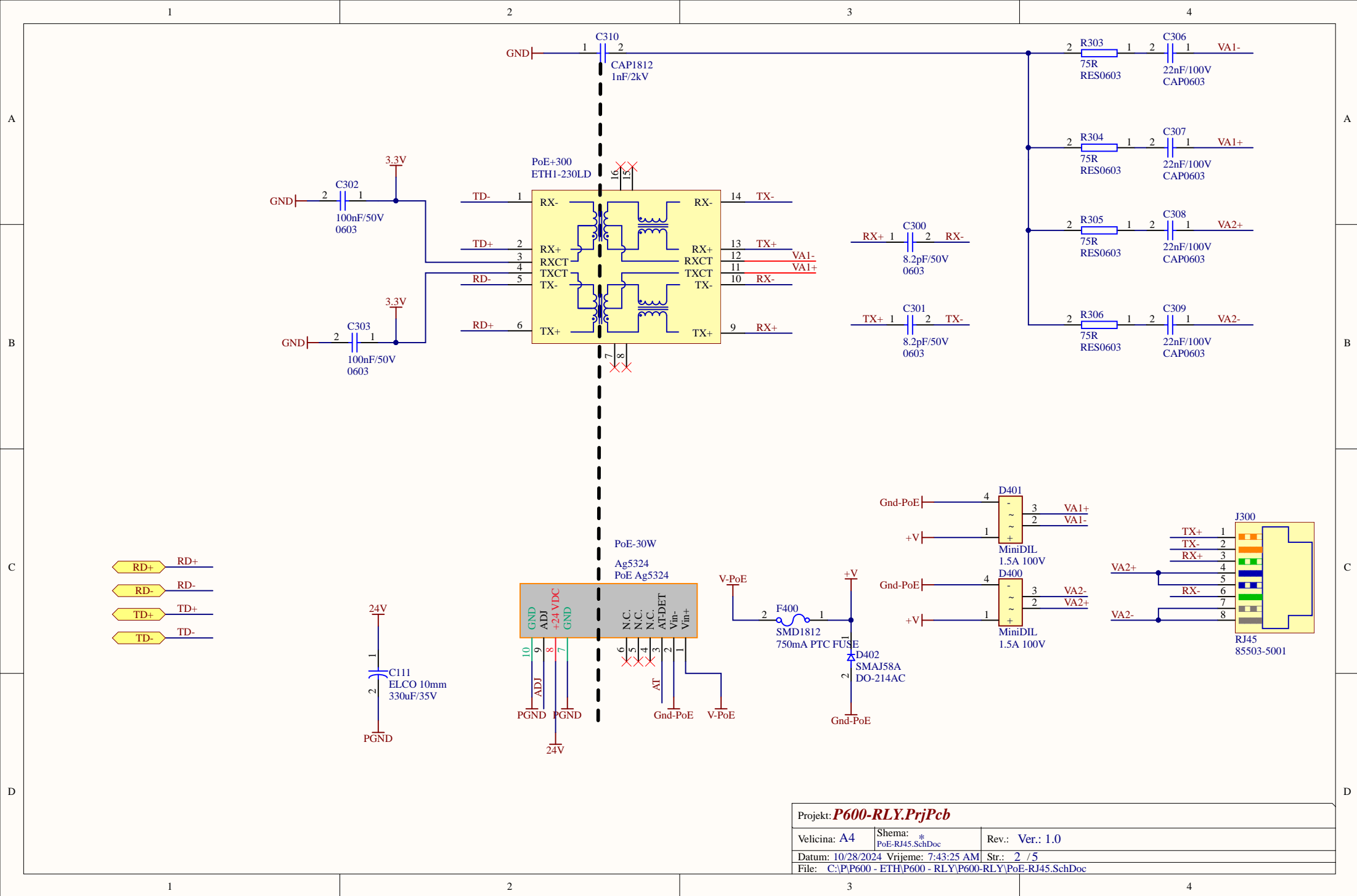
24->3,3  
MIC4680.SchDoc

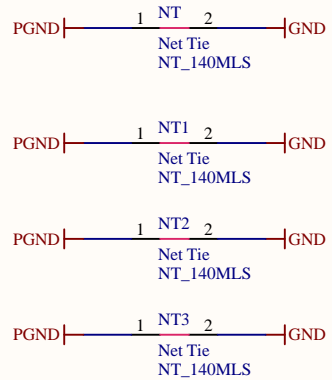
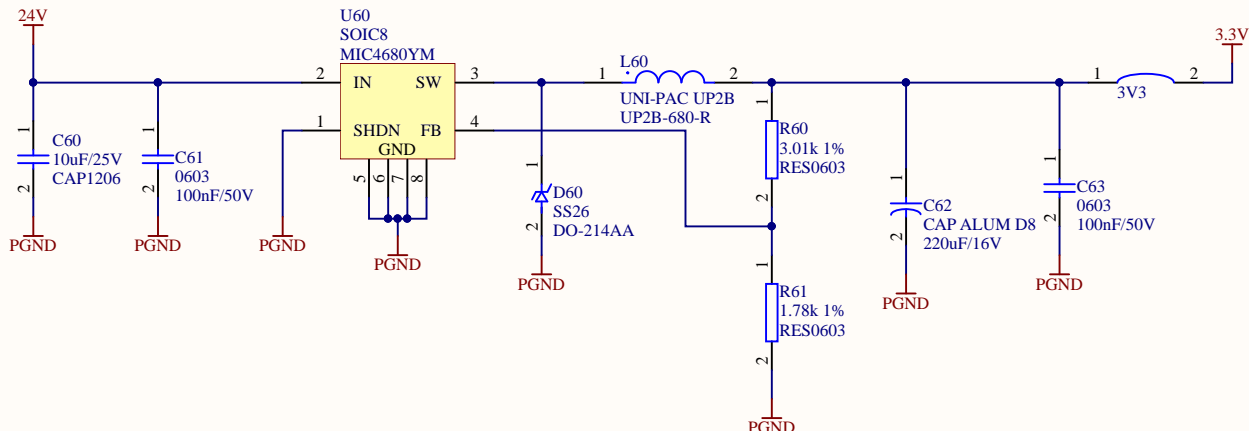


Projekt: <b>P600-RLY.PrjPcb</b>		
Velicina: <b>A4</b>	Shema: * MAIN.SchDoc	Rev.: Ver.: 1.0
Datum: 10/28/2024 Vrijeme: 7:43:25 AM Str.: 1 / 5		
File: C:\P600 - ETH\P600 - RLY\P600-RLY\MAIN.SchDoc		

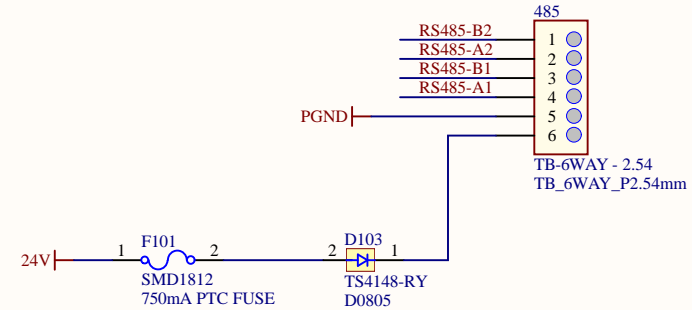
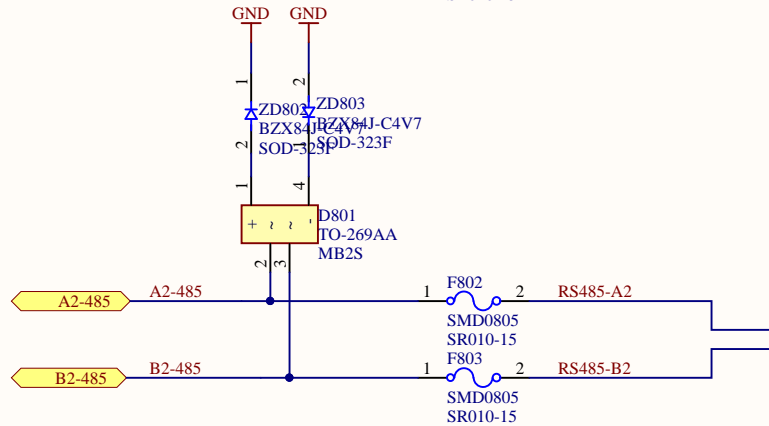
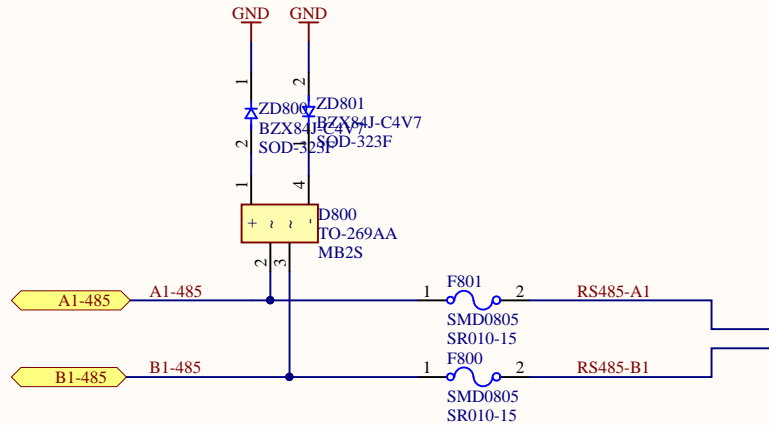
D

D

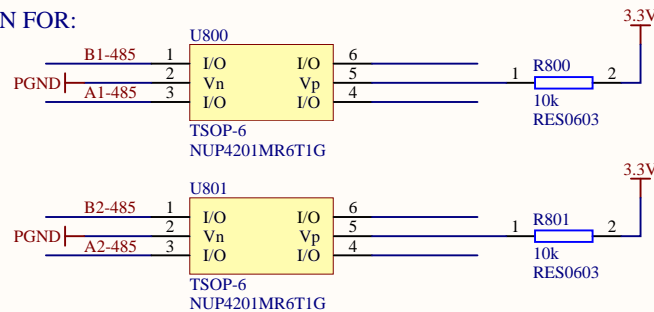




Projekt: <b>P600-RLY.PrjPcb</b>		
Velicina: <b>A4</b>	Shema: <b>* MIC4680.SchDoc</b>	Rev.: <b>Ver.: 1.0</b>
Datum: <b>10/28/2024</b> Vrijeme: <b>7:43:25 AM</b> Str.: <b>3 / 5</b>		
File: <b>C:\P\P600 - ETH\P600 - RLY\P600-RLY\MIC4680.SchDoc</b>		



TVS PROTECTION FOR:  
RS485 LINE AB



Projekt: **P600-RLY.PrjPcb**

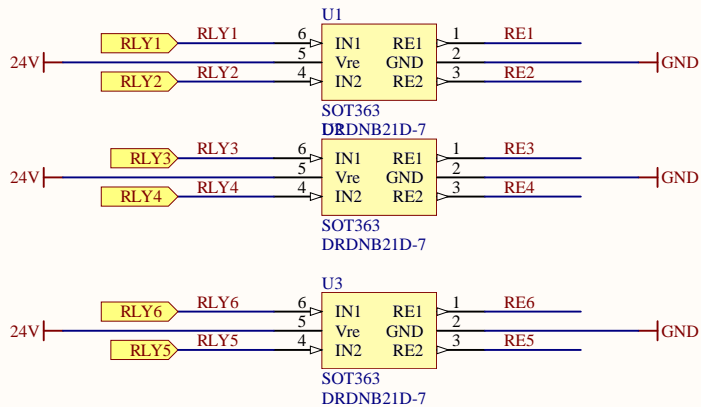
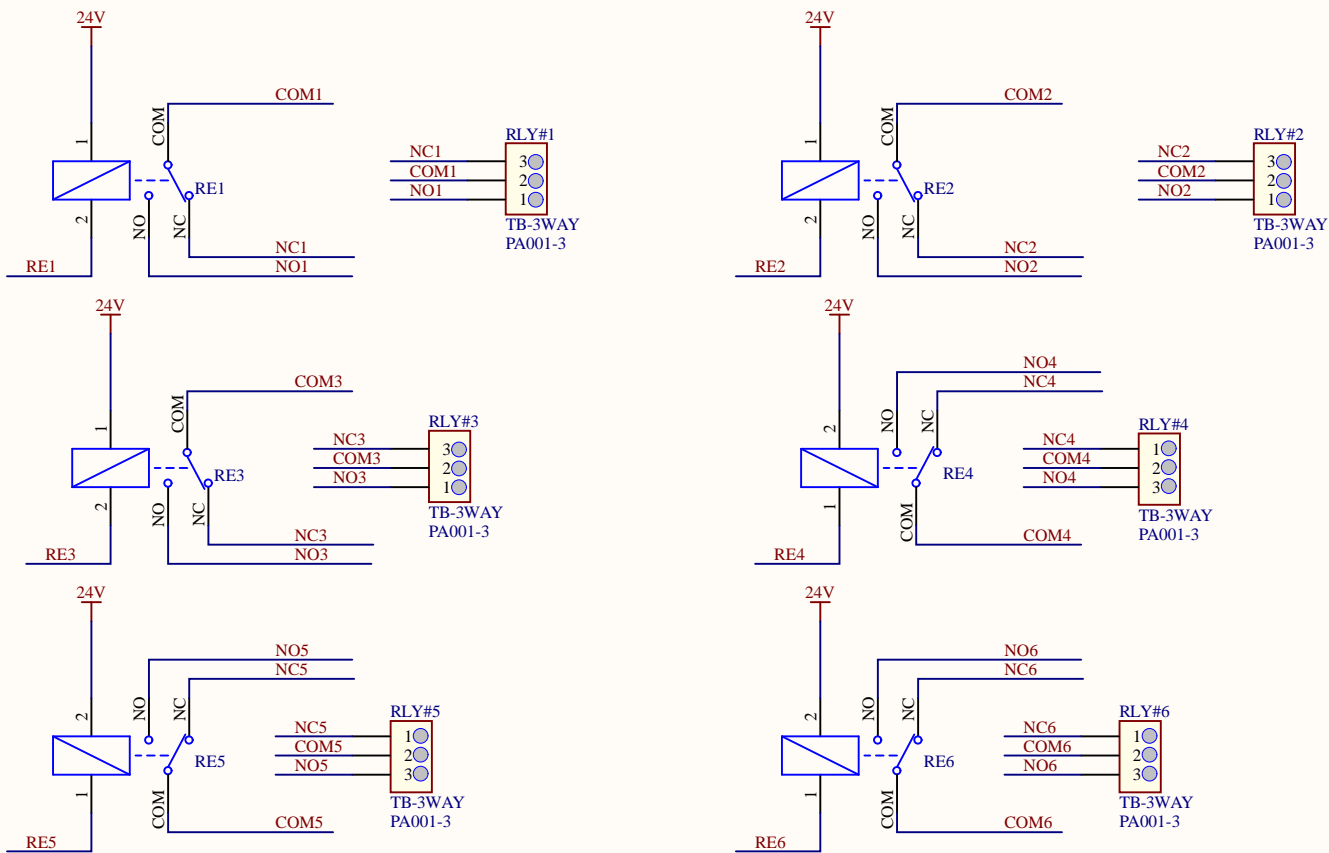
Velicina: A4

Shema:  
RS485-PROTECT.SchDoc

Rev.: Ver.: 1.0

Datum: 10/28/2024 Vrijeme: 7:43:25 AM Str.: 4 / 5

File: C:\P600 - ETH\P600 - RLY\P600-RLY\RS485-PROTECT.SchDoc



Varistor TME  
B72650M301K72  
SMD 3225

Projekt: **P600-RLY.PrjPcb**

Velicina: A4

Shema: \*  
RELAY.SchDoc

Rev.: Ver.: 1.0

Datum: 10/28/2024 Vrijeme: 7:43:25 AM Str.: 5 / 5

File: C:\P600 - ETH\P600 - RLY\P600-RLY\RELAY.SchDoc

485

RLY#1

RLY#2

RLY#3

C11

RE3

RE1

RE2

I1

L60

U60

D60

R60

R61

C62

NT

U1  
U2  
U3  
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U100

T3

PoE-30W

C303

C302

F400

RE6

RE5

RE4

PoE+30W

C301

C300

ETH

RLY#6

RLY#5

RLY#4

