

Projekt: **P601-MCU.PrjPcb**

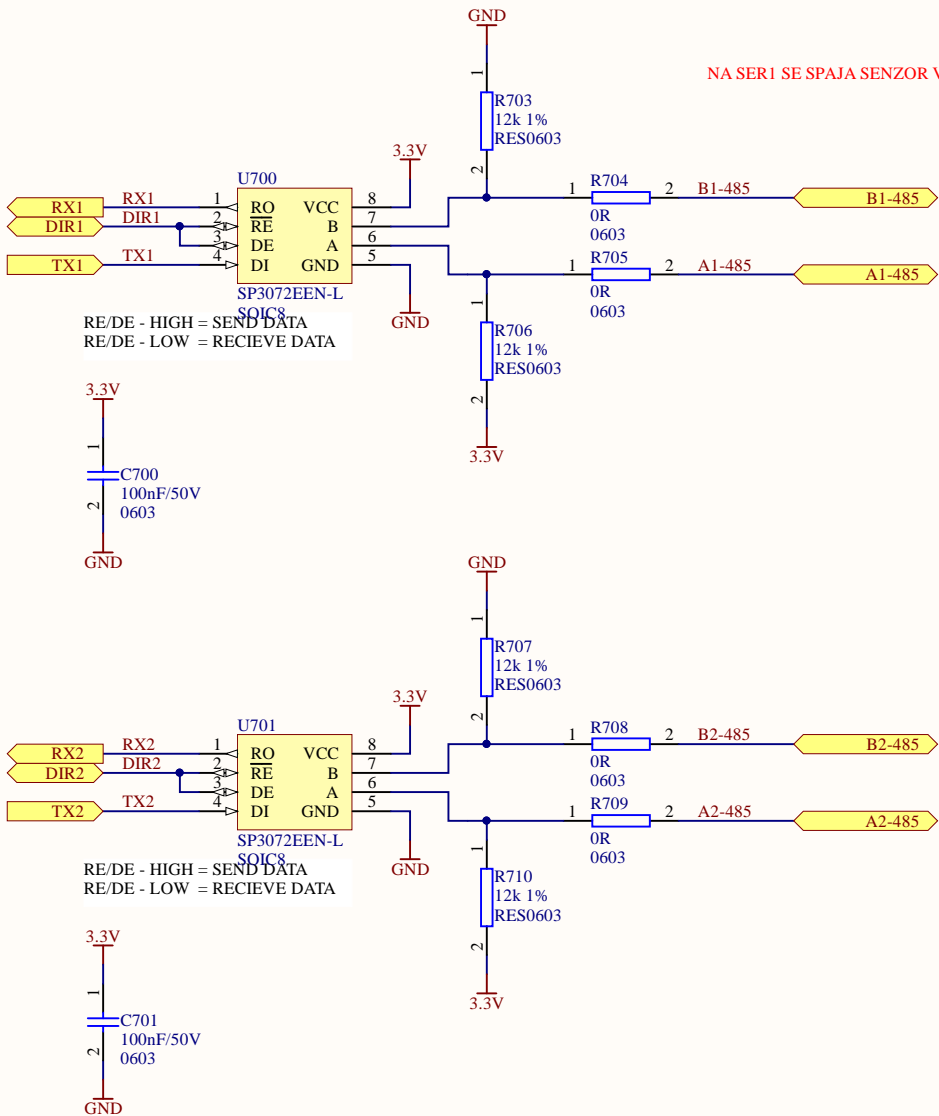
Velicina: **A4**

Shema:  
RTC.SchDoc

Rev.:

Datum: 9/16/2020 Vrijeme: 11:25:56 AM Str.: 4 / 5

File: C:\P\P600 - ETH\P601 - MCU\P601-MCU\RTC.SchDoc



Projekt: **P601-MCU.PrjPcb**

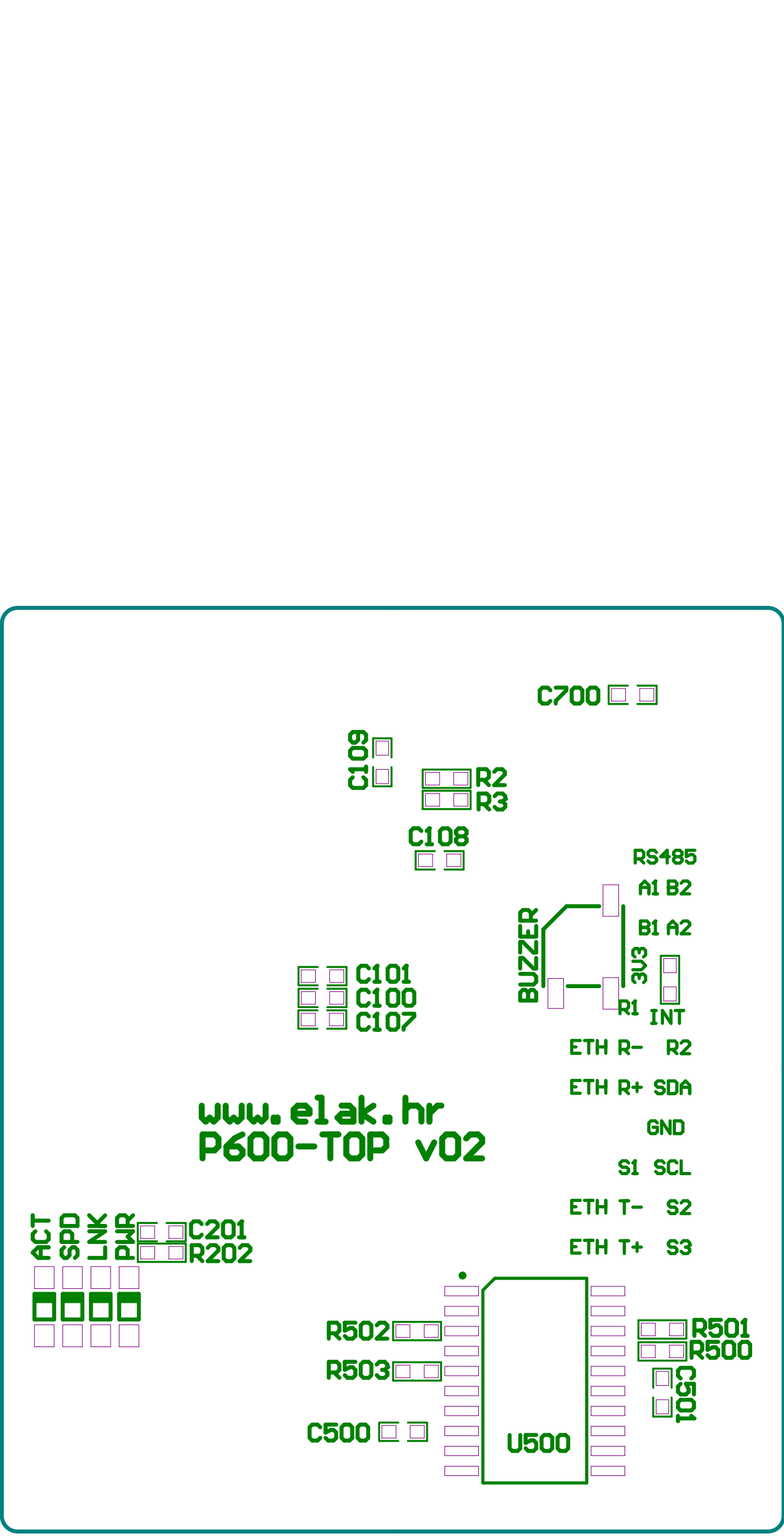
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Shema:  
485\_GPS.SchDoc

Rev.:

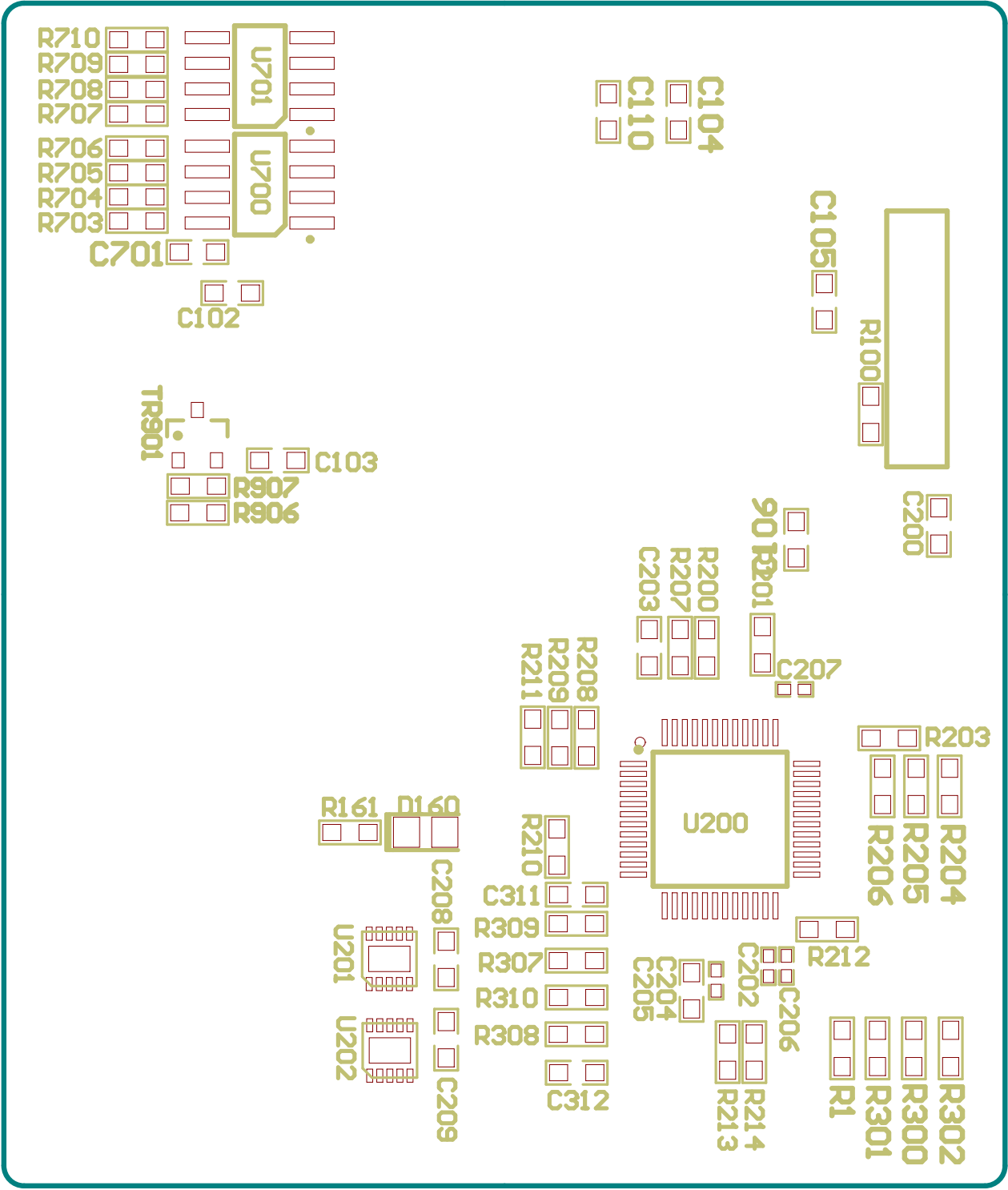
Datum: 9/16/2020 Vrijeme: 11:25:56 AM Str.: **5 / 5**

File: C:\P600 - ETH\P601 - MCU\P601-MCU\485\_GPS.SchDoc



Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.015mm	3.5	
3	Top Layer	Copper	0.036mm		
4	Dielectric 1	FR-4	1.550mm	4.8	
5	Bottom Layer	Copper	0.036mm		
6	Bottom Solder	Solder Resist	0.015mm	3.5	
7	Bottom Overlay				





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1	Top Overlay				
2	Top Solder	Solder Resist	0.015mm	3.5	
3	Top Layer	Copper	0.036mm		
4	Dielectric 1	FR-4	1.520mm	4.8	
5	Bottom Layer	Copper	0.036mm		
6	Bottom Solder	Solder Resist	0.015mm	3.5	
7	Bottom Overlay				