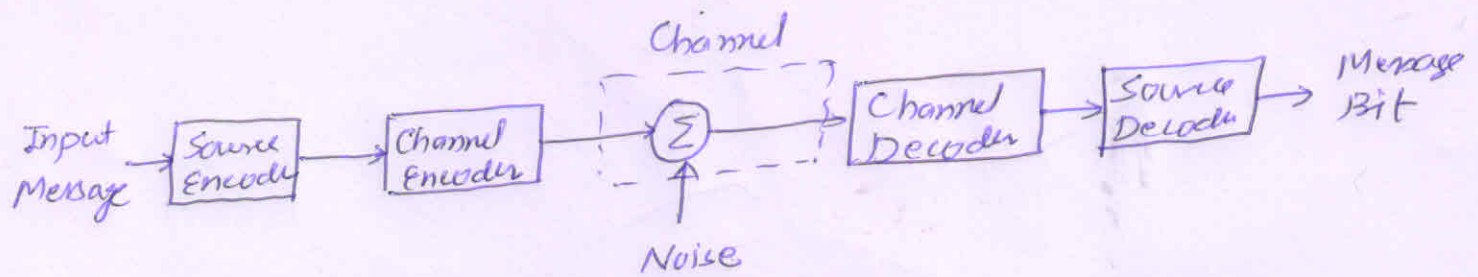


Error Control Coding



Channel Coding: The channel coder introduces systematic redundancy into the data stream by adding bits to the message bits in such a way as to facilitate the detection and correction of bit error in the original binary message sequence at the receiver. The channel decoder in the receiver exploits the redundancy to decide which message bits are actually transmitted. The combined objective of the channel encoder and decoder is to minimize the effect of channel noise.

Method of Controlling Errors.

- ① Forward Acting Error correction
- ② Error detection with retransmission.

I	II
(1) error detection and correction by receiver	① detection by receiver
② Fast	② slow
③ higher prob. of error	③ lower prob. of error

Types of Error

(A) Random Error

- (i) Due to Gaussian Noise
- (ii) uncorrelated error.
- (iii) not effect subsequent interval.

(B) Burst Error

- (i) impulsive Noise
- (ii) Source: lightning & switching transients.
- (iii) Effect several ~~sub~~ successive symbols.

Important terms.

BLOCK CODE: In block codes the binary message or data sequence is divided into sequential blocks each k bit long and each k bit block is converted into an n bit block, where $n > k$. The resultant block code is called (n, k) block code. Each of 2^k data words is mapped to a unique code word. The ratio k/n is called the code rate.

(A) Binary Field.

Addition

$$0 \oplus 0 = 0, 1 \oplus 1 = 0, 0 \oplus 1 = 1, 1 \oplus 0 = 1$$

multiplication.

$$0 \cdot 0 = 0, 0 \cdot 1 = 0, 1 \cdot 0 = 0, 1 \cdot 1 = 1$$

(B) Linear Block code

$$a = \{a_1, a_2, a_3, \dots, a_n\}$$

$$b = \{b_1, b_2, b_3, \dots, b_n\}$$

two code word in a code C

If sum of $a \oplus b$ is also a code word in C then C is linear Block code.

Example (4,2) LBC

1100

0011

1111

0000

$$\begin{array}{r} 1100 \\ \oplus 0011 \\ \hline 1111 \end{array}$$

$$\begin{array}{r} 1100 \\ \oplus 1111 \\ \hline 0011 \end{array}$$

$$\begin{array}{r} 1100 \\ \oplus 0000 \\ \hline 1100 \end{array}$$

(C) Hamming Distance

The hamming distance between the two code is equal to the number of elements in which they differ.

$$a = \{1100\}$$

$$b = \{1111\}$$

$$d(a, b) = 2$$

(D) Hamming Weight $w(c)$

$w(c)$ is the number of 1's in c .

$$\cancel{w(1100)} \quad w(a) = 2$$

$$w(b) = 4$$

(E) Minimum Hamming Distance d_{\min}

d_{\min} is smallest Hamming distance between any pair of code word in C

Note d_{\min} is the smallest Hamming weight of the non-zero code word in C .

Code	Hamming Weight $w(c)$
1100	2
0011	2
1111	4
0000	0

$$d_{\min} = 2$$

(E). Error detection and correction capabilities.

Detect upto 's' error per code word if

$$d_{\min} \geq s + 1$$

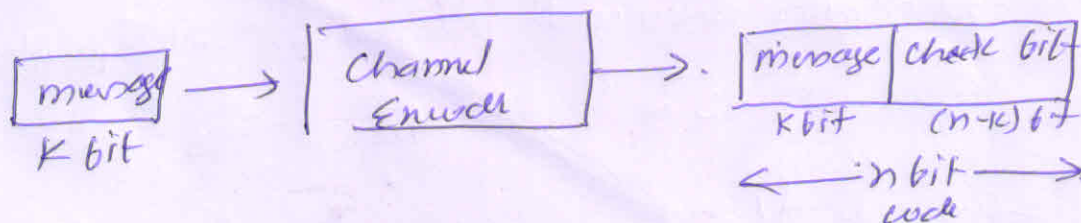
correct upto 't' error per code word if

$$d_{\min} \geq 2t + 1$$

(F). Systematic and Non systematic code.

In systematic block code, the data bits appear in specified location of C.

In non systematic code it is not possible to identify message bits and check bits.



Matrix Representation of LBC

(i) Data Matrix. $d = [d_1 d_2 d_3 \dots d_k]_{1 \times k}$

(ii) Code Matrix $c = [c_1 c_2 c_3 \dots c_n]_{1 \times n}$

~~(iii) Generator Matrix~~

Parity Equations. (6, 3).

$$c_1 = d_1$$

$$c_2 = d_2$$

$$c_3 = d_3$$

$$c_4 = d_1 \oplus d_2$$

$$c_5 = d_1 \oplus d_2 \oplus d_3$$

$$c_6 = d_2 \oplus d_3$$

Parity Equations.

(iii) Parity Matrix P

$$P = \begin{bmatrix} & d_1 & d_2 & d_3 \\ & 1 & 1 & 0 \\ & 1 & 1 & 1 \\ & 0 & 1 & 1 \end{bmatrix}$$

$$P^T = \begin{bmatrix} 1 & 1 & 0 \\ 1 & 1 & 1 \\ 0 & 1 & 1 \end{bmatrix}$$

(iv) Generator Matrix

$$G = [I_k \ P^T]_{k \times n}$$

$$= [I_3 \ P^T]$$

$$= \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 1 \\ 0 & 0 & 1 & 0 & 1 & 1 \end{bmatrix}$$

(v) Code C

$$c = dG$$

Example $(6, 3)$

$$d = \text{110}$$

$$c = ?$$

Sol
method I

$$d_1 = 1$$

$$d_2 = 1$$

$$d_3 = 0$$

$$c_1 = d_1 = 1$$

$$c_2 = d_2 = 1$$

$$c_3 = d_3 = 0$$

$$c_4 = d_1 \oplus d_2 = 1 \oplus 1 = 0$$

$$c_5 = d_1 \oplus d_2 \oplus d_3 = 1 \oplus 1 \oplus 0 = 0$$

$$c_6 = d_2 \oplus d_3 = 1 \oplus 0 = 1$$

$$c = \left[\begin{array}{ccc|ccc} 1 & 1 & 0 & 0 & 0 & 1 \end{array} \right]$$

data check bit.

method II

$$c = dG$$

$$= \begin{bmatrix} 1 & 1 & 0 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 1 \\ 0 & 0 & 1 & 0 & 1 & 1 \end{bmatrix}$$

$$= \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & 1 \end{bmatrix}$$