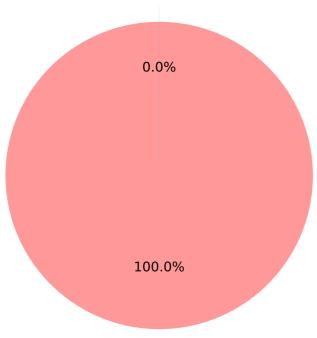
RISC-V Instruction Distribution Memory Instructions



Register Instructions