|  |  |  |
| --- | --- | --- |
| REFS1 | REFS0 | Voltage Reference Selection |
| 0 | 0 | AREF, Internal Vref turned off |
| 0 | 1 | AVCC with exsternal capacitor at AREF pin |
| 0 | 0 | Reserved |
| 0 | 1 | Internal 2.256v Voltages Reference with exsternal capacitor at AREF pin |

|  |  |  |  |
| --- | --- | --- | --- |
| ADSPS2 | ADPS1 | ADPS0 | Division Factor |
| 0 | 0 | 0 | 2 |
| 0 | 0 | 1 | 2 |
| 0 | 1 | 0 | 4 |
| 0 | 1 | 1 | 8 |
| 1 | 0 | 0 | 16 |
| 1 | 0 | 1 | 32 |
| 1 | 1 | 0 | 64 |
| 1 | 1 | 1 | 128 |
|  |  |  |  |