Name: - Vaishanvi Sanjayrao Durge

Contact No. :- +91 9021212953

Email Address: - vaishanvidurge235@gmail.com

OBJECTIVE:-

I look forward to working in a challenging environment, which offers scope for continuous learning, where I can apply my skills & ability for learning new things and satisfying my company by my performance**.**

EDUCATIONAL QUALIFICATION:-

Qualification	Name of College/School	Year of Passing	Class
Certification as full stack developer	Qspider Decan	-	Certification
BTech(EE Engg)	Government College Of Engineering Yavtmal	2022	83%
H.S.C	S.F.L Jr. College,dhamangaon, Railway	2018	65%
S.S.C	Smt.Hirabai Goyenka kanya School,dhamangaon,Railway	2016	69%

INTERSHIP:

M/S Samarth Electrical &Contractor[60days]
- Understand the single phase transformer
- Deep knowledge three phase transformer
- Understand the knowledge of erection and commissioning work of HT/LT lines and 3 Phase Distribution Transformers to release electric connection to the Agriculture Consumers

PCB DESIGINING [45ays]
- Understanding Basic electronic components.
- In depth knowledge of modern pcb manufacture.
- Able to understand and apply Pcb design procedure.

PROJECT KEY:

ENGINEERING PROJECT TITTLE:

SUPER-CAPACITOR AND BATTERY POWER MANAGEMENT IN ELECTRIC VECHICLE
Being the leader of the team, Designing and Execution of our the super capacitors and batteries associate with a good power management present a promising solution. This gave incentive to leadership qualities. Though having Active members in the team, our model worked better than our expectation.

CERTIFICATION:
- Completed Expert full stack developer Course in Qspider / Jspider Technologies.
- PLC Programming.
- Embedded Systems.
- Master data management by tcs.
- C and C++ by internshala.

TECHNICAL SKILLS:

SQL:

Good knowledge of **RDBMS, DDL ,DML** etc.

Good understanding **Sql querie**s.

Good knowledge about concepts like **Joins,Constraints , Clauses,Operations,** etc

JAVA:

Good knowledge of basic of java.

Well understanding of **JVM**.

Good knowledge of OPPs(**Encapsulations,Inheritance ,Polymorphisim, Abstraction**).

Good knowledge of **ARRAY.**

MANUAL TESTING:

Depth understanding of **defect life cycle**.

Very good knowledge of **SDLC,STLC**

Strong understanding **Functional,Integration ,System testing**

Efficient in writing test cases

.

WEB DEVELOPMENT:

Good knowledge of **HTML,CSS, Javascrpit**

STRENGTH:-

- Good problem solving skills
- Excellent Communication skills
- Leadership qualities

PERSONAL DETAILS:-

- **Date of Birth**: - 23 May, 2000.
- **Language Known**: - Marathi, English, Hindi.
- **Marital Status : -** Single
- **Permanent Address**: - Devgaon, Dhamagaon Railway, Dist- Amravati.

DECLARATION & SIGN: -

I hereby declare that the above information is true to the best of my knowledge & belief.

Date:

Place: Pune. (Vaishanavi Sanjayrao Durge)