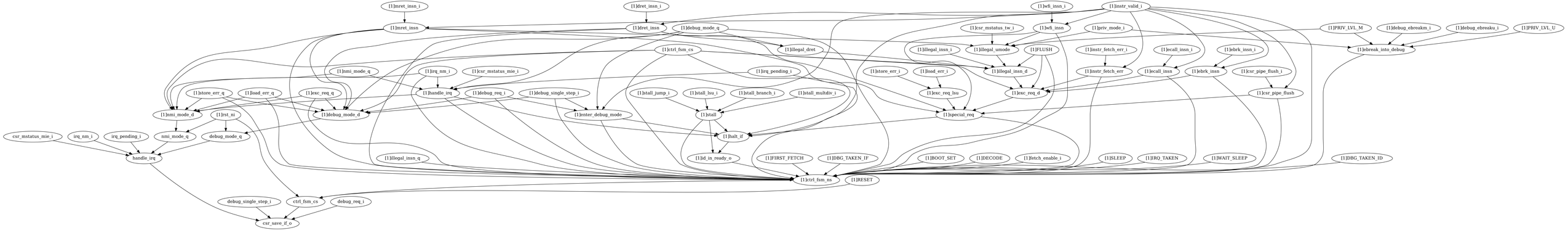
[1]mret_insn_i
[1]dret_insn_i
[1]wfi_insn_i
[1]instr_valid_i
[1]mret_insn
[1]dret_insn
[1]debug_mode_q
[1]csr_mstatus_tw_i
[1]wfi_insn
[1]priv_mode_i
[1]PRIV_LVL_M
[1]debug_ebreakm_i
[1]debug_ebreaku_i
[1]PRIV_LVL_U
[1]ctrl_fsm_cs
[1]illegal_dret
[1]illegal_insn_i
[1]illegal_umode
[1]FLUSH
[1]instr_fetch_err_i
[1]ecall_insn_i
[1]ebrk_insn_i
[1]ebreak_into_debug
[1]nmi_mode_q
[1]irq_nm_i
[1]csr_mstatus_mie_i
[1]irq_pending_i
[1]illegal_insn_d
[1]store_err_i
[1]load_err_i
[1]instr_fetch_err
[1]ecall_insn
[1]ebrk_insn
[1]csr_pipe_flush_i
[1]store_err_q
[1]load_err_q
[1]exc_req_q
[1]handle_irq
[1]debug_req_i
[1]debug_single_step_i
[1]stall_jump_i
[1]stall_lsu_i
[1]stall_branch_i
[1]stall_multdiv_i
[1]exc_req_lsu
[1]exc_req_d
[1]csr_pipe_flush
[1]nmi_mode_d
[1]debug_mode_d
[1]enter_debug_mode
[1]stall
[1]special_req
[1]rst_ni
csr_mstatus_mie_i
irq_nm_i
irq_pending_i
nmi_mode_q
debug_mode_q
[1]halt_if
handle_irq
[1]illegal_insn_q
[1]id_in_ready_o
[1]FIRST_FETCH
[1]DBG_TAKEN_IF
[1]BOOT_SET
[1]DECODE
[1]fetch_enable_i
[1]SLEEP
[1]IRQ_TAKEN
[1]WAIT_SLEEP
[1]DBG_TAKEN_ID
[1]ctrl_fsm_ns
[1]RESET
debug_single_step_i
ctrl_fsm_cs
debug_req_i
csr_save_if_o