368:AL
369:IF
!(rst_i)
rst_i
373:BL
369:BL
reg26 <= 16'h0000;
codecReady <= 1'b0;
374:IF
RESET
!(RESET)
374:BL
387:NS
codecReady <= 1'b0;
375:IF
pe_sync
375:BL
381:IF
376:IF
(slot0i[15] == 1'b1)
(slot0i[15:13] == 3'b111)
376:BL
381:BL
codecReady <= 1'b1;
377:IF
(slot1i[18:12] == 7'h26)
377:BL
reg26 <= slot2i[19:4];
Leaf_368:AL
53:AS
cs = cyc_i && stb_i && (adr_i[63:8] == 56'hFFFF_FFFF_FFDC_10);
179:AL
55:AL
56:NS
ack1 <= cs & !ack1;
Leaf_55:AL
180:BL
181:IF
(cs & we_i)
181:BL
regfile[fnLM4550ToRealRegMap(adr_i[6:0])] <= dat_i;
57:AS
ack_o = (cs)? (we_i)? 1'b1 : ack1 : 1'b0;
Leaf_179:AL
176:AS
rfadri = regfile[fnLM4550ToRealRegMap(adr_i[6:0])];
245:NS
rgno <= 5'd1;
Leaf_213:AL
254:AS
doRead = fnRealToLM4550RegMap(rgno) == 7'h26;
175:AS
rfrgno = regfile[rgno];
256:AL
257:IF
rst_i
!(rst_i)
273:BL
257:BL
dirty <= 32'd0;
slot0 <= 0;
slot1 <= 0;
slot2 <= 0;
slot3 <= 0;
slot4 <= 0;
slot5 <= 0;
slot6 <= 0;
slot7 <= 0;
slot8 <= 0;
slot9 <= 0;
slot10 <= 0;
slot11 <= 0;
slot12 <= 0;
276:IF
RESET
276:BL
274:IF
(cs & we_i)
275:NS
dirty[fnLM4550ToRealRegMap(adr_i[6:0])] <= 1'b1;
277:IF
!((codecReady & pe_sync & |dirty))
(codecReady & pe_sync & |dirty)
326:IF
277:BL
(codecReady & pe_sync)
!((codecReady & pe_sync))
350:IF
278:IF
326:BL
slot0[15] <= 1'b1;
slot0[14] <= 1'b0;
slot0[13] <= 1'b0;
slot0[12] <= 1'b1;
slot0[11] <= 1'b1;
slot0[10:0] <= 11'd0;
slot1 <= 20'd0;
slot2 <= 20'd0;
slot3[19:2] <= PSGout;
slot3[1:0] <= 2'b00;
slot4[19:2] <= PSGout;
slot4[1:0] <= 2'b00;
slot6 <= 20'd0;
slot7 <= 20'd0;
slot8 <= 20'd0;
slot9 <= 20'd0;
slot5 <= 20'd0;
slot10 <= 20'd0;
slot11 <= 20'd0;
slot12 <= 20'd0;
pe_sync
!((rgno < 7'd27))
(rgno < 7'd27)
350:BL
slot0 <= 0;
slot1 <= 0;
slot2 <= 0;
slot3 <= 0;
slot4 <= 0;
slot5 <= 0;
slot6 <= 0;
slot7 <= 0;
slot8 <= 0;
slot9 <= 0;
slot10 <= 0;
slot11 <= 0;
slot12 <= 0;
301:BL
dirty[rgno] <= 1'b0;
slot0[15] <= 1'b1;
slot0[14] <= 1'b0;
slot0[13] <= 1'b0;
slot0[12] <= 1'b1;
slot0[11] <= 1'b1;
slot0[10:0] <= 11'd0;
slot1 <= 20'd0;
slot2 <= 20'd0;
slot3[19:2] <= PSGout;
slot3[1:0] <= 2'b00;
slot4[19:2] <= PSGout;
slot4[1:0] <= 2'b00;
slot6 <= 20'd0;
slot7 <= 20'd0;
slot8 <= 20'd0;
slot9 <= 20'd0;
slot5 <= 20'd0;
slot10 <= 20'd0;
slot11 <= 20'd0;
slot12 <= 20'd0;
278:BL
slot0[15] <= 1'b1;
slot0[14] <= 1'b1;
slot0[13] <= 1'b1;
slot0[12:0] <= 13'd0;
slot1[19] <= doRead;
slot1[18:12] <= fnRealToLM4550RegMap(rgno);
slot1[11:0] <= 12'd0;
slot2[19:4] <= (doRead)? 16'h0000 : rfrgno;
slot2[3:0] <= 4'd0;
slot3 <= 20'd0;
slot4 <= 20'd0;
slot6 <= 20'd0;
slot7 <= 20'd0;
slot8 <= 20'd0;
slot9 <= 20'd0;
slot5 <= 20'd0;
slot10 <= 20'd0;
slot11 <= 20'd0;
slot12 <= 20'd0;
dirty[rgno] <= 1'b0;
Leaf_256:AL
213:AL
189:AL
214:CX
dirty
237:CA
240:CA
234:CA
239:CA
243:CA
235:CA
233:CA
229:CA
244:CA
236:CA
216:CA
224:CA
231:CA
222:CA
226:CA
221:CA
228:CA
215:CA
230:CA
238:CA
217:CA
220:CA
227:CA
219:CA
218:CA
245:CA
247:CA
223:CA
242:CA
232:CA
241:CA
246:CA
225:CA
237:NS
rgno <= 5'd9;
240:NS
rgno <= 5'd6;
234:NS
rgno <= 5'd12;
239:NS
rgno <= 5'd7;
243:NS
rgno <= 5'd3;
235:NS
rgno <= 5'd11;
233:NS
rgno <= 5'd13;
229:NS
rgno <= 5'd17;
244:NS
rgno <= 5'd2;
236:NS
rgno <= 5'd10;
216:NS
rgno <= 5'd30;
224:NS
rgno <= 5'd22;
231:NS
rgno <= 5'd15;
222:NS
rgno <= 5'd24;
226:NS
rgno <= 5'd20;
221:NS
rgno <= 5'd25;
228:NS
rgno <= 5'd18;
215:NS
rgno <= 5'd31;
230:NS
rgno <= 5'd16;
238:NS
rgno <= 5'd8;
217:NS
rgno <= 5'd29;
220:NS
rgno <= 5'd26;
227:NS
rgno <= 5'd19;
219:NS
rgno <= 5'd27;
218:NS
rgno <= 5'd28;
247:NS
rgno <= 5'd0;
223:NS
rgno <= 5'd23;
242:NS
rgno <= 5'd4;
232:NS
rgno <= 5'd14;
241:NS
rgno <= 5'd5;
246:NS
rgno <= 5'd0;
225:NS
rgno <= 5'd21;
190:IF
cs
!(cs)
190:BL
204:NS
dat_o <= 16'h0000;
191:CS
adr_i[6:0]
195:CA
196:CA
200:CA
194:CA
193:CA
199:CA
198:CA
197:CA
192:CA
195:NS
dat_o <= 16'h0000;
196:NS
dat_o <= { 16{ |dirty } };
200:NS
dat_o <= rfadri;
194:NS
dat_o <= reg26;
193:NS
dat_o <= 16'h0101;
199:NS
dat_o <= 16'h4350;
198:NS
dat_o <= 16'h4E53;
197:NS
dat_o <= 16'h0000;
192:NS
dat_o <= 16'h0D50;
Leaf_189:AL