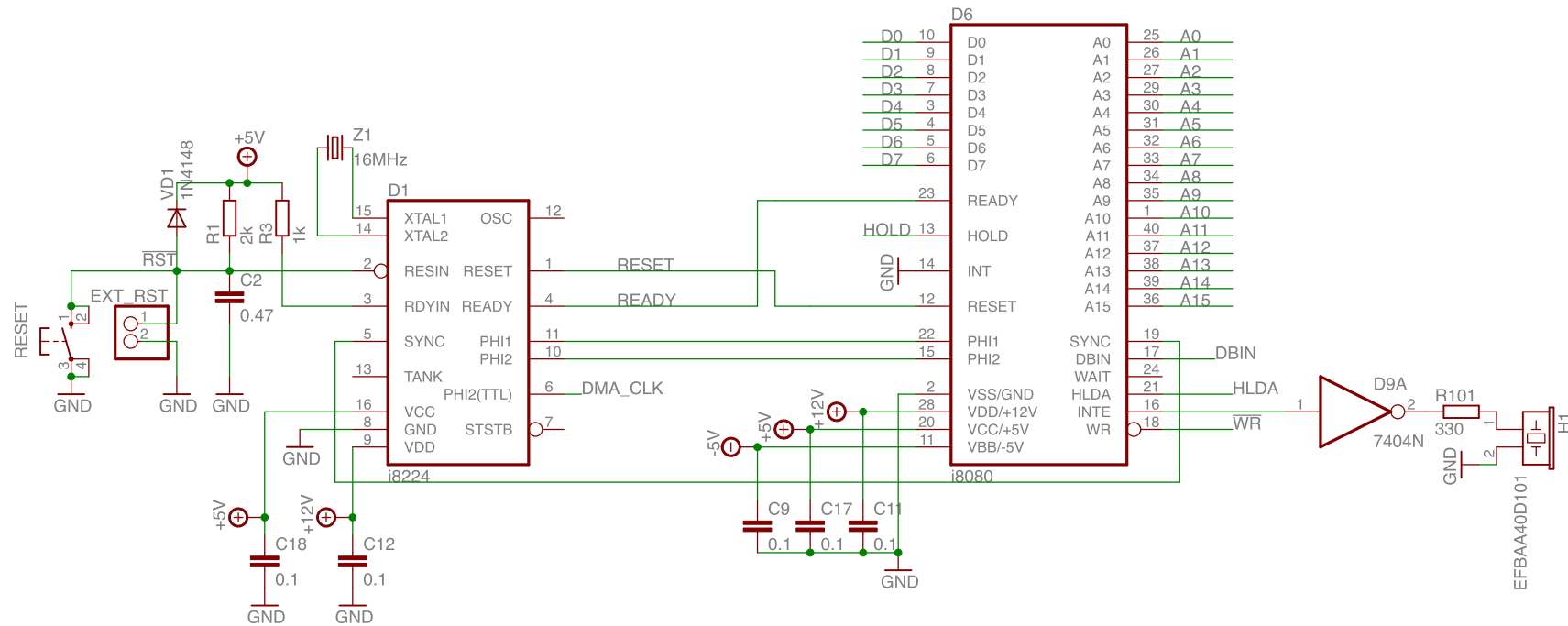


Clock and reset

CPU



CPU and Clock

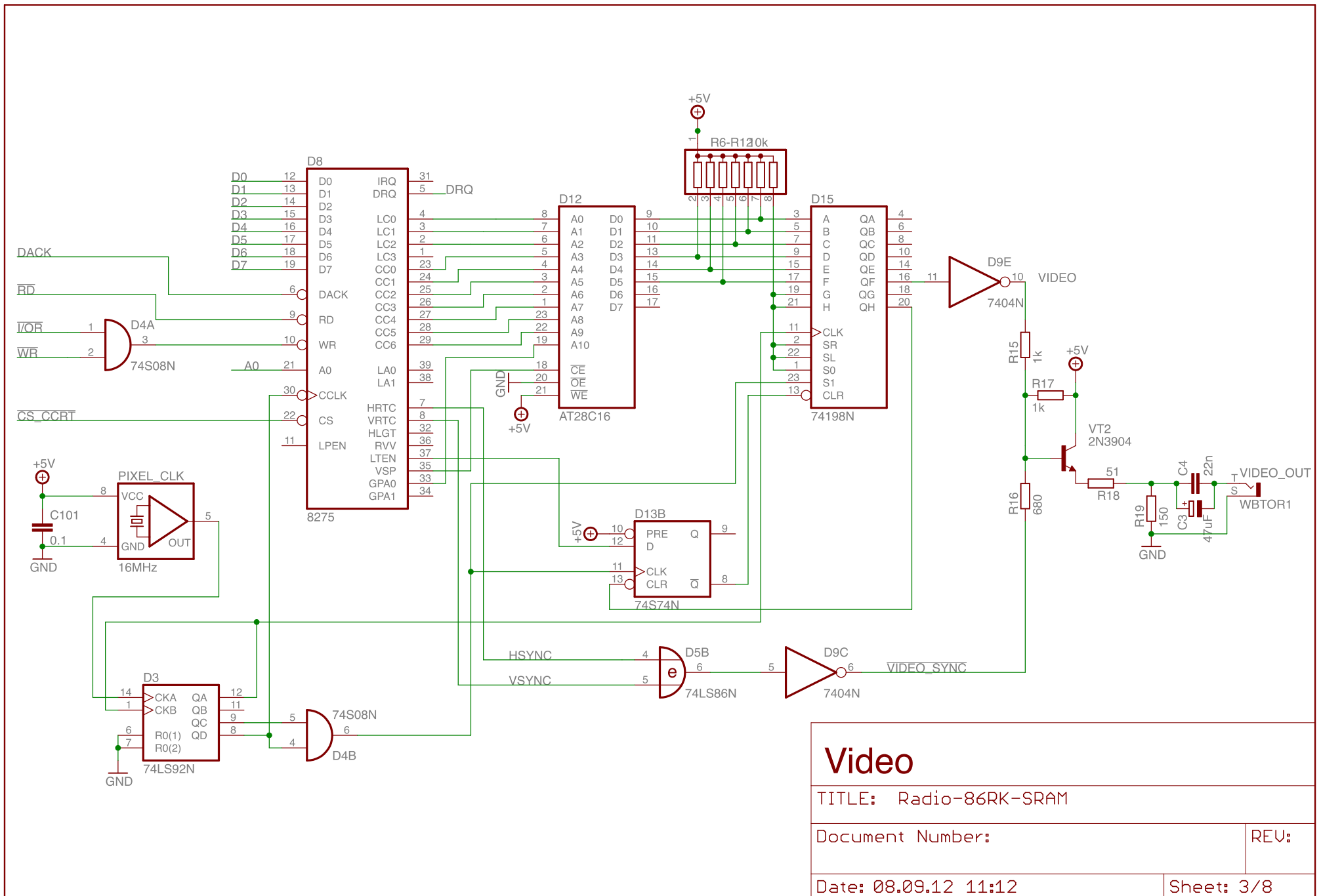
TITLE: Radio-86RK-SRAM

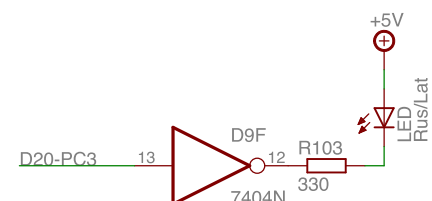
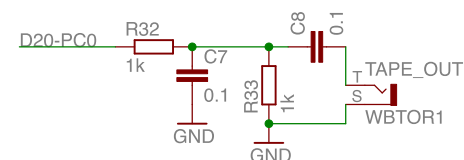
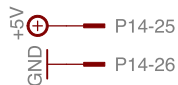
Document Number:

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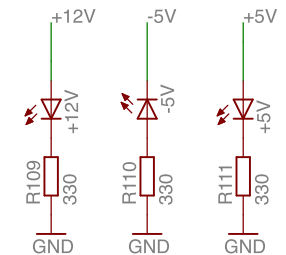
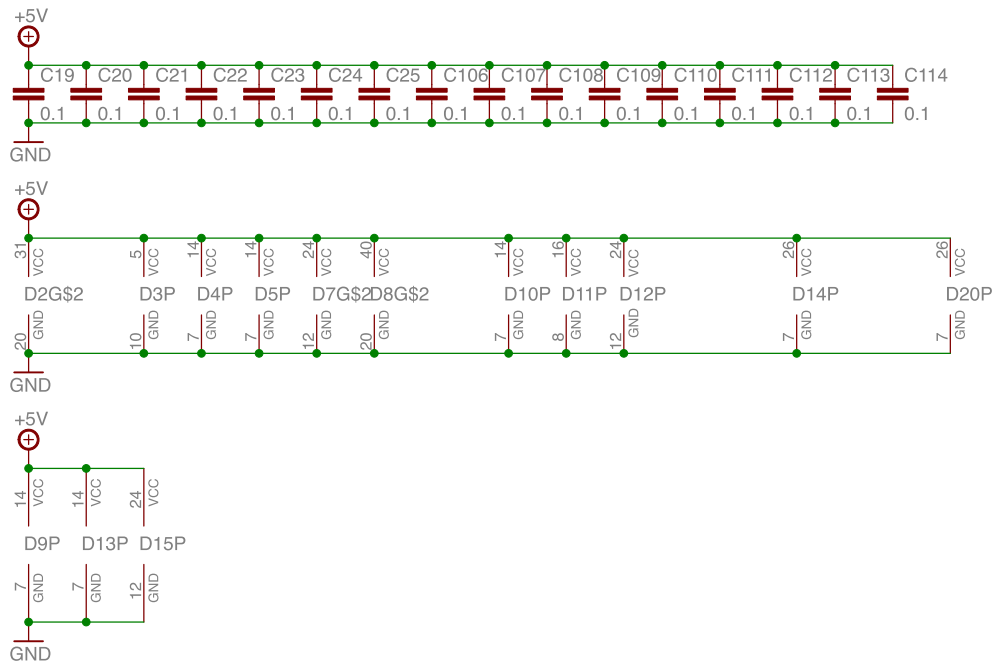
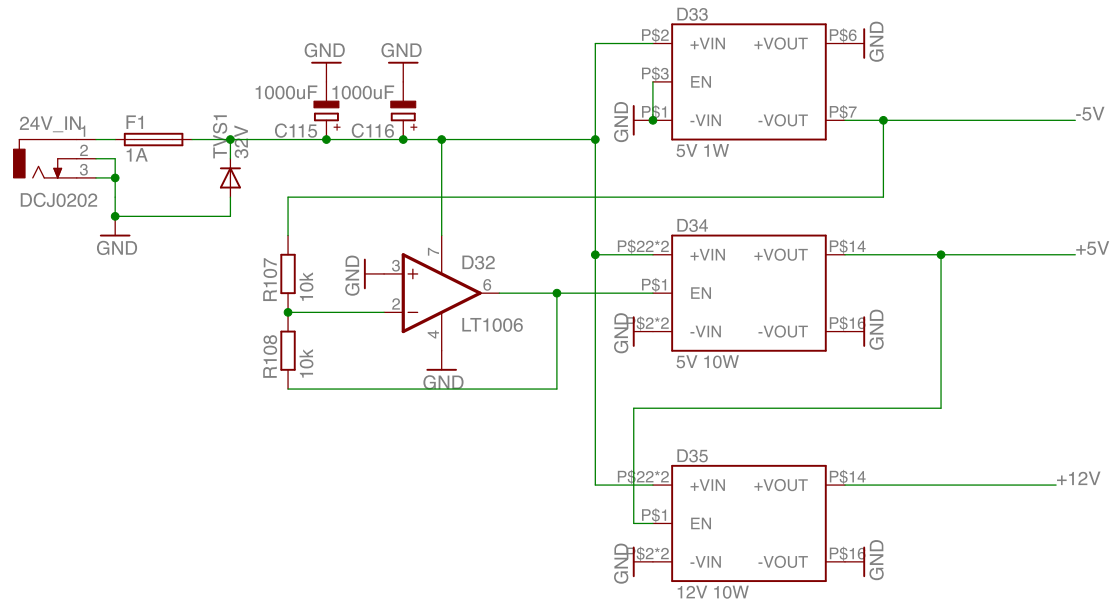
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Power Supply

TITLE: Radio-86RK-SRAM

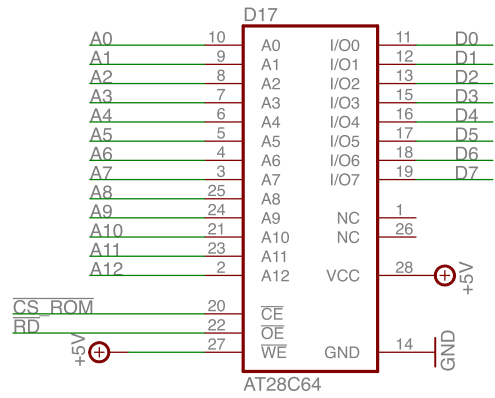
Document Number:

REV:

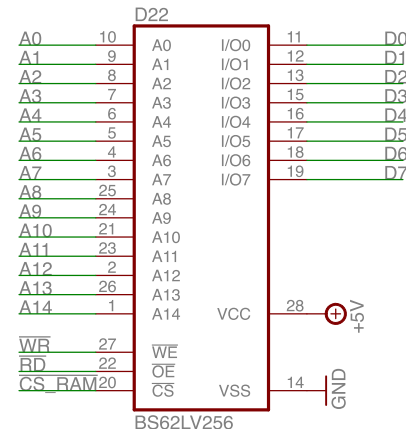
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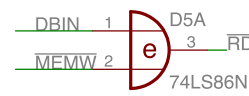
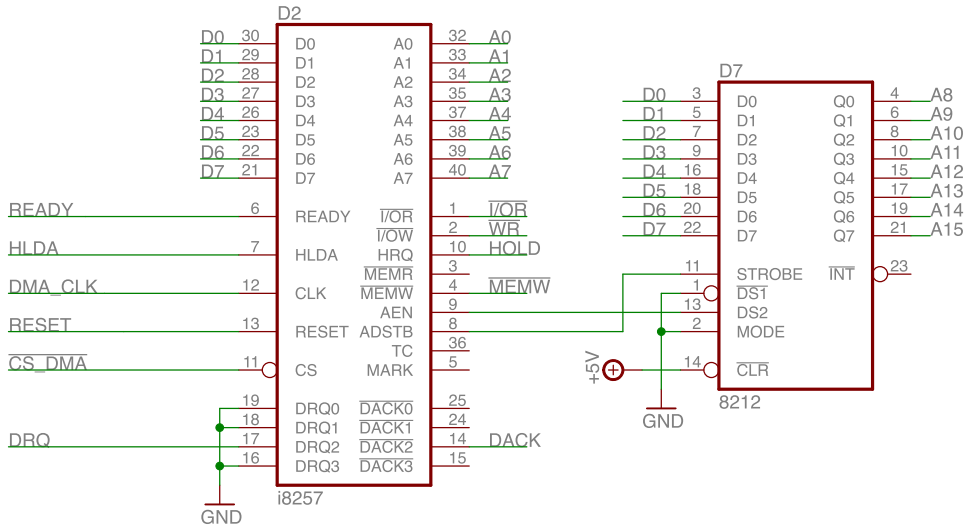
ROM



RAM



DMA



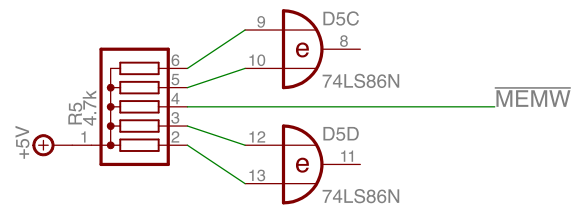
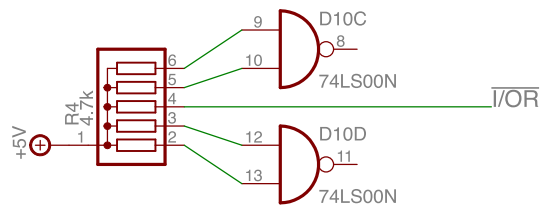
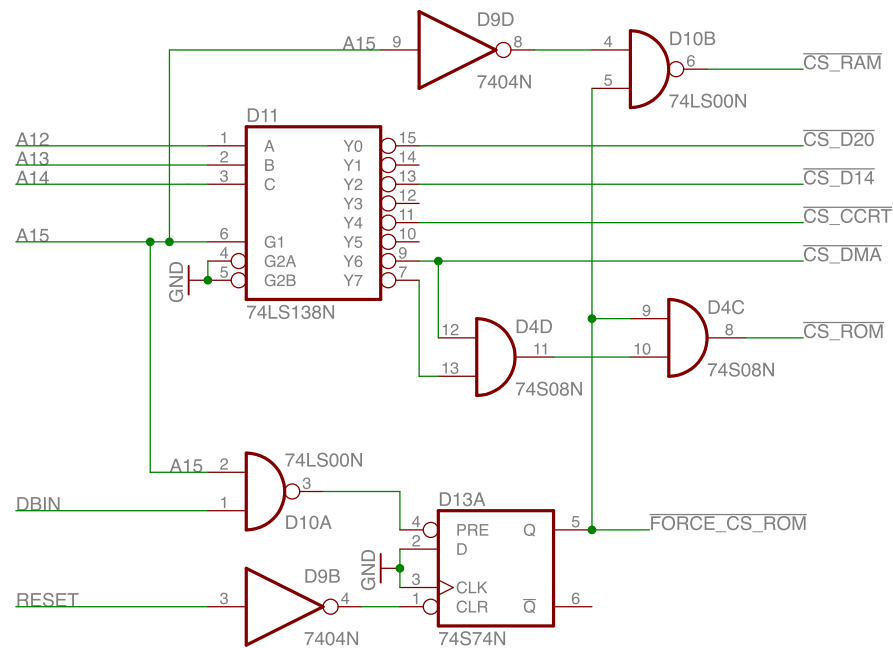
RAM, ROM, DMA

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\overline{CS} decoder



\overline{CS} decoder

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