Jumpers for ZX81+38

JP#	top/bottom	location	function	Default
JP1	bottom	below U23	Size of ROM space 8K/16K	16K ROM
JP2	top	left below J1	Connect 5V to 9V expansion port	open
JP3	NA	nowhere	not used	NA
JP4	top	left below U22	50Hz or 60Hz screen refresh	open 50Hz
JP5	top	left of U9	Pixel invert white or black	white
JP6	top	right of U9	background color white of black	white
JP7	NA	nowhere	not used	NA
JP8	bottom	below U5 (EPROM)	A11 or A12 for pin 23 EPROM	A11
JP9	bottom	below U5 (EPROM)	A13 or VCC for pin 26 EPROM	A13
JP10	bottom	below U5 (EPROM)	VCC or GND for pin 27 EPROM (A14)	GND
JP11	bottom	below U5 (EPROM)	Pin 20 EPROM (nROMCS or A11)	nROMCS
JP12	bottom	below U6 (RAM)	pin 1 (GND or A14) 16K or 32K RAM	16K

A HM62256BLP-7 is a 32K x 8-bit RAM chip, but default only 16K is used.

These are the types of ROM supported, the 27C256 the 28C256 and the original ROM