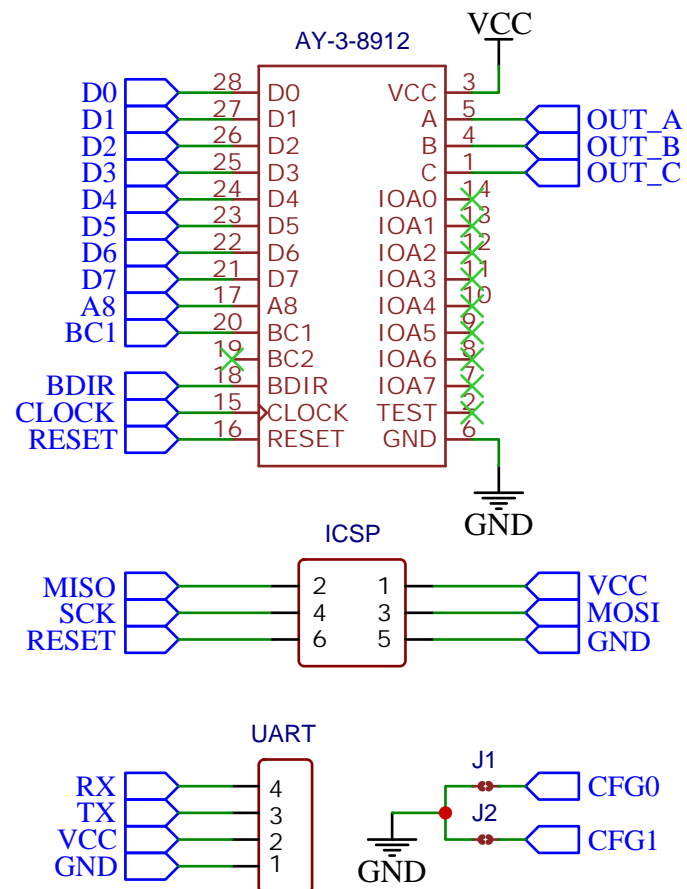
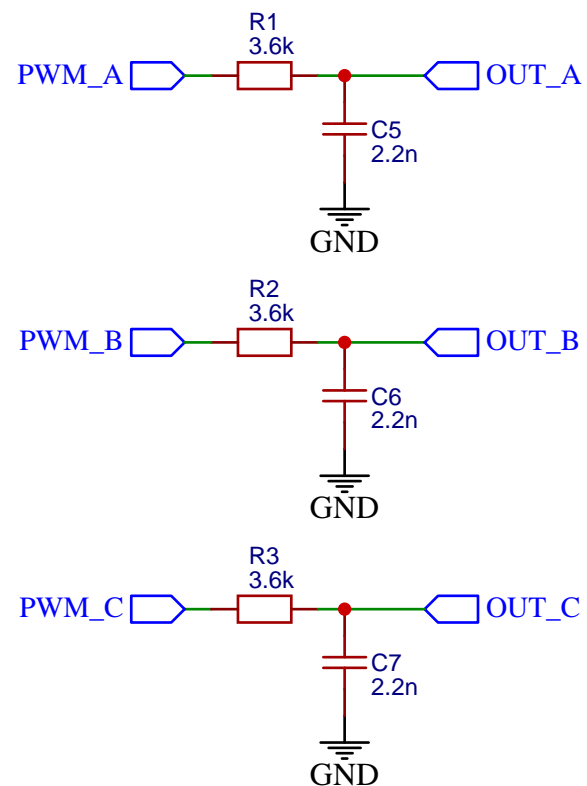


Headers



Low-pass filters



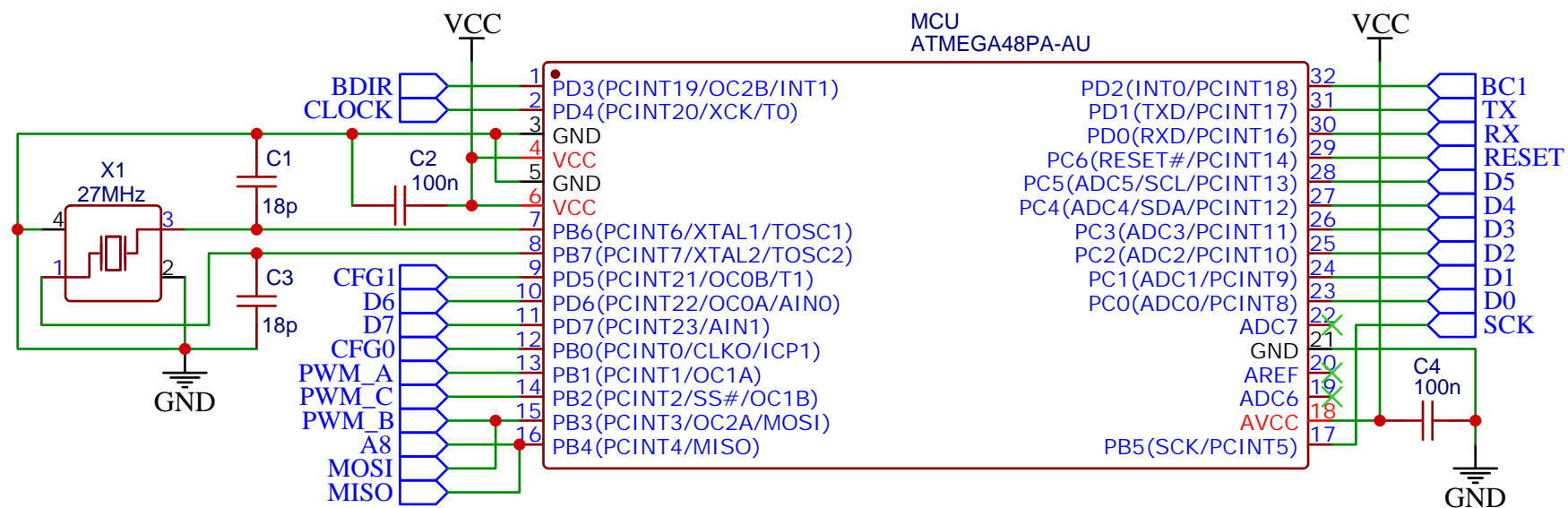
Schematic and PCB notes:

1. The MCU ATmega48 can be replaced with ATmega88, ATmega168 or ATmega328.
2. The X1 oscillator can be replaced with another in range 20 - 40 MHz depending on the firmware configuration.
3. Low-pass filters are designed for a load resistance of 1 kΩ.
4. Jumpers J1 and J2 are designated for configuration the firmware.

For more details please visit:

<https://github.com/Yevgeniy-Olexandrenko/avr-ay-board>

Processing



TITLE: AY-3-8912 Emulator (AVR-AY)		REV: 1.3
EasyEDA	Company: Olexandrenko	Sheet: 1/1
	Date: 2022-06-24	Drawn By: Yevgeniy