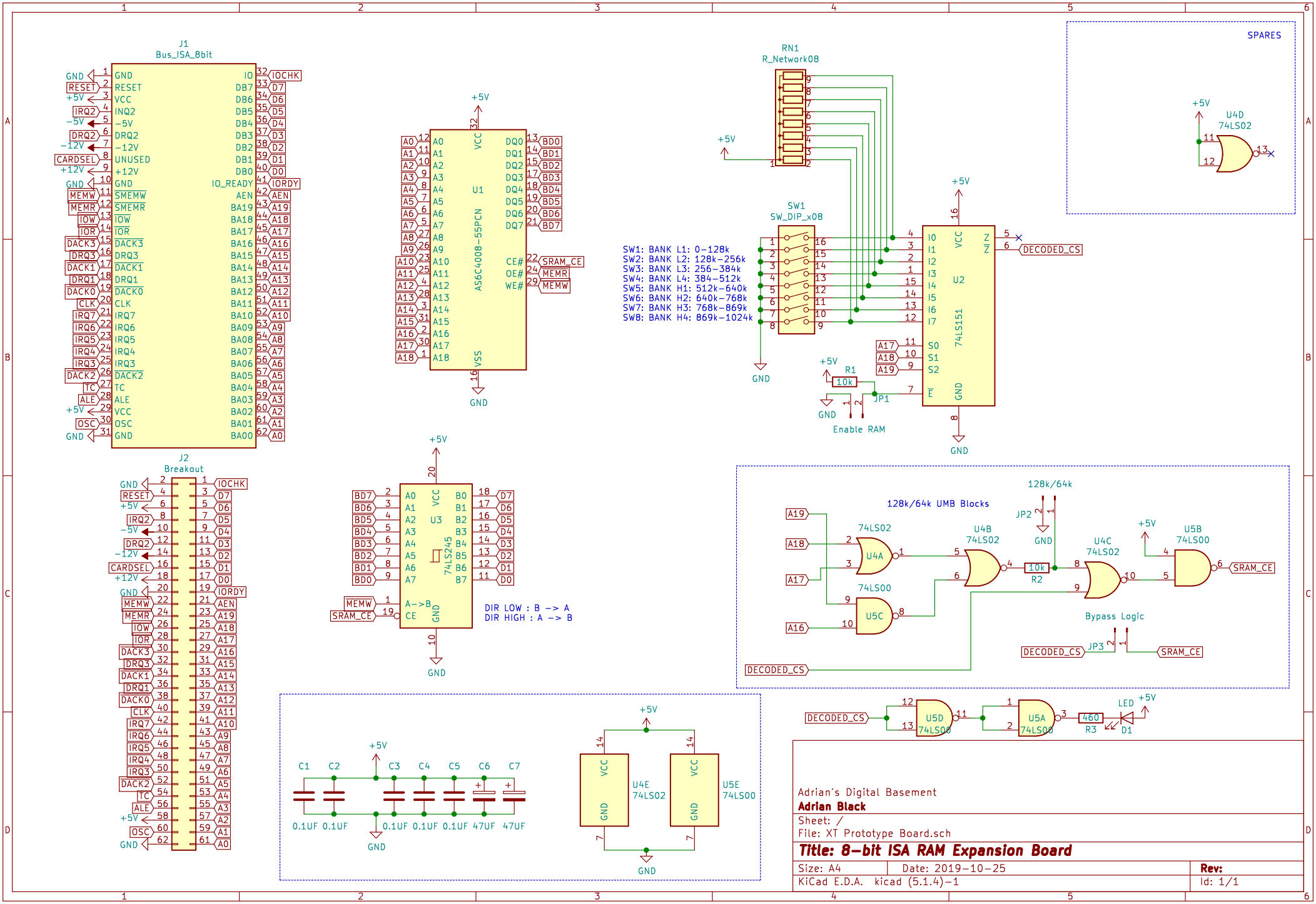
J1
Bus_ISA_8bit
J2
Breakout
RN1
R_Network08
SW1
SW_DIP_x08
SW1: BANK L1: 0-128k
SW2: BANK L2: 128k-256k
SW3: BANK L3: 256-384k
SW4: BANK L4: 384-512k
SW5: BANK H1: 512k-640k
SW6: BANK H2: 640k-768k
SW7: BANK H3: 768k-869k
SW8: BANK H4: 869k-1024k
U1
AS6C4008-55PCN
U2
74LS151
U3
74LS245
DIR LOW : B -> A
DIR HIGH : A -> B
Enable RAM
SPARES
U4D
74LS02
128k/64k UMB Blocks
128k/64k
U4A
U4B
U4C
U5B
U5C
Bypass Logic
U5D
U5A
U4E
U5E
Adrian's Digital Basement
Adrian Black
Sheet: /
File: XT Prototype Board.sch
Title: 8-bit ISA RAM Expansion Board
Size: A4
Date: 2019-10-25
Rev:
KiCad E.D.A. kicad (5.1.4)-1
Id: 1/1