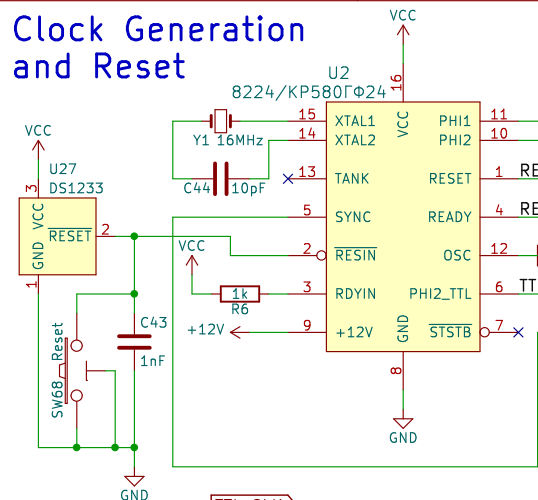
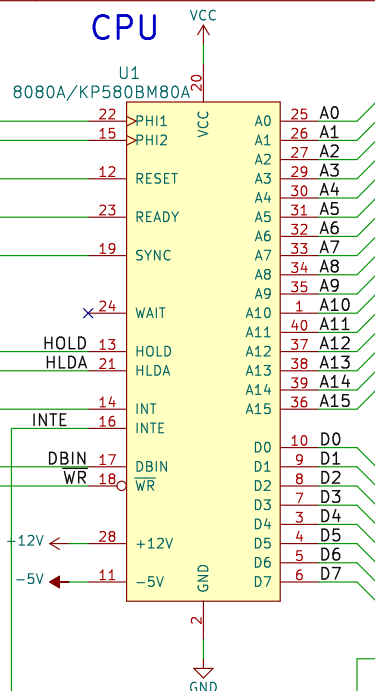


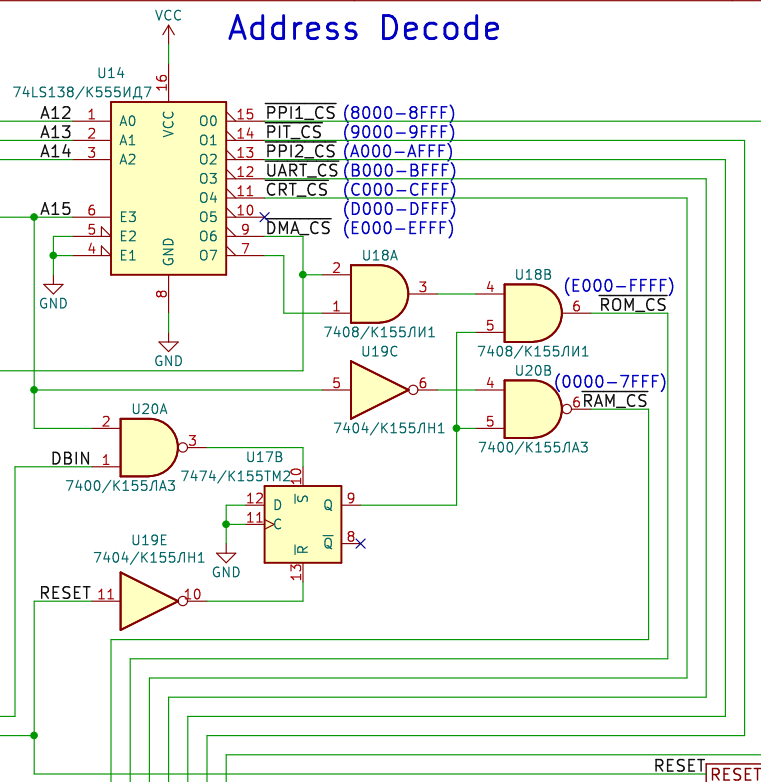
Clock Generation and Reset



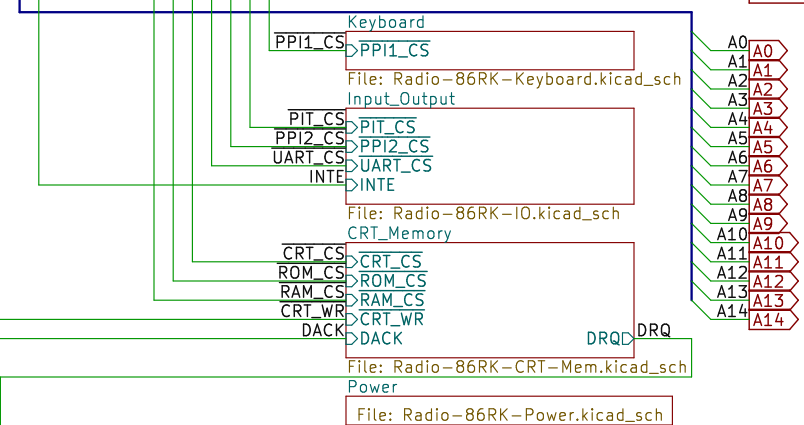
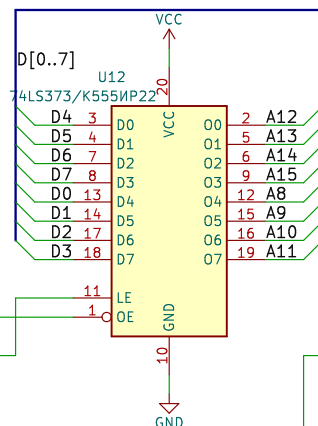
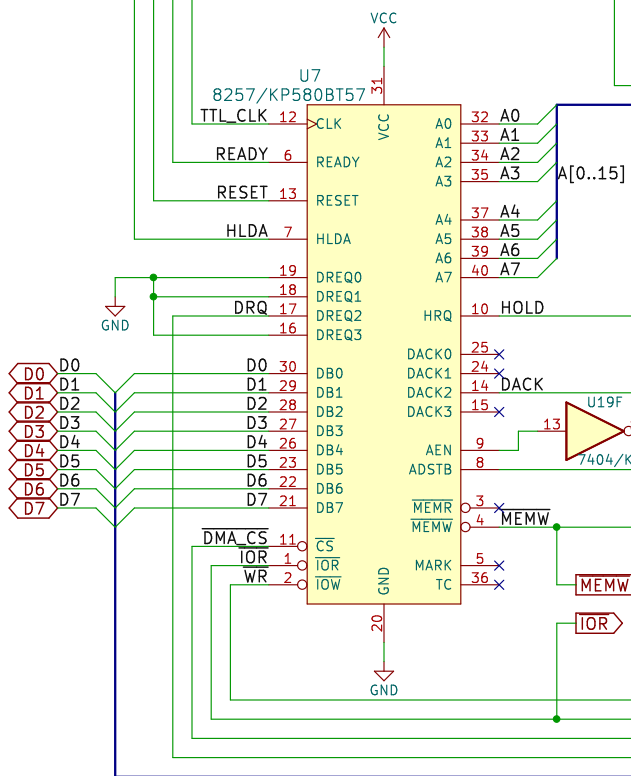
CPU



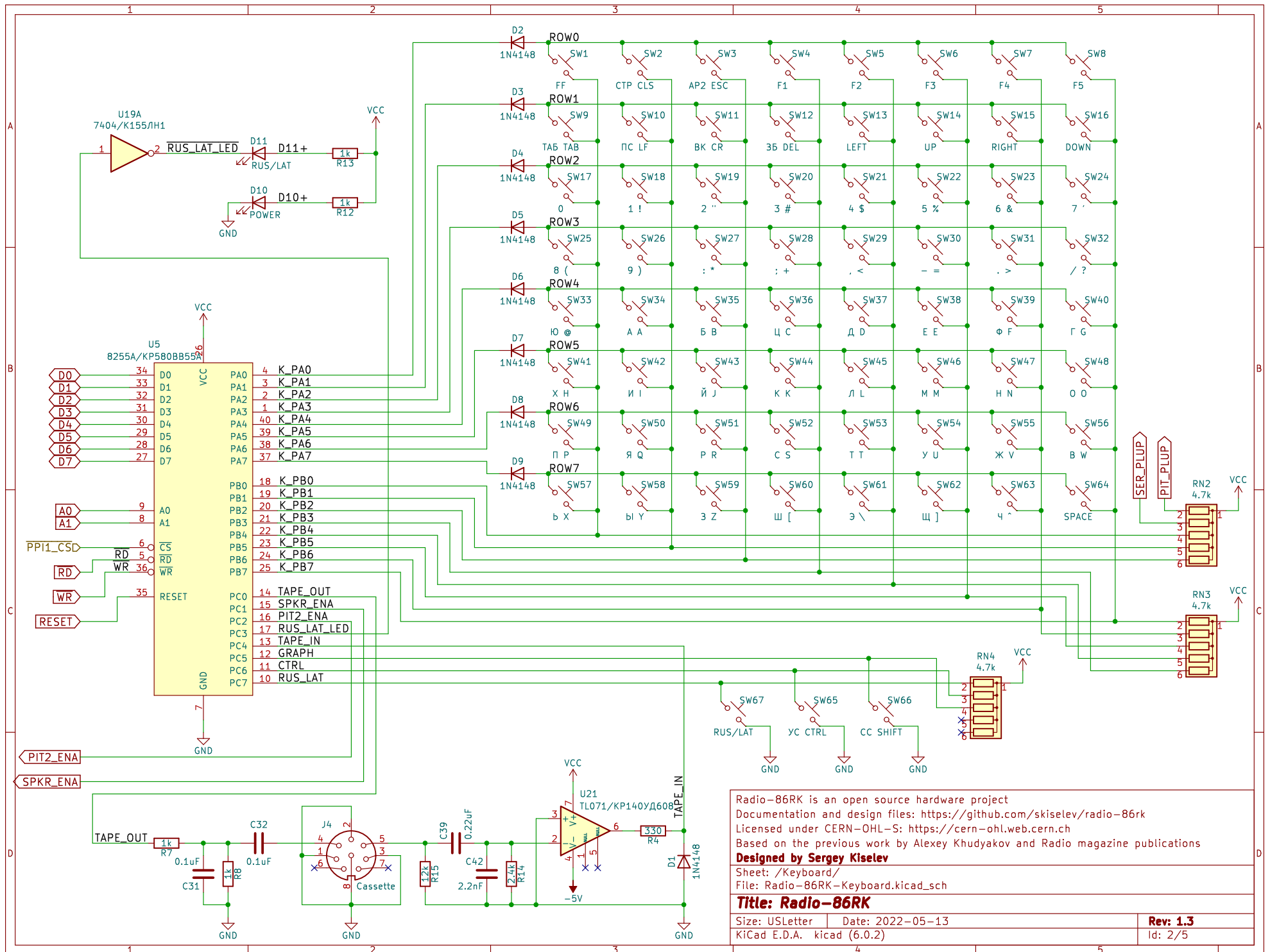
Address Decode



DMA Controller



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 Based on the previous work by Alexey Khudyakov and Radio magazine publications
Designed by Sergey Kiselev
 Sheet: /
 File: Radio-86RK.kicad_sch
Title: Radio-86RK
 Size: USLetter Date: 2022-05-13
 KiCad E.D.A. kicad (6.0.2)
 Rev: 1.3
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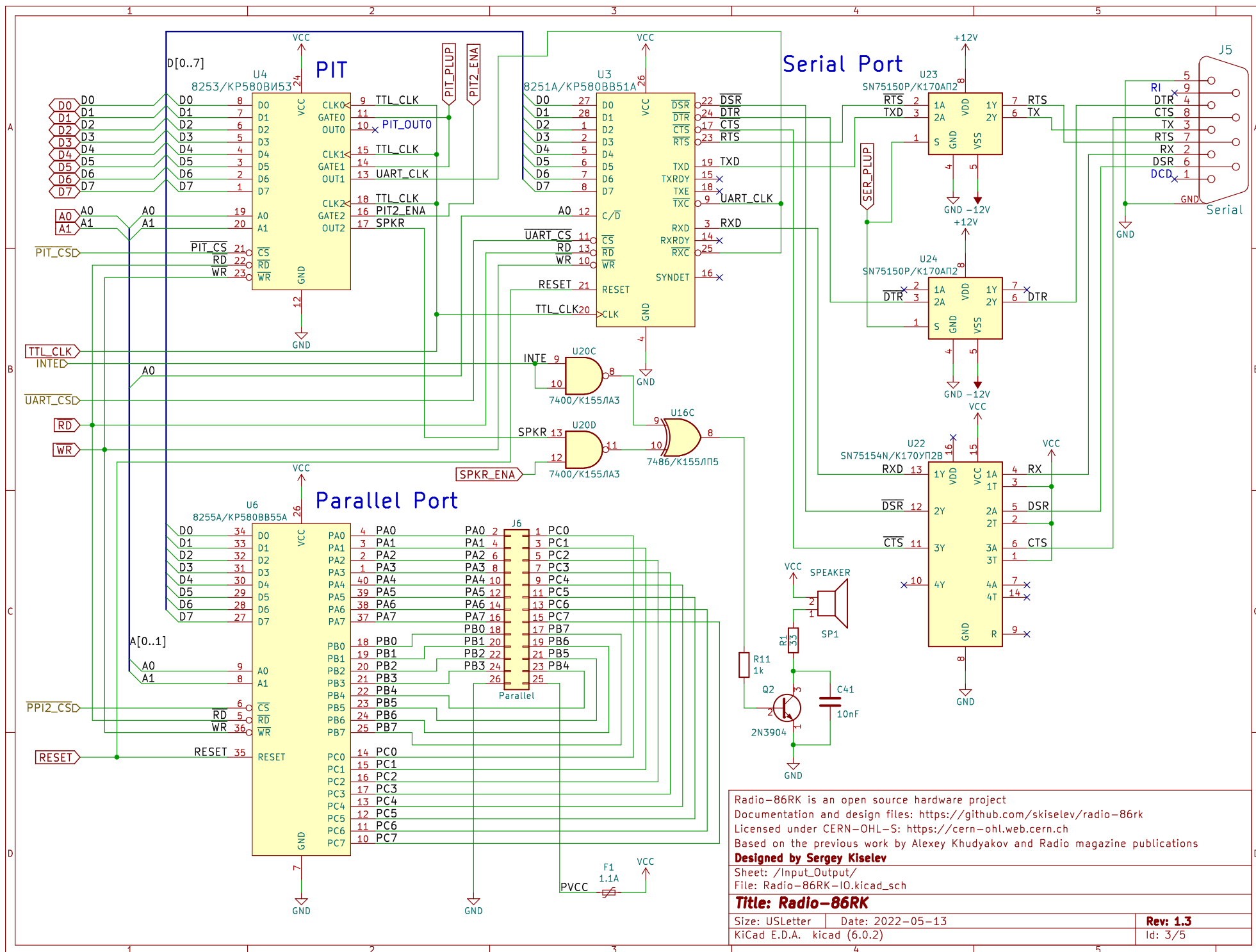
Designed by Sergey Kiselev

Sheet: /Keyboard/
 File: Radio-86RK-Keyboard.kicad_sch

Title: Radio-86RK

Size: USLetter Date: 2022-05-13
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Rev: 1.3
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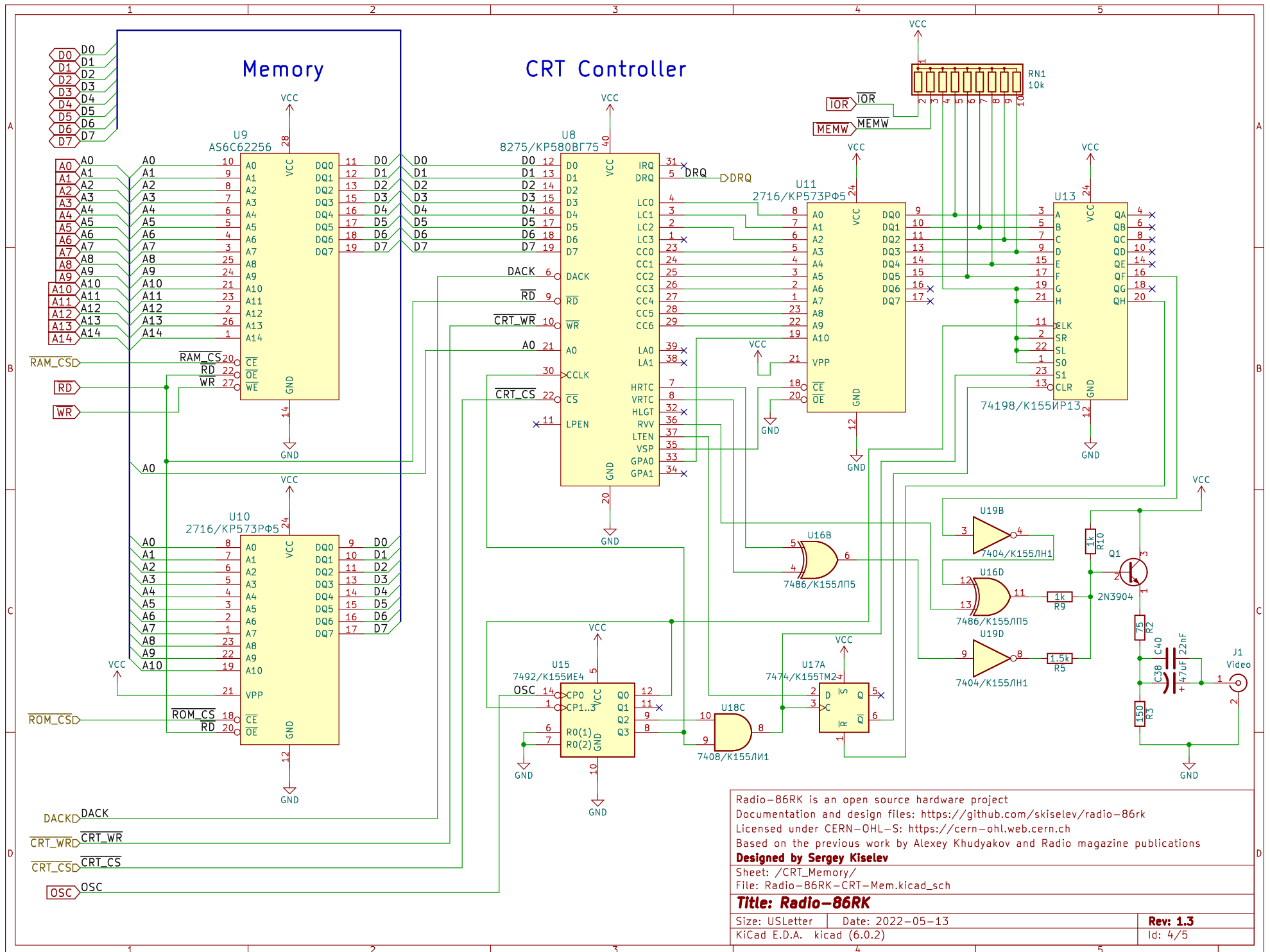
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Size: USLetter Date: 2022-05-13

KiCad E.D.A. kicad (6.0.2)

Rev: 1.3

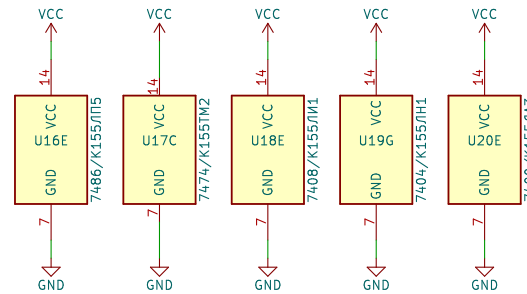
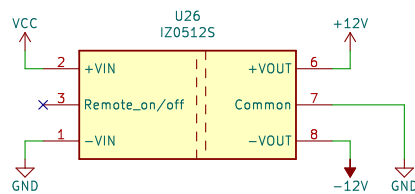
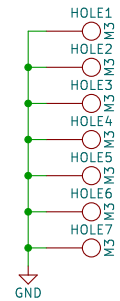
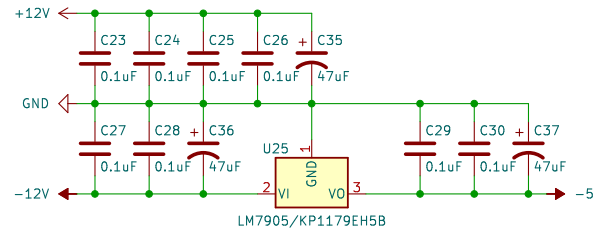
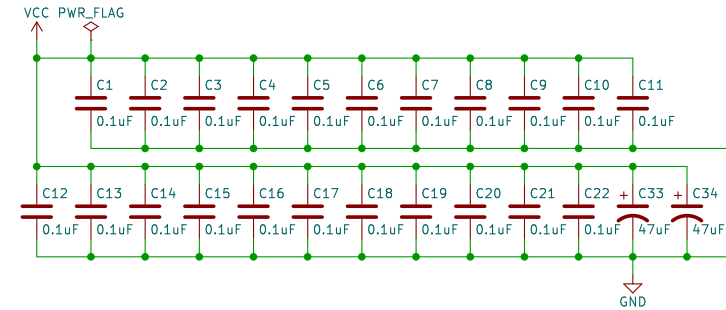
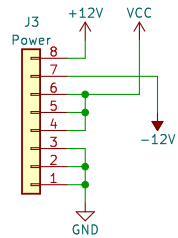
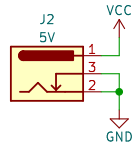
Id: 3/5



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 Sheet: /CRT_Memory/
 File: Radio-86RK-CRT-Mem.kicad_sch

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| Title: Radio-86RK | |
| Size: USLetter | Date: 2022-05-13 |
| KiCad E.D.A. kicad (6.0.2) | Rev: 1.3 |
| | Id: 4/5 |

Power Supply



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Title: Radio-86RK

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Rev: 1.3
Id: 5/5