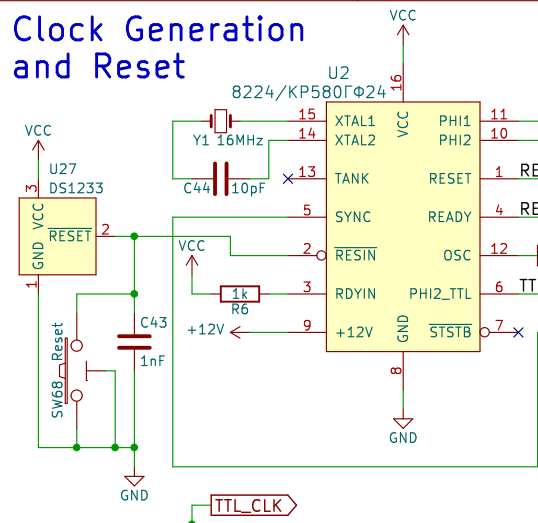
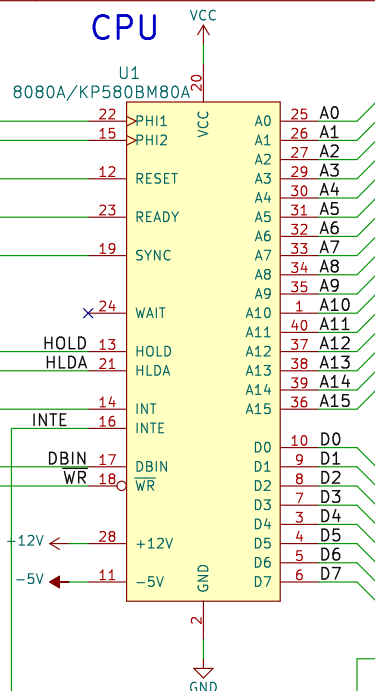


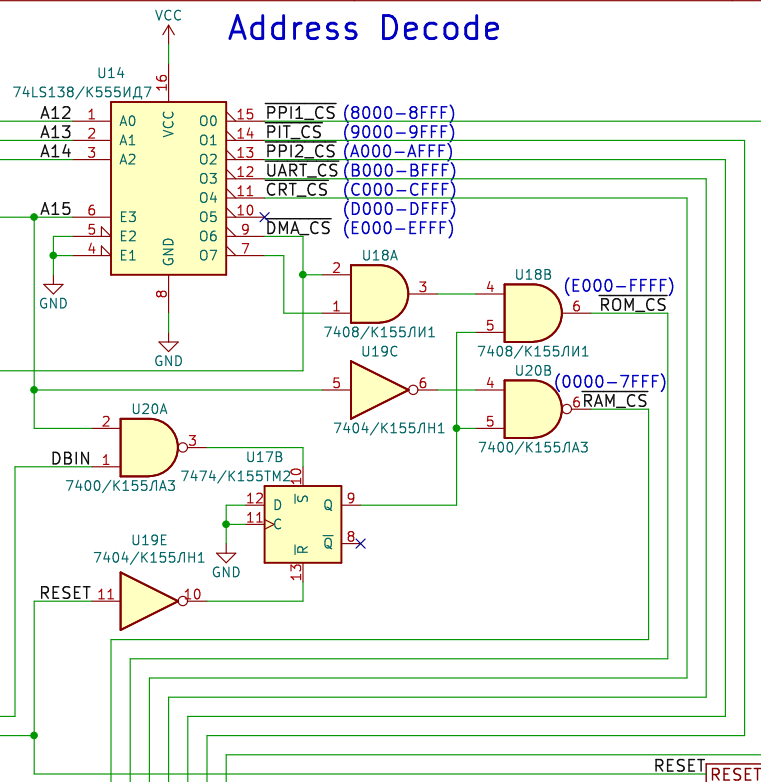
Clock Generation and Reset



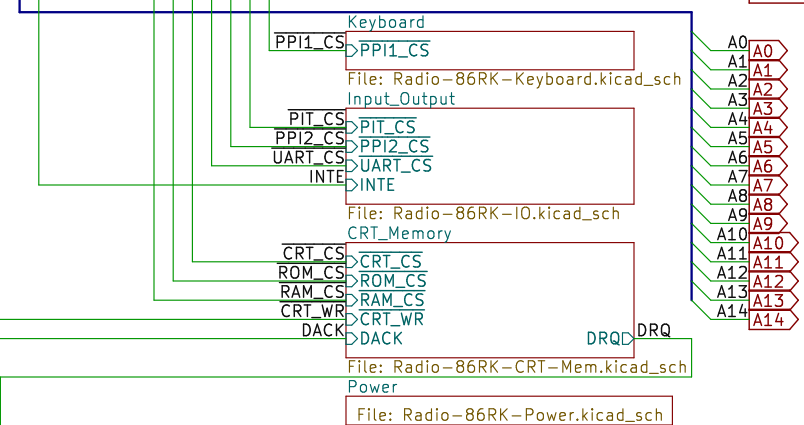
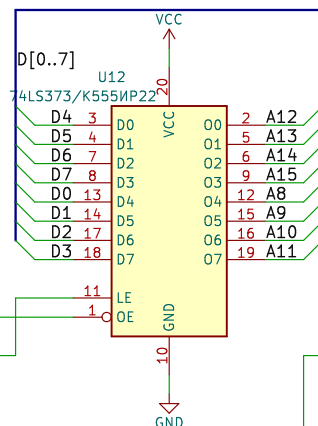
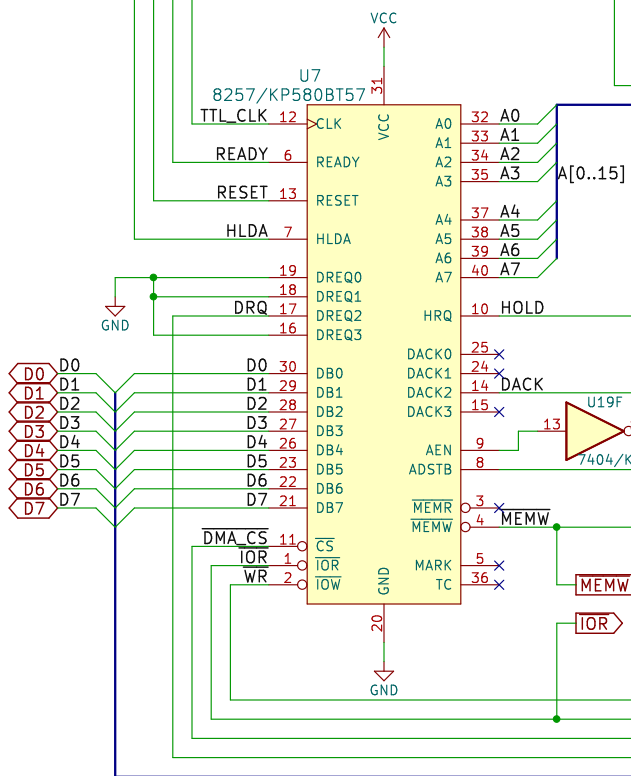
CPU



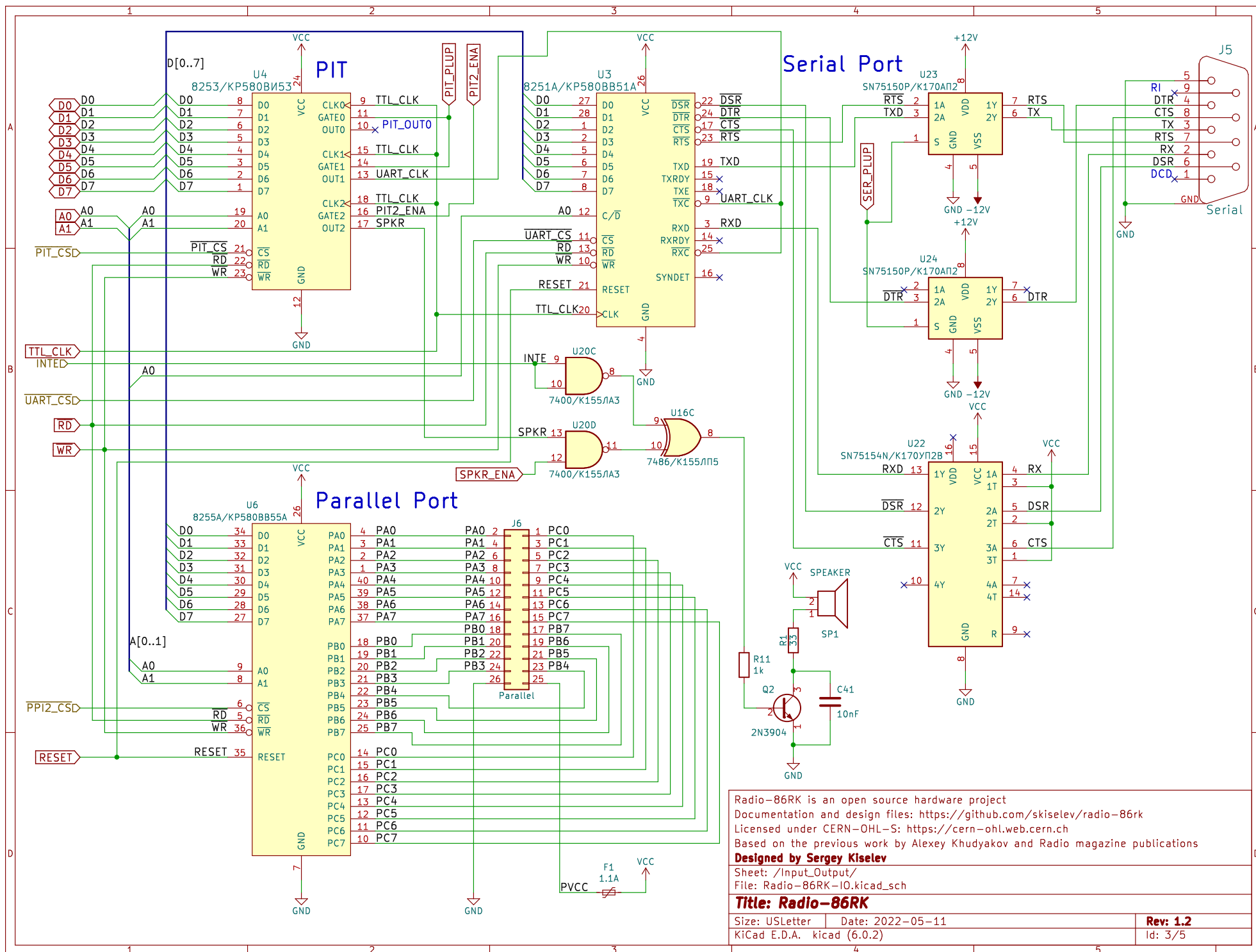
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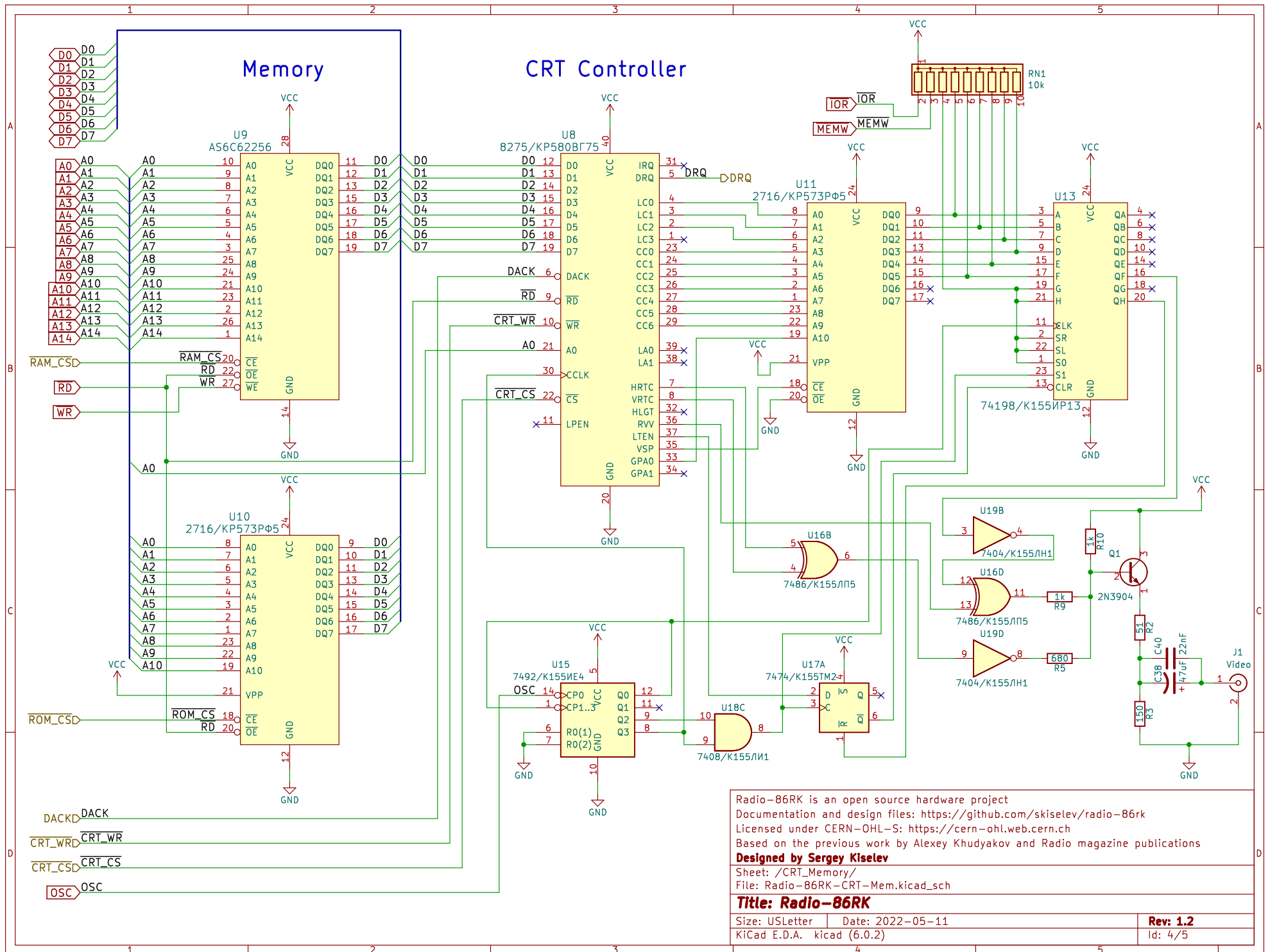


DMA Controller



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 Based on the previous work by Alexey Khudyakov and Radio magazine publications
Designed by Sergey Kiselev
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Title: Radio-86RK
 Size: USLetter Date: 2022-05-11 Rev: 1.2
 KiCad E.D.A. kicad (6.0.2) Id: 1/5





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Designed by Sergey Kiselev

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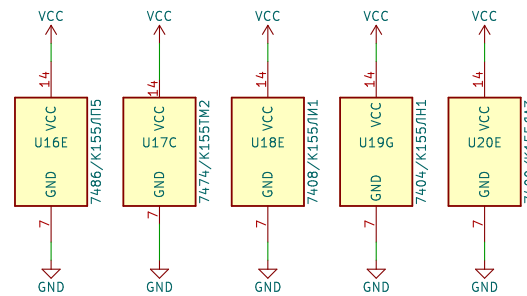
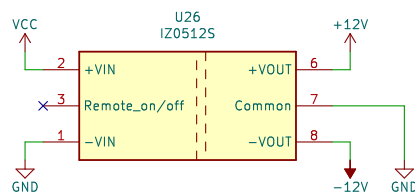
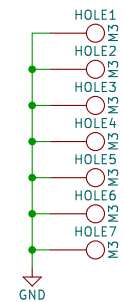
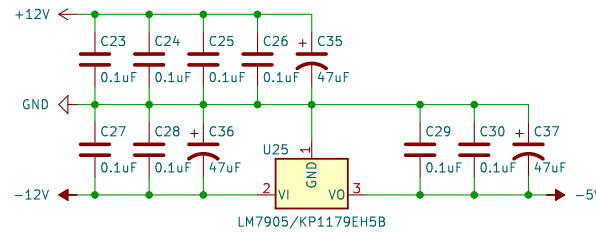
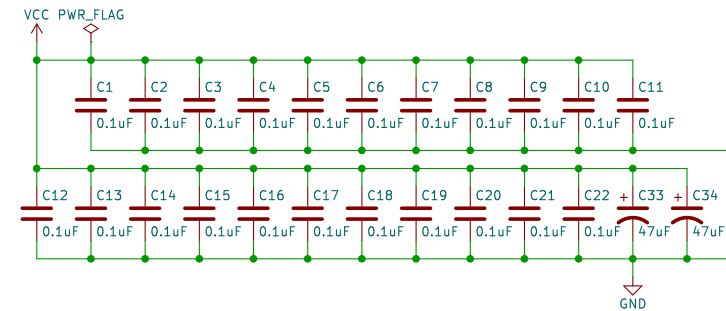
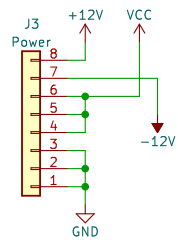
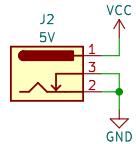
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Power Supply



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Title: Radio-86RK

Size: A4 Date: 2022-05-11
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Rev: 1.2
Id: 5/5