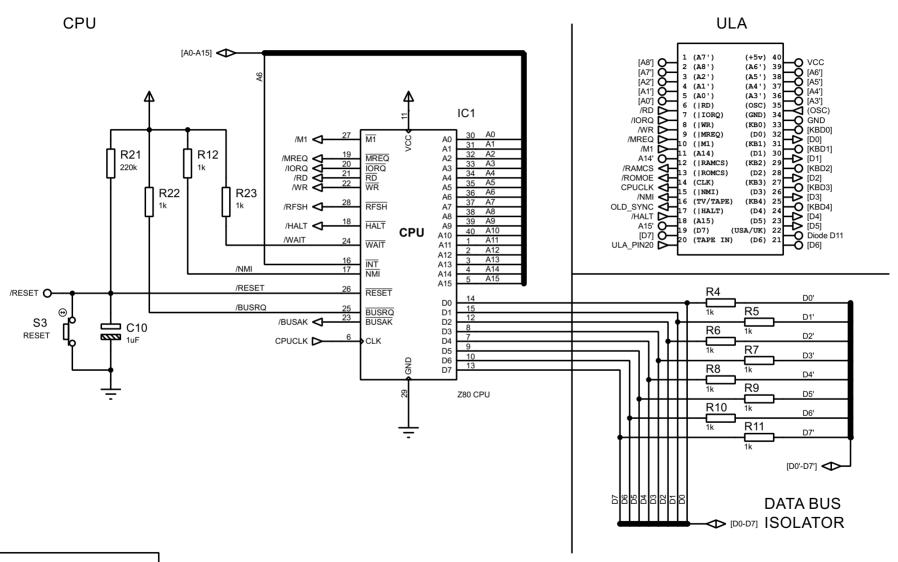
ZX80/81 Project

by Augusto Baffa, Phd

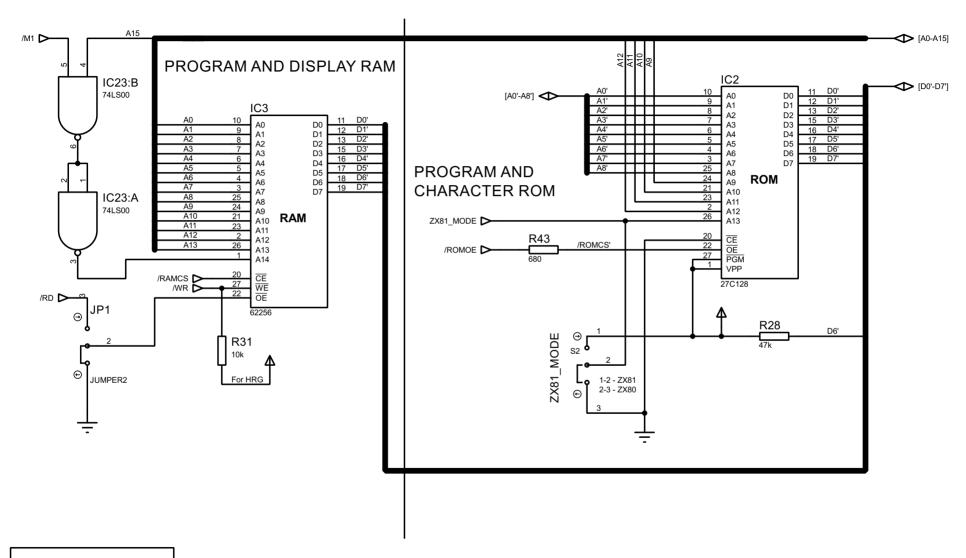
Assembled and Tested - Feb/2021

http://www.augustobaffa.pro.br/



ZX81 Project - Base

Augusto Baffa © Feb/2021



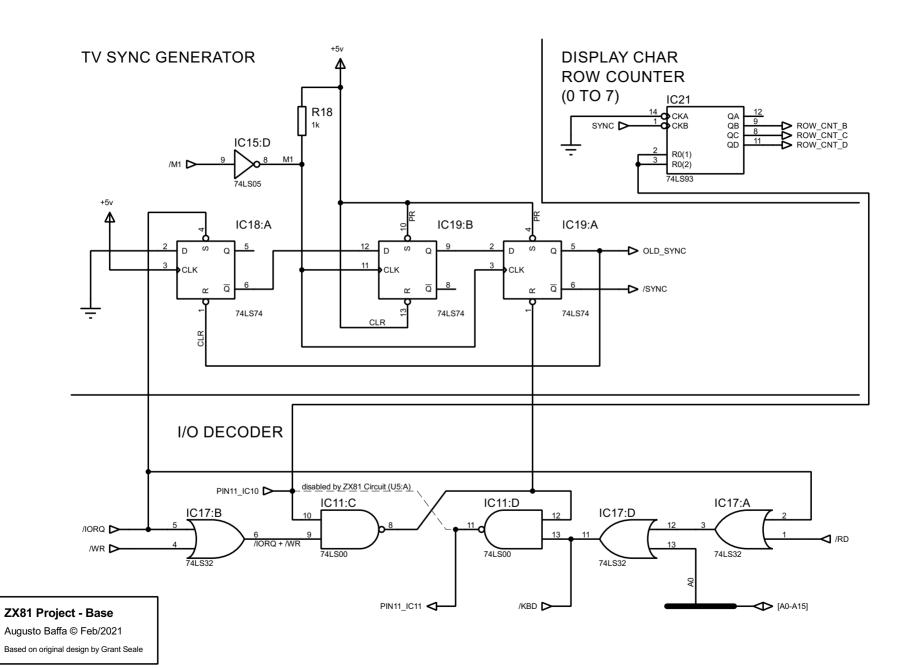
ZX81 Project - Base

Augusto Baffa © Feb/2021

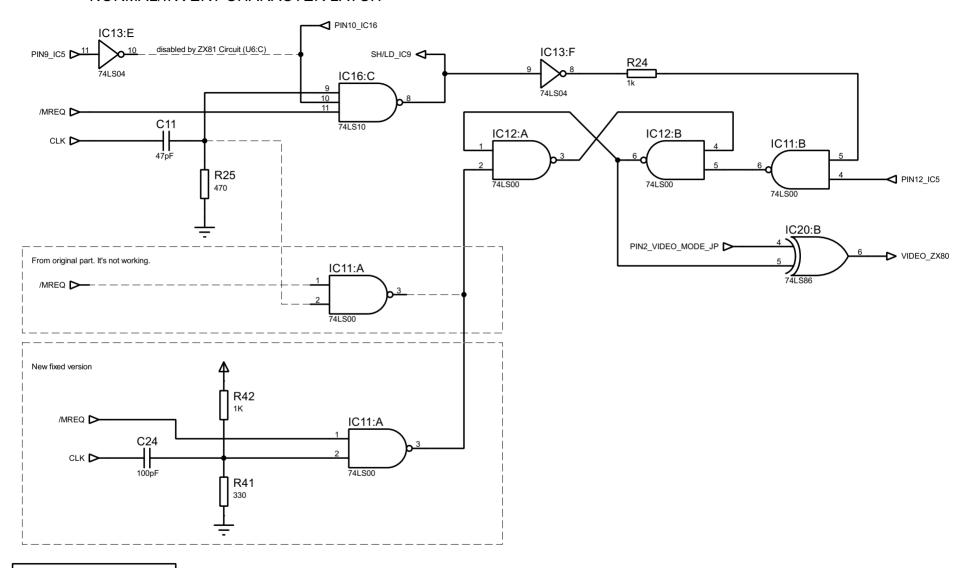
CLOCK GENERATOR +5v IC18:B +5v CRYSTAL 6.5536 MHz R20 330 IC15:E IC20:C IC20:D α -> CPUCLK 74LS05 74LS74 3.25 MHz 74LS86 IC15:F 74LS86 IC20:A 74LS05 C15 220pF 74LS86 → 2xCLK -> /CLK 6.5 MHz

ZX81 Project - Base

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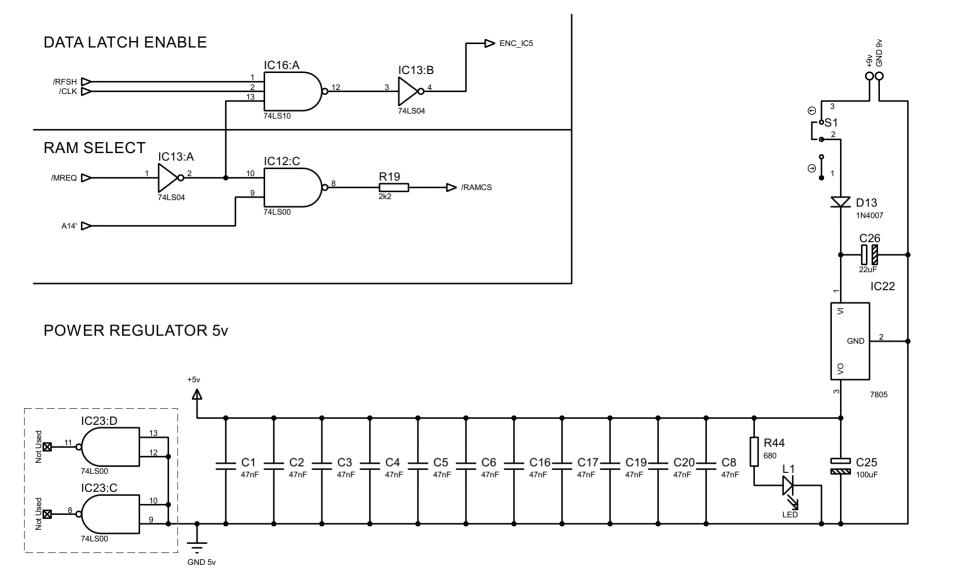


NORMAL/INVERT CHARACTER LATCH



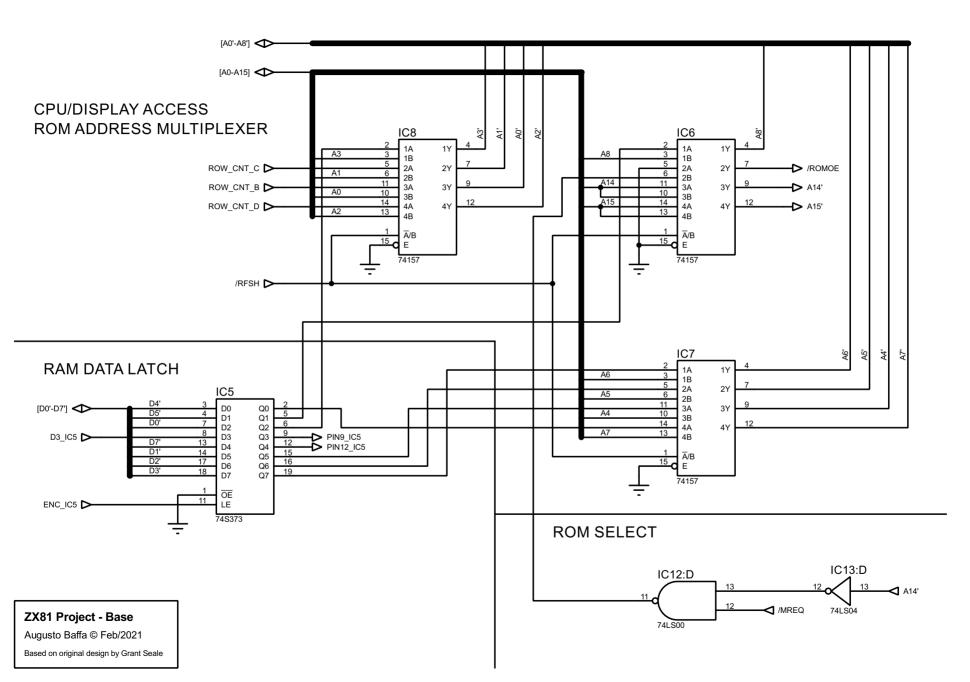
ZX81 Project - Base

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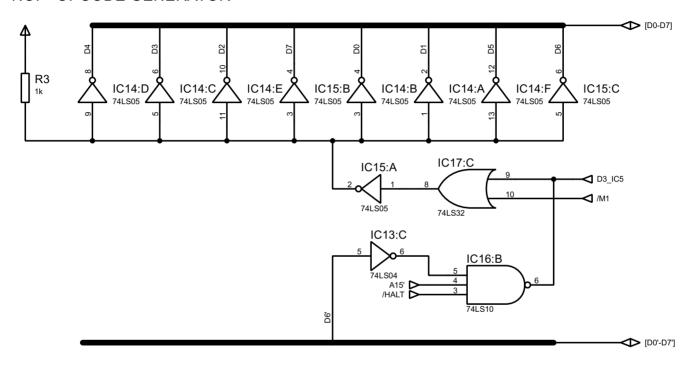


ZX81 Project - Base

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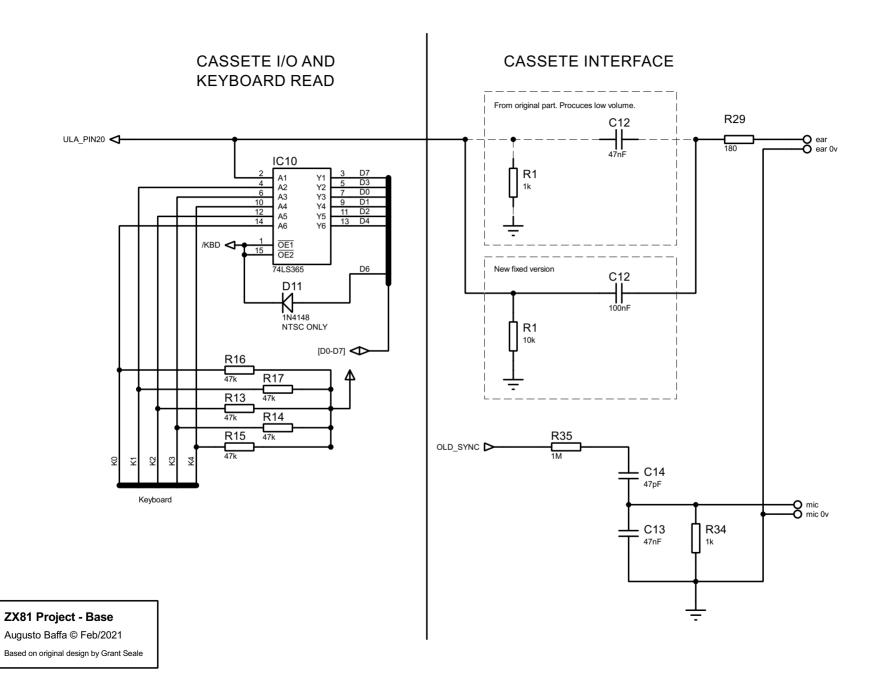


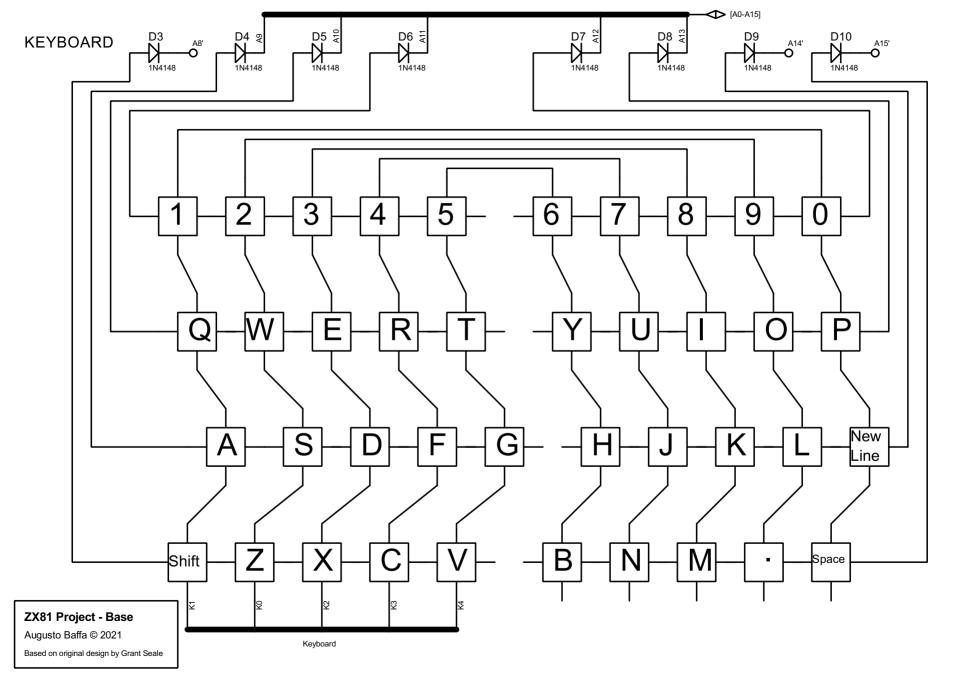
"NOP" OPCODE GENERATOR

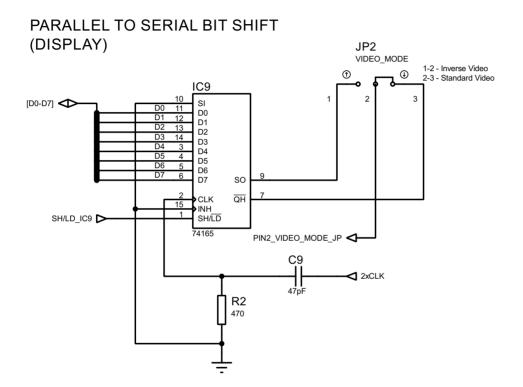


ZX81 Project - Base

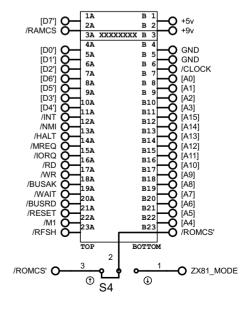
Augusto Baffa © Feb/2021





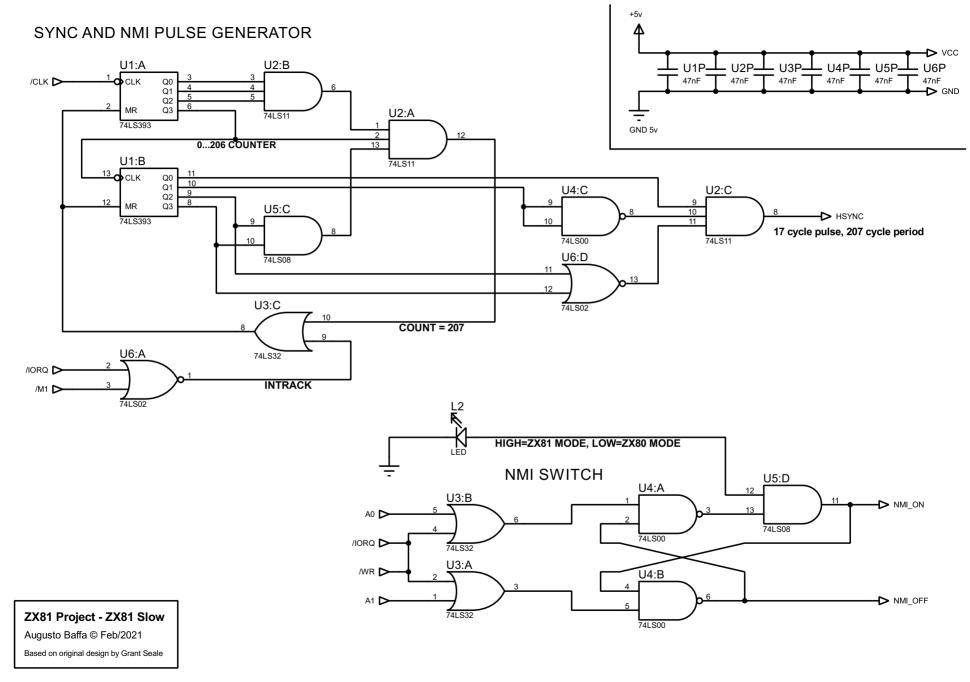


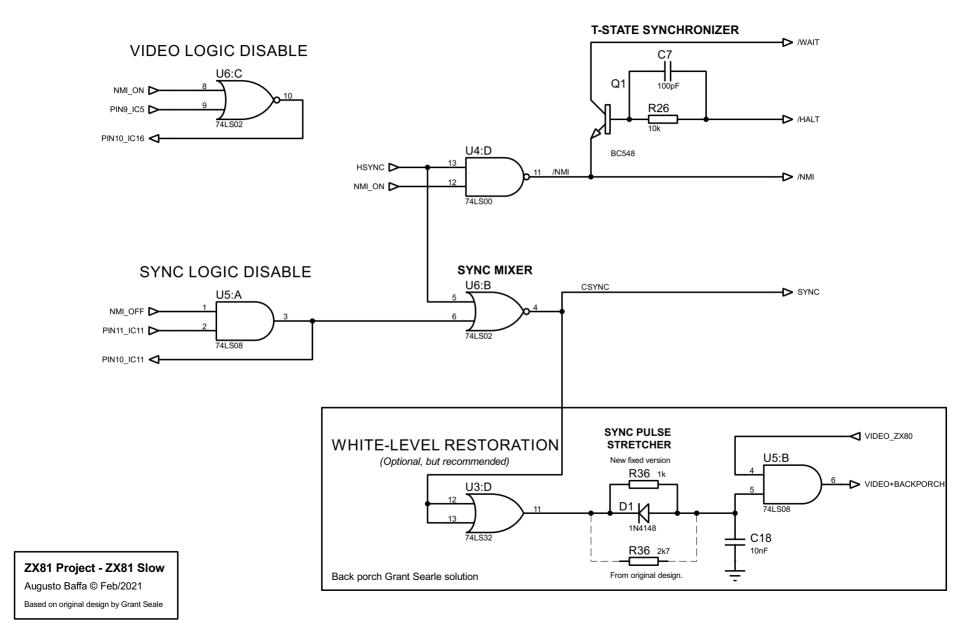
EDGE CONECTOR

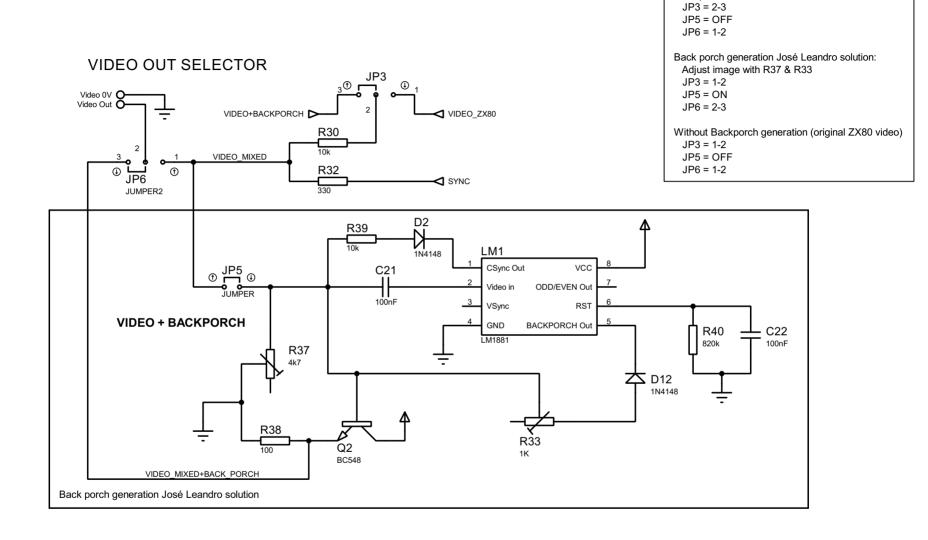


ZX81 Project - Base

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Back porch Grant Searle solution:

ZX81 Project - Video BP

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Based on original design by José Leandro