

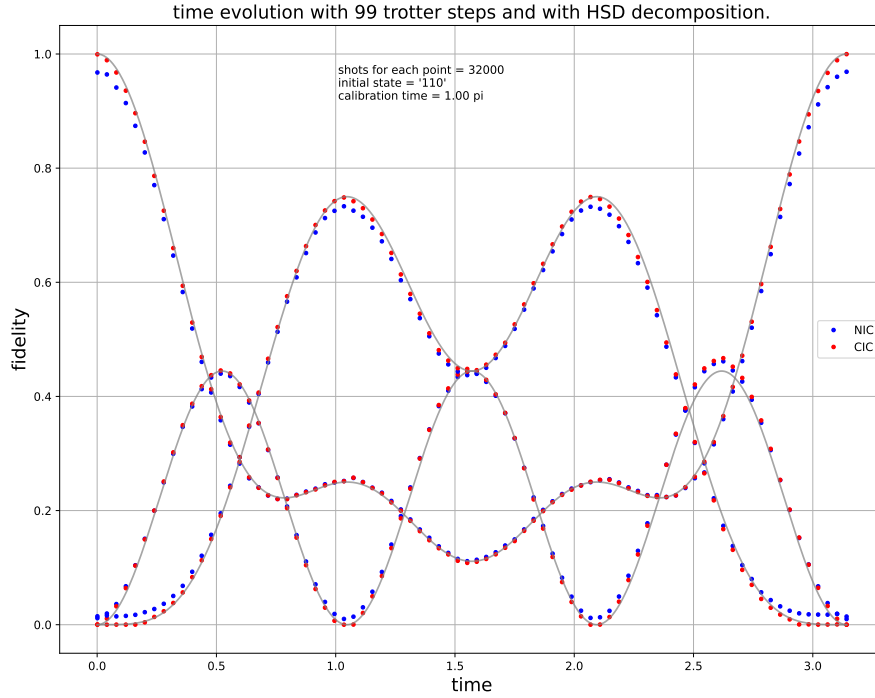
# Time Evolution plots

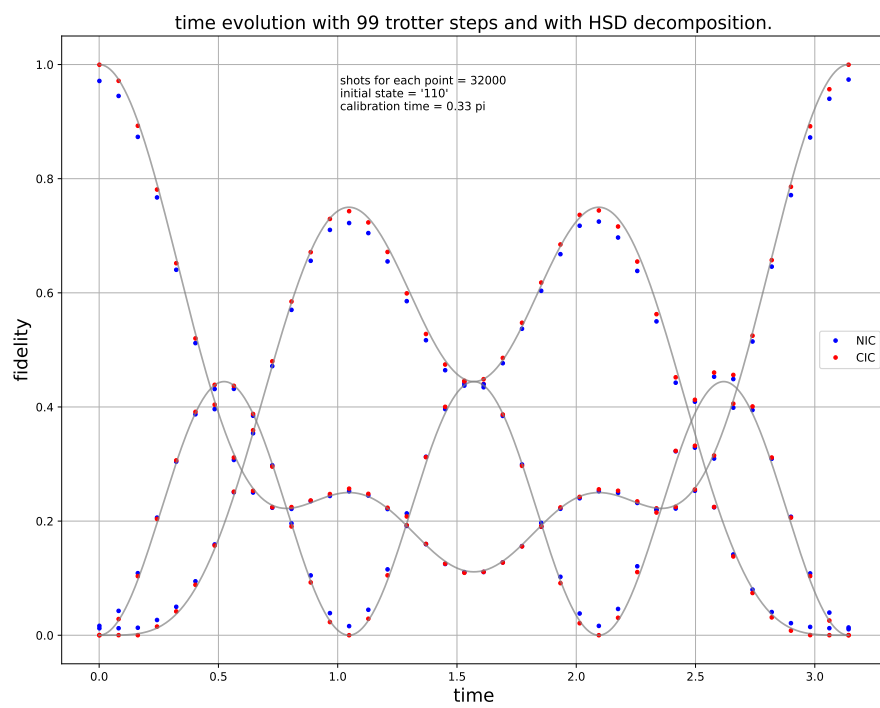
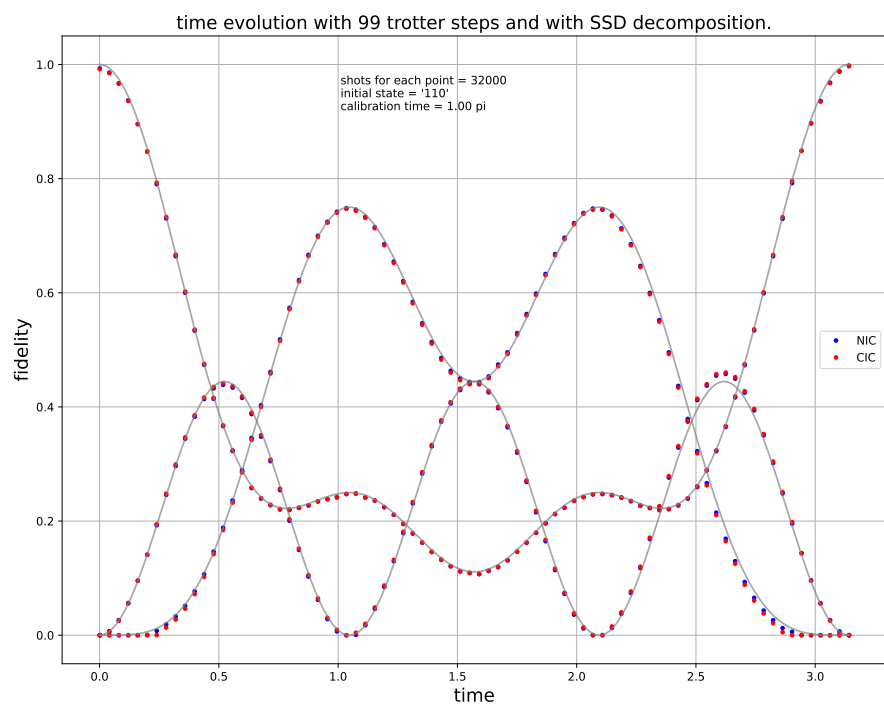
Amico Valerio, Bensch Fabio, Pariente Ruben

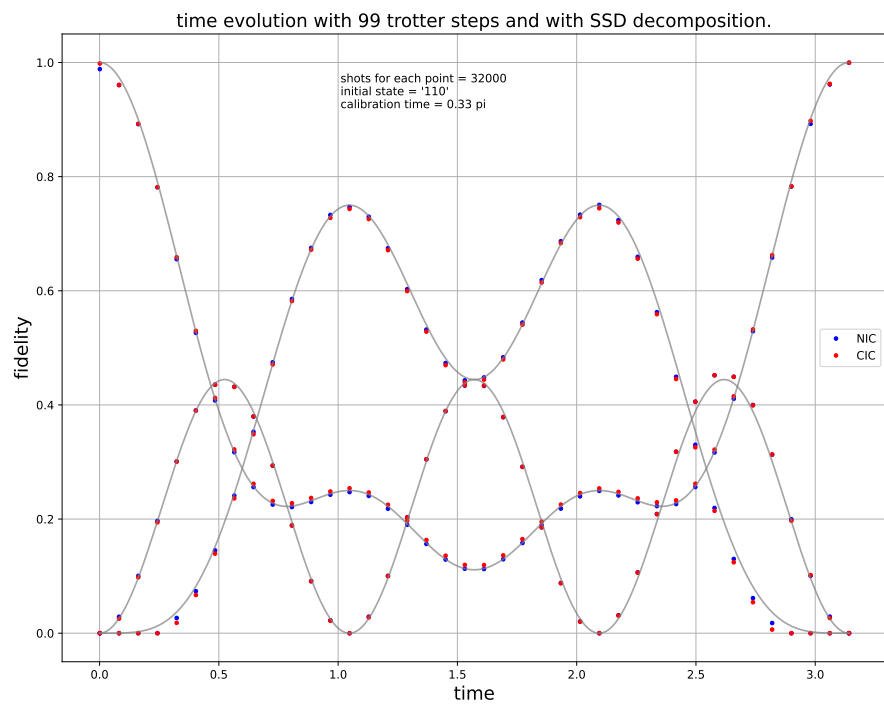
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## 1 Time Evolution HSD and SSD

Here is plotted the time evolution of all the target states ('011', '101' and '110') obtained initializing the system in '110'. All simulations were executed on noisy jackarta simulator backend. We built the calibration matrices for both SSD and HSD using the evolution circuit at the time  $\pi$  and  $\pi/3$ . Fidelity scores are plotted for both NIC and CIC mitigation techniques.







## 2 Comparison between HSD and SSD

Next, in a semilogarithmic plot is shown the absolute value of the distances from the theoretical line, with both NIC and CIC. The target state is '110'. Here the calibration time is fixed to  $\pi$ .

