

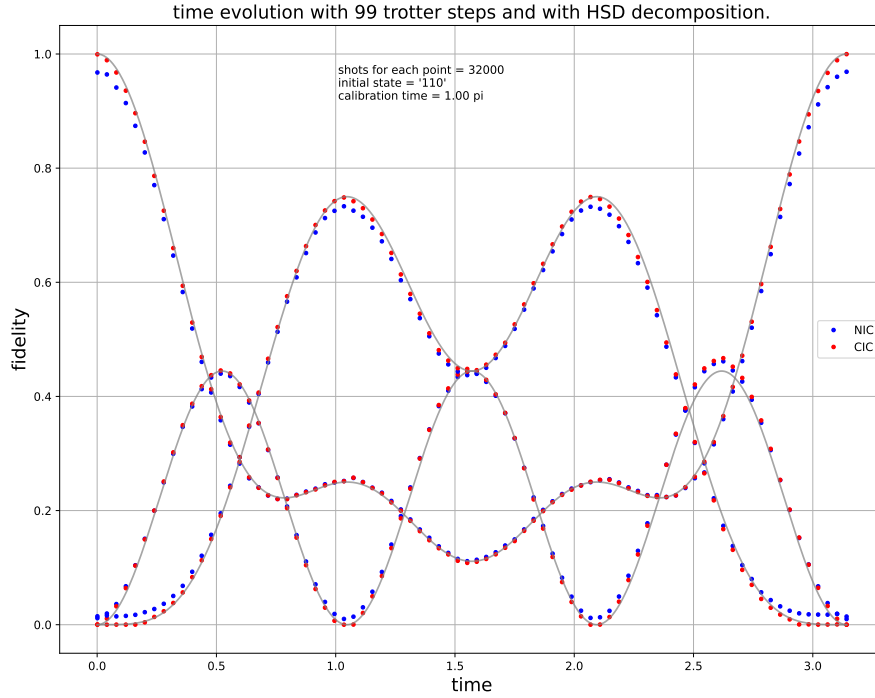
# Time Evolution plots

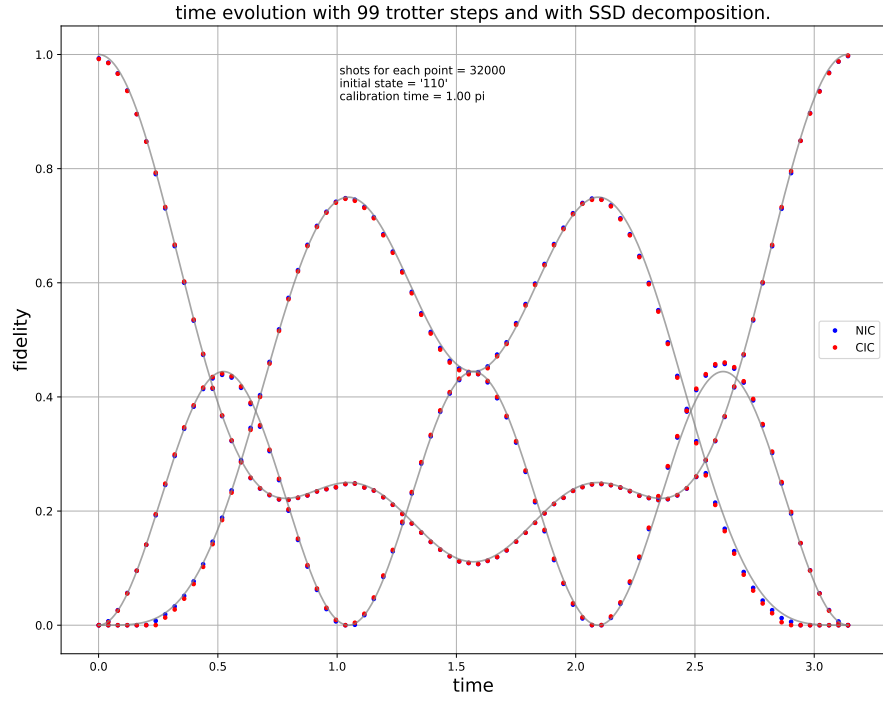
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## 1 Time Evolution HSD and SSD

Here is plotted the time evolution of all the target states ('011', '101' and '110') obtained initializing the system in '110'. All simulations were executed on noisy jackarta simulator backend We built the calibration matrices for both SSD and HSD using the evolution circuit in the time  $\pi$ . Are plotted the fidelity scores for both NIC and CIC mitigation technique.





## 2 Comparison between HSD and SSD

Here there are plots of fidelity for the target state '110', and the absolute distance from the theoretical line vs. time. With both NIC and CIC.

