**Lab3:**

Q1.1:

L1, L2 latency: 20  
RAM latency: 186

Q1.2:

Shared memory size

Q2.1:

It's not working, because the part 2 code is not working per every offset. So the permitted bytes of the flag is only the first 4, and the rest of the flag is not reachable without speculative approach.

Q2.3:

The attacker can achieve such leakage by accessing memory areas that in the kernel area. Using a speculation condition, the attacker can access the kernel restricted memory without any error.  
  
Q2.4:  
As we tried,10 conditions are enough for the training  
  
Q3.2:  
We want to make the speculative window bigger as possible, so we need the condition of part3\_limit to be resolved as late as possible. So we trying to flush all the cache to DRAM, so the access to part3\_limit variable will be very slow, and so we'll be able to make the speculative approach in this time.

Q3.3:

We need the cache properties of the machine.