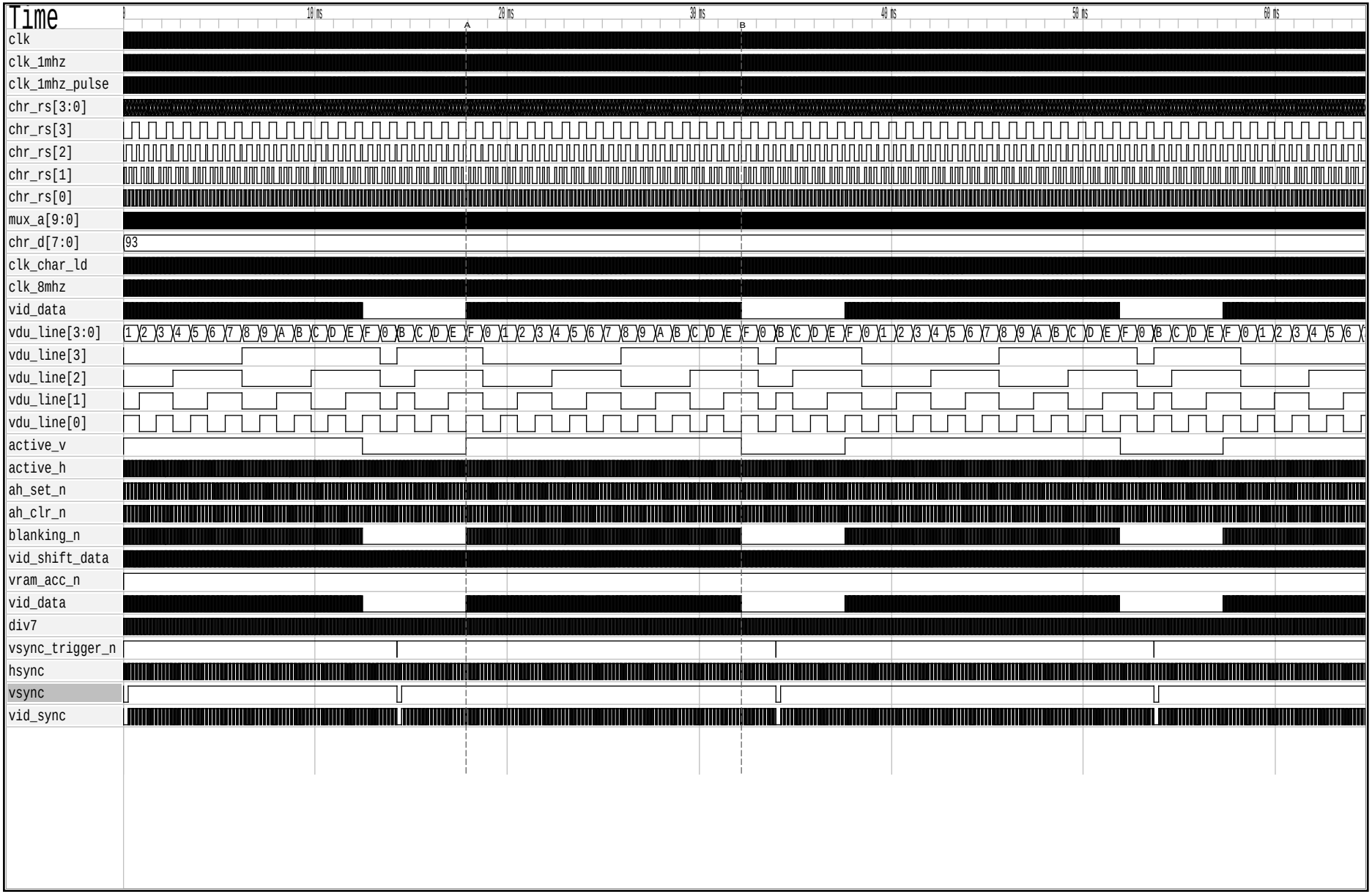
Time
0
10 ns
20 ns
30 ns
40 ns
50 ns
60 ns
A
B
clk
clk_1mhz
clk_1mhz_pulse
chr_rs[3:0]
chr_rs[3]
chr_rs[2]
chr_rs[1]
chr_rs[0]
mux_a[9:0]
chr_d[7:0]
93
clk_char_ld
clk_8mhz
vid_data
vdu_line[3:0]
vdu_line[3]
vdu_line[2]
vdu_line[1]
vdu_line[0]
active_v
active_h
ah_set_n
ah_clr_n
blanking_n
vid_shift_data
vram_acc_n
vid_data
div7
vsync_trigger_n
hsync
vsync
vid_sync