

IDE ASSIGNMENT

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IITH - Future Wireless Communications (FWC)

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1 QUESTION

verify the correct operation of a divide-by-5 counter implemented using a 7474 IC, where the binary count is displayed using seven segment display.

2 COMPONENTS

Component	Values	Quantity
Arduino	UNO	1
JumperWires	M-F	30
seven segment	common Anode	1
Breadboard		1
Resistors	220ohms	1
IC	7474	2
IC	7447	1

Figure.Components

3 CIRCUIT DIAGRAM

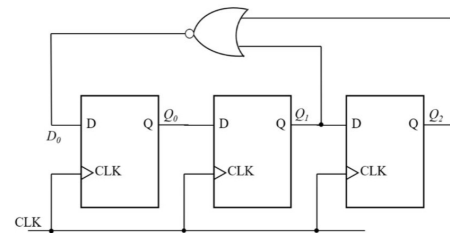


Fig. 1: Divide by 5 counter

4 TRUTH TABLE

Present state			Flip-Flop input			Next State		
Q2	Q1	Q0	D2	D1	D0	Q2'	Q1'	Q0'
0	0	0	0	0	1	0	0	1
0	0	1	0	1	1	0	1	1
0	1	1	1	1	0	1	1	0
1	1	0	1	0	0	1	0	0
1	0	0	0	0	1	0	0	1

Truth table

Fig. 2: Truth table

4.1 BOOLEAN EQUATION

From the circuit diagram we can conclude:

$$D0 = \overline{Q1 + Q2} \quad (1)$$

$$D1 = Q0 \quad (2)$$

$$D2 = Q1 \quad (3)$$

5 HARDWARE

- 1) Make the connections between the seven segment display and the 7447 IC as shown below.

7447	\bar{a}	\bar{b}	\bar{c}	\bar{d}	\bar{e}	\bar{f}	\bar{g}
Display	a	b	c	d	e	f	g

Fig. 3: seven segment connections

- 2) connect the Arduino, 7447 and the two 7474 ICs according to below table.

	INPUT			OUTPUT			CLOCK		5V			
Arduino	Q0	Q1	Q2	Q0'	Q1'	Q2'	D13					
7474	D6	D7	D8	D2	D3	D4	CLK1	CLK2	1	4	10	13
7474	5	9	5	2	12	2	CLK1	CLK2	1	4	10	13
7447				7	1	2						16

Fig. 4: Connections

6 SEVEN SEGMENT OUTPUT

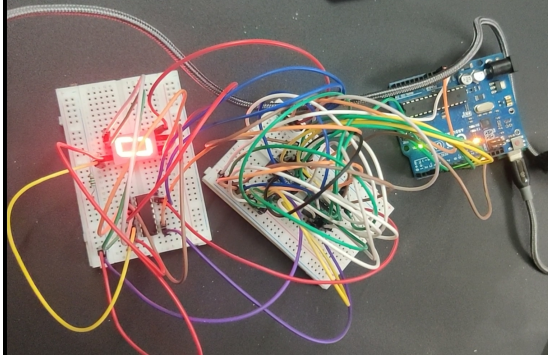


Fig. 5: Connections

7 CONCLUSION

Hence we have implemented the divide by 5 counter digital circuit. Execute the circuit using below code.

https://github.com/Vamsichowdary04/CBSE_vector_12_and_prob_12/blob/main/ide_idepfoo.cpp