



Institute of Technology of Cambodia



Telecommunication and Network

Computer Architecture

TP 03

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Score

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Exercises

1. Use identifies of Boolean algebra to simply the following logic expression:

a) $f = \overline{a}bc + \overline{a}\overline{b}c$

b) $f = \overline{b}(ac + \overline{a} \cdot \overline{c})$

c) $f = ab\overline{c} + a\overline{b}\overline{c} + \overline{a}b\overline{c}$

d) $f = (\overline{a} + \overline{b}) + (\overline{a} + b)$

e) $f = (a + \overline{b} + a\overline{b}) \overline{c}$

f) $f = a\overline{b}\overline{c} + a\overline{b}\overline{c}\overline{d} + a\overline{c}$

2. After simplify please draw the circuit from above logic expression

3. Find the logic function from table below karnaugh table and draw circuit:

$F1(a,b,c,d) = ?$

$cd \backslash ab$	00	01	11	10
00	1	1	1	1
01	0	0	0	0
11	0	0	0	0
10	1	1	1	1

$F2(a,b,c,d) = ?$

$cd \backslash ab$	00	01	11	10
00	1	0	0	1
01	0	0	0	1
11	1	1	0	0
10	1	1	0	1

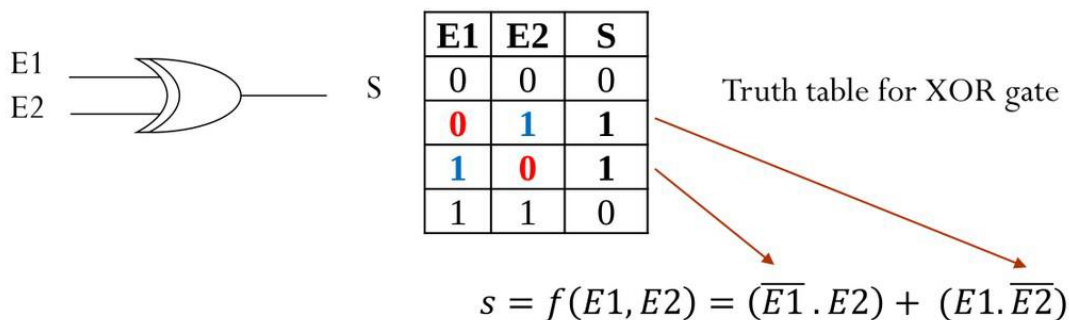
$F3(a,b,c,d) = ?$

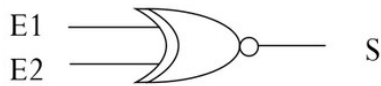
$cd \backslash ab$	00	01	11	10
00	0	0	1	1
01	0	1	1	0
11	1	0	0	1
10	0	1	0	1

$F4(a,b,c,d) = ?$

$bd \backslash a$	0	1
00	1	1
01	0	1
11	1	1
10	1	1

4. build XOR and XNOR gates using AND, OR, NOT gate.





E1	E2	S
0	0	1
0	1	0
1	0	0
1	1	1

Truth table for XNOR gate

$$s = f(E1, E2) = ?$$

Answer

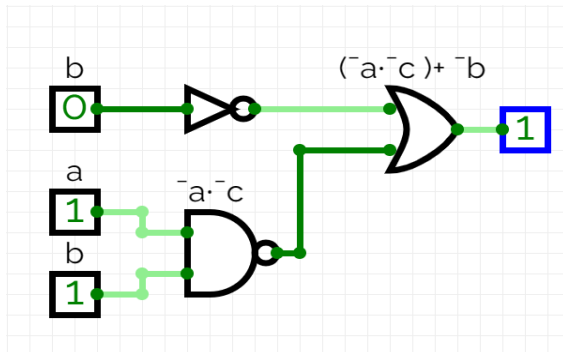
1. Use identifies of Boolean algebra to simply the following logic expression:

- a) $f = \overline{a}bc + \overline{a}bc$
 $= \overline{a}bc + \overline{b} + \overline{ac}$ (De Morgan's law)
 $= \overline{b}(ac + 1) + \overline{ac}$
 $= \overline{b} + \overline{ac}$ (Domination law)
- b) $f = \overline{b}(ac + \overline{a} \cdot \overline{c})$
 $= \overline{b}ac + \overline{b} \cdot \overline{a} \cdot \overline{c}$ (Absorption law)
 $= \overline{b}ac + \overline{(b + a + c)}$ (De Morgan's law)
- c) $f = ab\overline{c} + a\overline{b}\overline{c} + \overline{a}b\overline{c}$
 $= \overline{c}(ab + a\overline{b} + \overline{a}b)$
 $= \overline{c}(a(b + \overline{b}) + \overline{a}b)$
 $= \overline{c}(a + \overline{a}b)$ (Unit property)
 $= \overline{c}a + \overline{a}b\overline{c}$ (Absorption law)
- d) $f = (\overline{a} + \overline{b}) + (\overline{a} + b)$
 $= (\overline{a}b) + (\overline{a} + b)$ (De Morgan's law)
- e) $f = (a + \overline{b} + a\overline{b}) \overline{c}$
 $= \overline{c}(a(\overline{b} + 1) + \overline{b})$
 $= \overline{c}(a + \overline{b})$ (Domination law)
 $= \overline{c}a + \overline{c}\overline{b}$ (Distributive law)
 $= \overline{c}a + \overline{(c + d)}$ (De Morgan's law)
- f) $f = a\overline{b} \overline{c} + a\overline{b} \overline{c} \overline{d} + a\overline{c}$
 $= \overline{c}a(\overline{b} + \overline{b} \overline{d} + 1)$
 $= \overline{c}a(\overline{b}(1 + \overline{d}) + 1)$

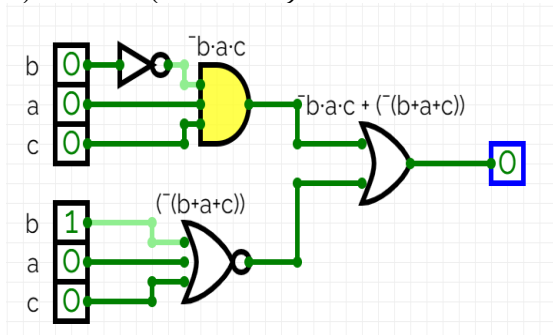
$$= \bar{c}a \text{ (Domination law)}$$

2. After simplify please draw the circuit from above logic expression:

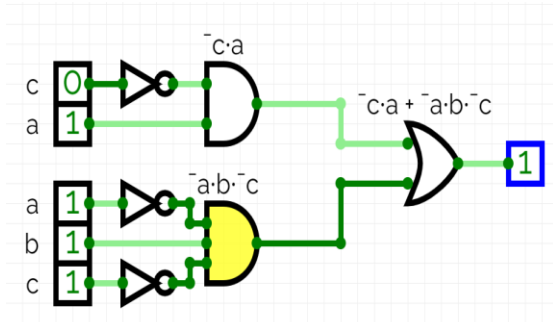
a) $f = \bar{b} + \bar{a}c$



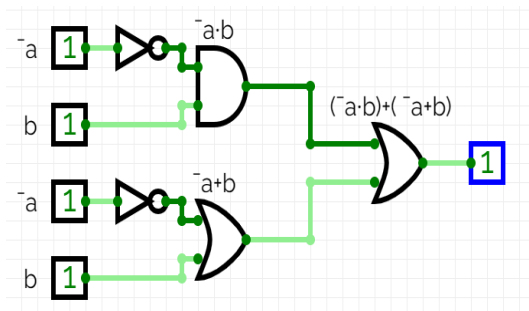
b) $f = \bar{b}ac + \overline{(b+a+c)}$



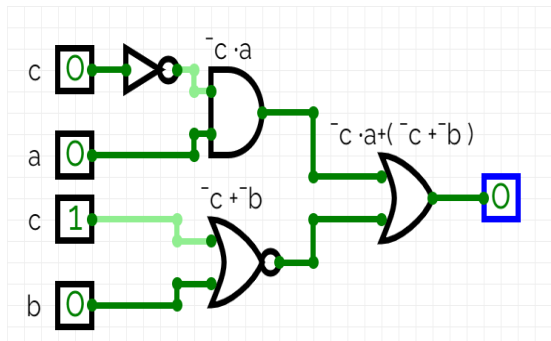
c) $f = \bar{c}a + \bar{a}b\bar{c}$



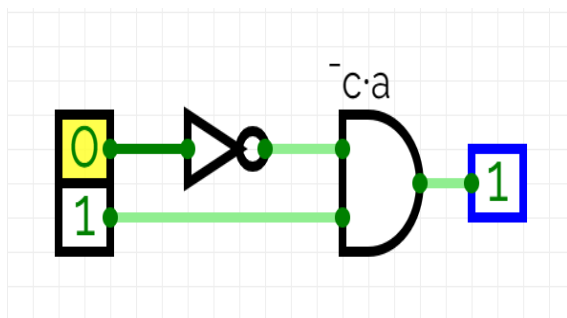
d) $f = (\overline{ab}) + (\bar{a} + b)$



e) $\bar{c}a + (\bar{c} + \bar{d})$



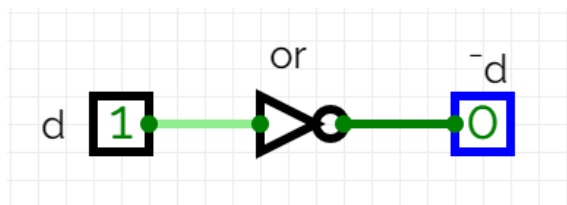
f) $f = \bar{c}a$



3. Find the logic function from table below karnaugh table and draw circuit:

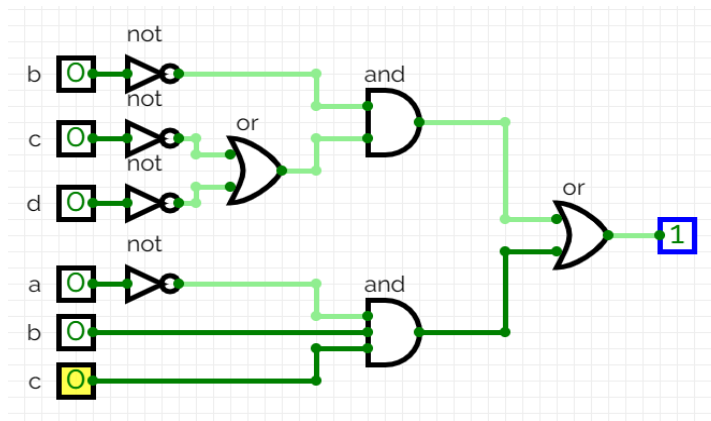
cd \ ab	00	01	11	10
00	1	1	1	1
01	0	0	0	0
11	0	0	0	0
10	1	1	1	1

$$\begin{aligned}
 F(a,b,c,d) &= \bar{b}\bar{c}\bar{d} + b\bar{c}\bar{d} + \bar{b}c\bar{d} + bc\bar{d} \\
 &= \bar{c}\bar{d}(\bar{b}+b) + c\bar{d}(\bar{b}+b) \\
 &= \bar{c}\bar{d} + c\bar{d} \text{ (Unit property)} \\
 &= \bar{d}(\bar{c} + c) \\
 &= \bar{d} \text{ (Unit property)}
 \end{aligned}$$



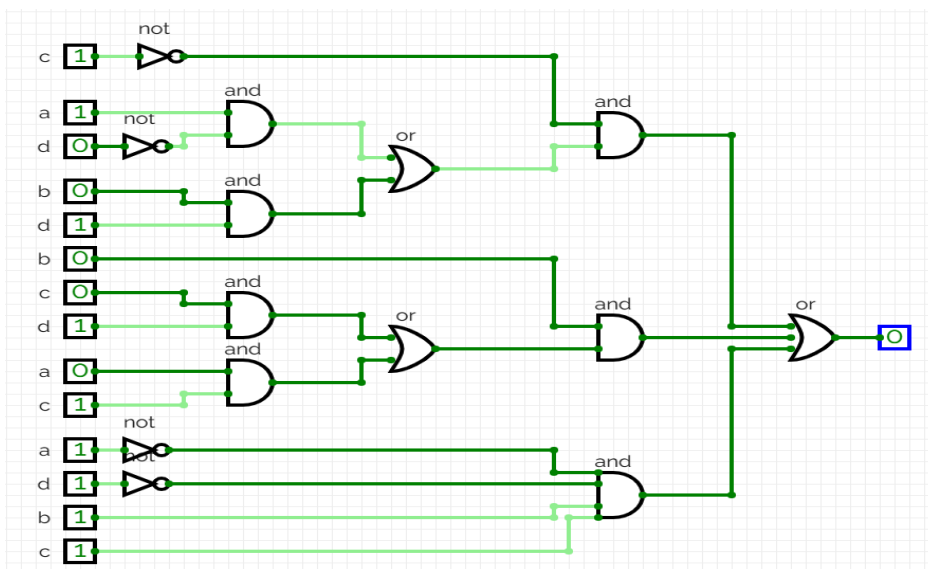
cd \ ab		ab				
		00	01	11	10	
cd	00	1	0	0	1	F2(a,b,c,d) = ?
	01	0	0	0	1	
	11	1	1	0	0	
	10	1	1	0	1	

$$\begin{aligned}
 F2(a,b,c,d) &= \overline{b}\overline{c}\overline{d} + a\overline{b}\overline{c} + \overline{b}c\overline{d} + \overline{a}\overline{b}c + \overline{a}bc \\
 &= \overline{b}\overline{d}(\overline{c}+c) + \overline{b}\overline{c}(\overline{a}+a) + \overline{a}bc \\
 &= \overline{b}\overline{d} + \overline{b}\overline{c} + \overline{a}bc \quad (\text{Unit property}) \\
 &= \overline{b}(\overline{d} + \overline{c}) + \overline{a}bc
 \end{aligned}$$



cd \ ab		ab				
		00	01	11	10	
cd	00	0	0	1	1	F3(a,b,c,d) = ?
	01	0	1	1	0	
	11	1	0	0	1	
	10	0	1	0	1	

$$\begin{aligned}
 f3(a,b,c,d) &= a\overline{c}\overline{d} + b\overline{c}d + \overline{b}cd + a\overline{b}c + \overline{a}bcd \\
 &= \overline{c}(a\overline{d} + bd) + \overline{b}(cd + ac) + \overline{a}d\overline{b}c
 \end{aligned}$$

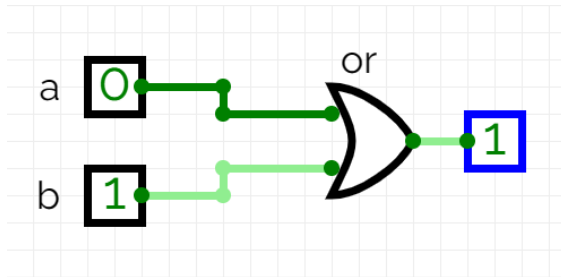


bd \ a	0 1	
	0	1
00	1	1
01	0	1
11	1	1
10	1	1

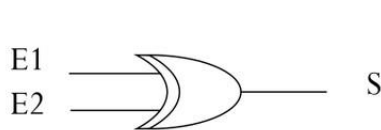
$F4(a,b,c,d) = ?$



$$\begin{aligned}
 F4(a,b,c,d) &= a + \overline{b}a + ba + b\overline{a} \\
 &= a(1 + a) + b(a + \overline{a}) \\
 &= a + b \quad (\text{Domination law}) \quad (\text{Unit property})
 \end{aligned}$$



4. build XOR and XNOR gates using AND, OR, NOT gate.



E1	E2	S
0	0	0
0	1	1
1	0	1
1	1	0

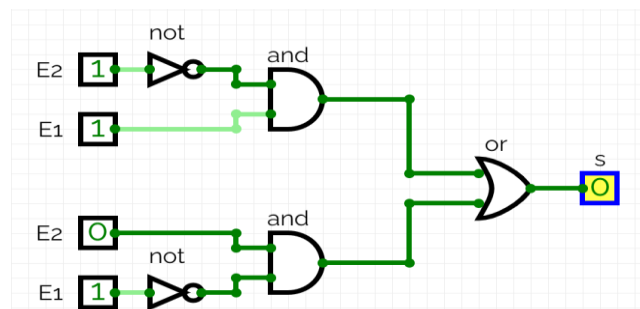
Truth table for XOR gate

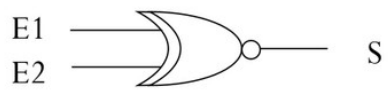
$$s = f(E1, E2) = (\overline{E1} \cdot E2) + (E1 \cdot \overline{E2})$$

E1	E2	s
0	1	1
1	0	1

E2 \ E1	0	1
	0	1
0	0	1
1	1	0

$$S = f(E1, E2) = (E1 \cdot \overline{E2}) + (\overline{E1} \cdot E2)$$





E1	E2	S
0	0	1
0	1	0
1	0	0
1	1	1

Truth table for XNOR gate

$$s = f(E1, E2) = ?$$

E1	E2	s
0	0	1
1	1	1

E1	E2	
	0	1
0	1	0
1	0	1

$$s = f(E1, E2) = \overline{E1} \cdot \overline{E2} + E1 \cdot E2$$

