

A

B

C

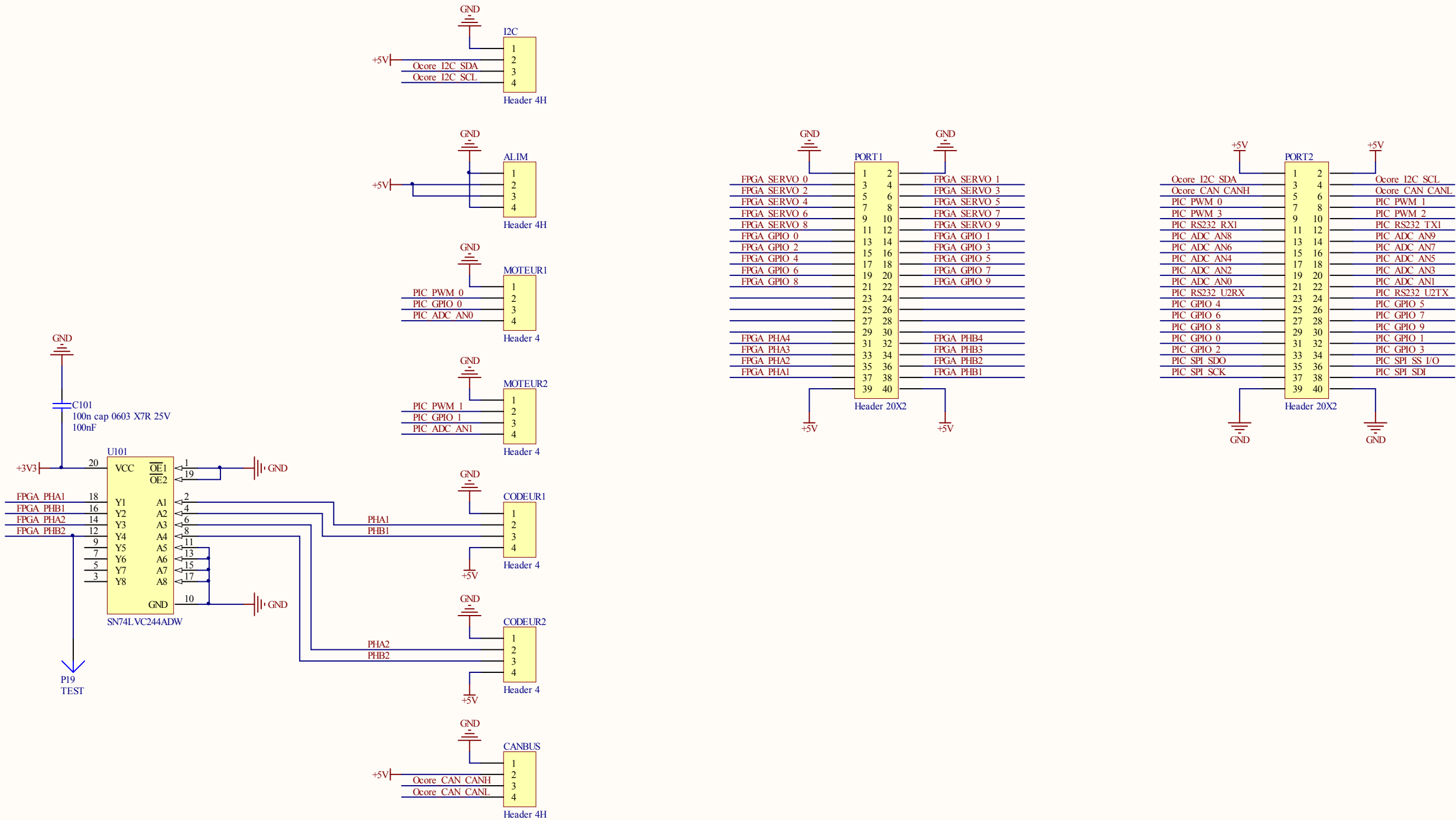
D

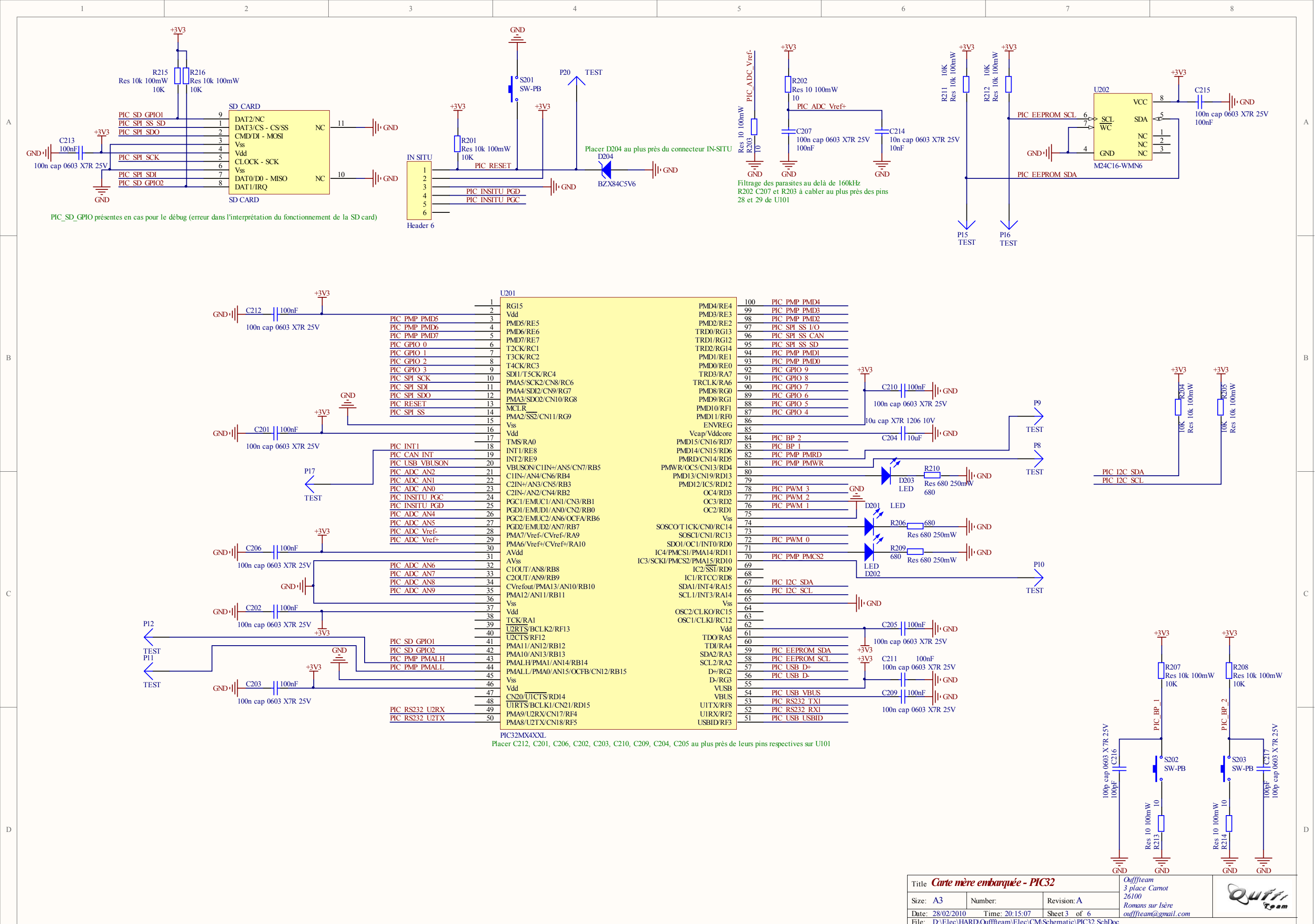
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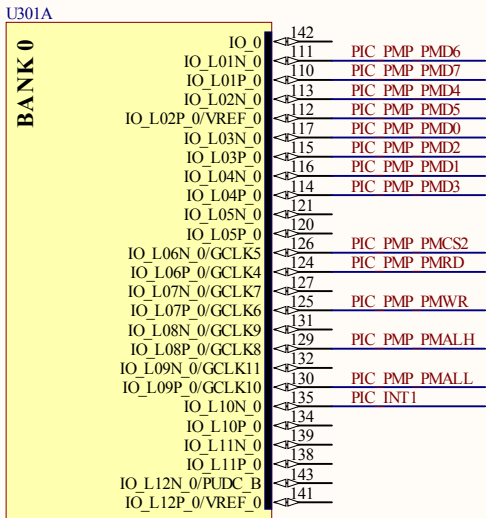
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C

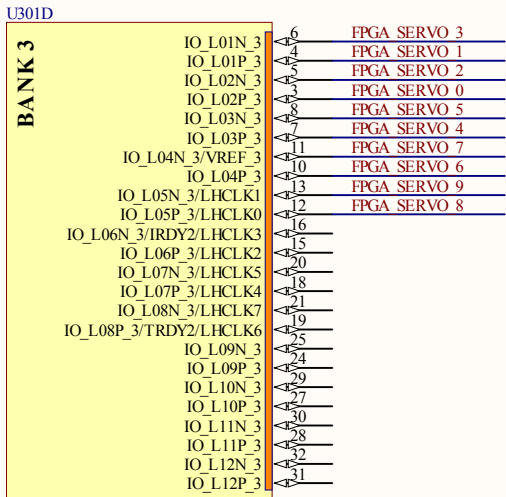
D



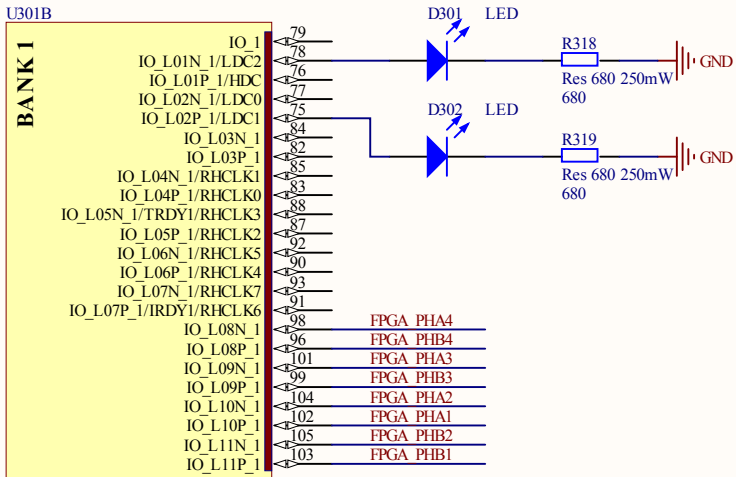
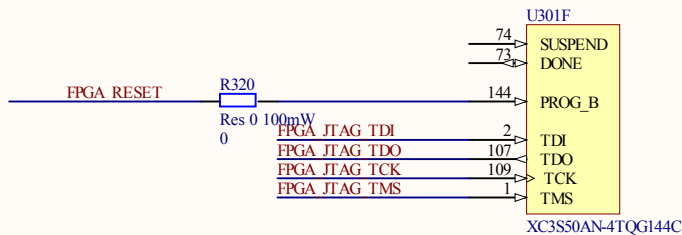
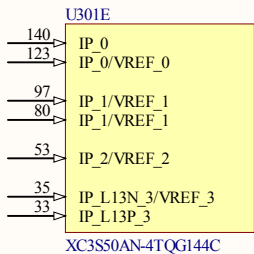
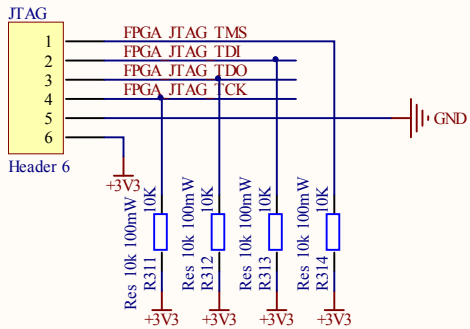




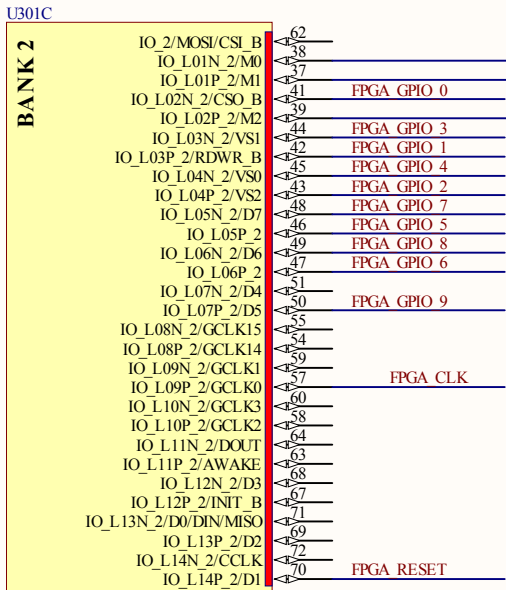
XC3S50AN-4TQG144C



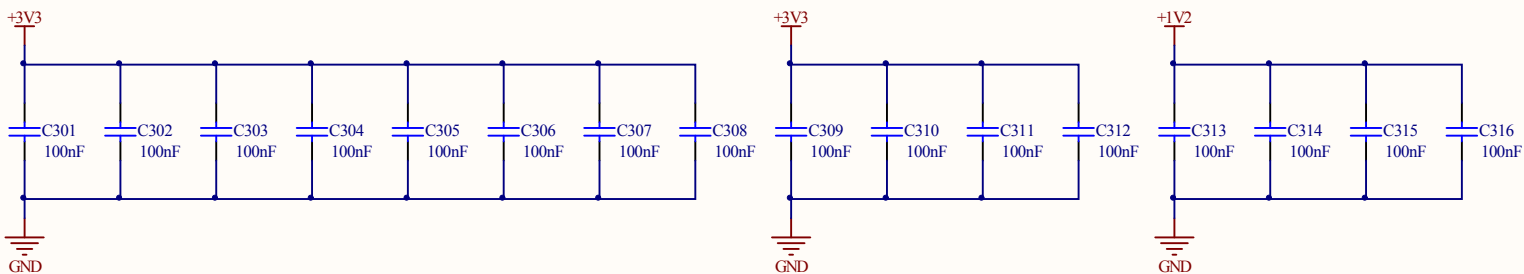
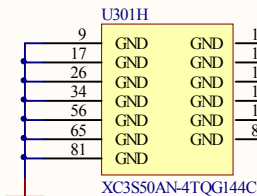
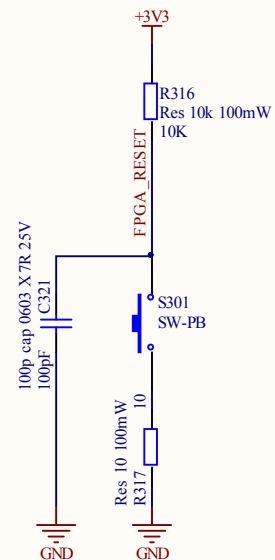
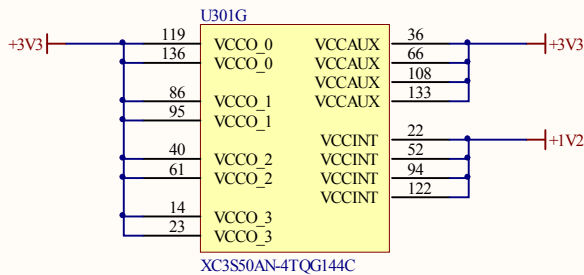
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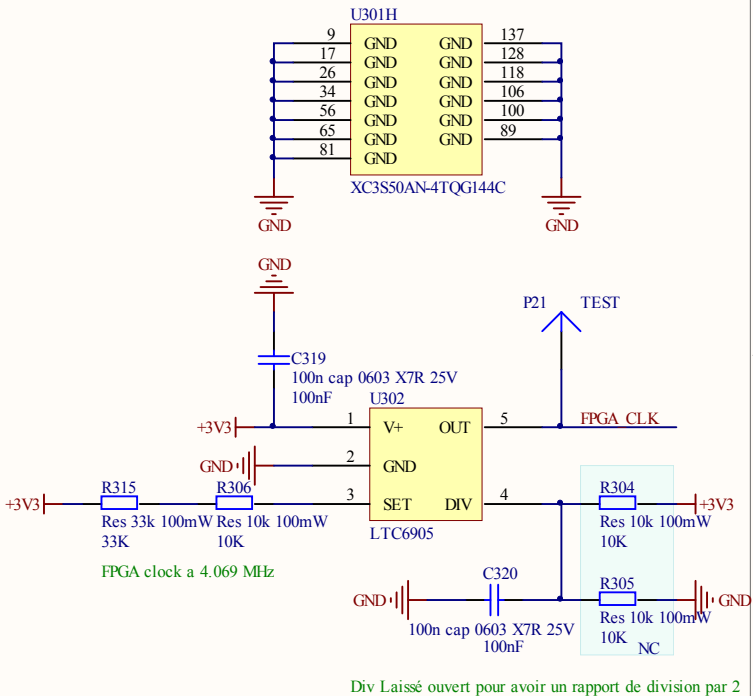
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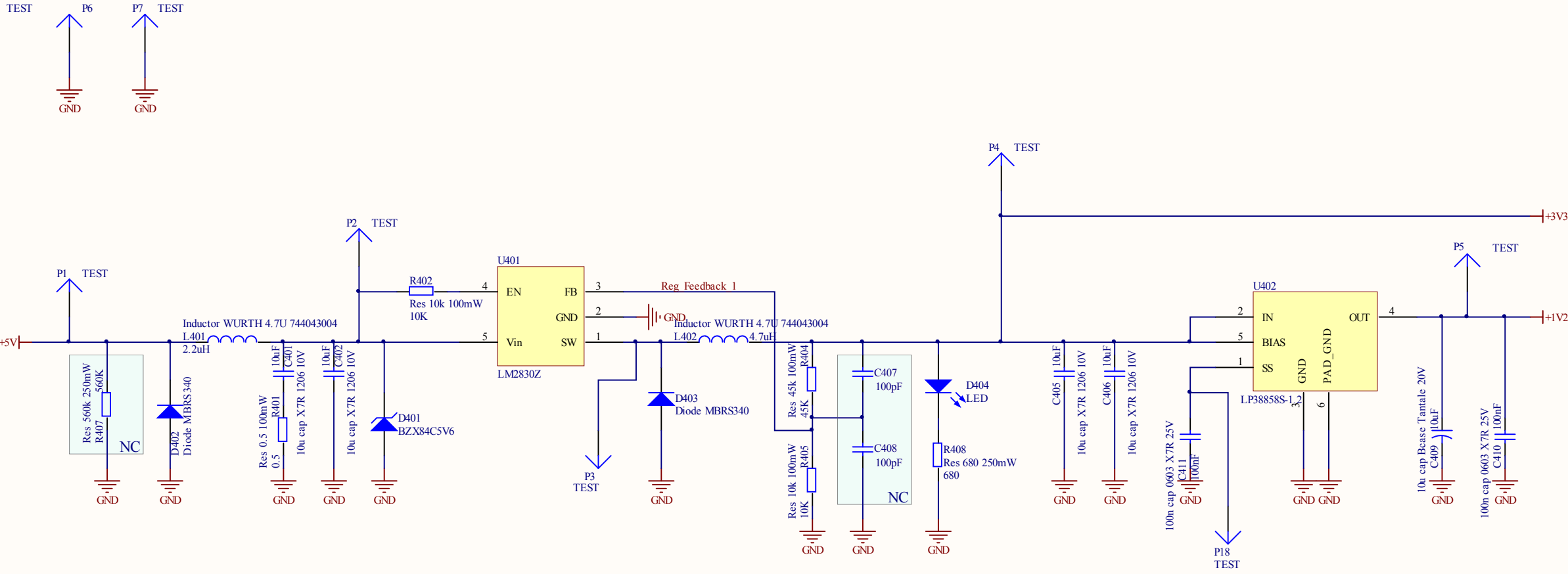


Condensateurs de découplage: 100n cap 0603 X7R 25V



Div Laisse ouvert pour avoir un rapport de division par 2

Title <i>Carte mère embarquée - CPLD</i>			Ouffteam 3 place Camot 26100 Romans sur Isère ouffteam@gmail.com	
Size: A3	Number:	Revision: A		
Date: 28/02/2010	Time: 20:15:07	Sheet 4 of 6		
File: D:\Elec\HARD\Ouffteam\Elec\CM\Schematic\FPGA.SchDoc				



Filtrage LC, coupure à 34 kHz -
Router les boucles de fort courant les plus courtes possibles, utiliser des plans pour router les nets à 'fort courant'
Protéger la net Reg_Feedback_1 des parasites

R501 permet de définir la pente des fronts (valeur précise reste à définir)

C503 active le Buffer de bus I2 en 20us