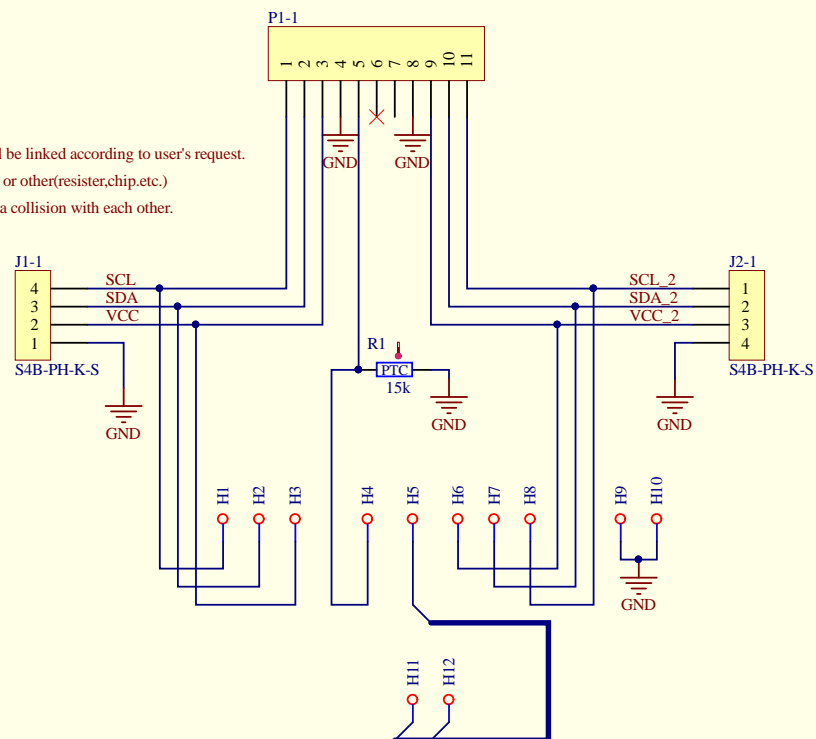



J1(SDA)-<->P1(5) will be linked according to user's request.
 will be linked by line or other(resister,chip.etc.)
 The reason is to avoid a collision with each other.



Title				<i>External Board 1 Mode</i>		
Size: A4	Number:	Revision: 1.0.0		Drawn by: Anne Moller		
Date: 3/29/2016	Time:10:03:02 PM	Sheet 1 of 1				
File: D:\GitWork\Vapeix-Shield-PCB\External Board\External Board_1mode.SchDoc						