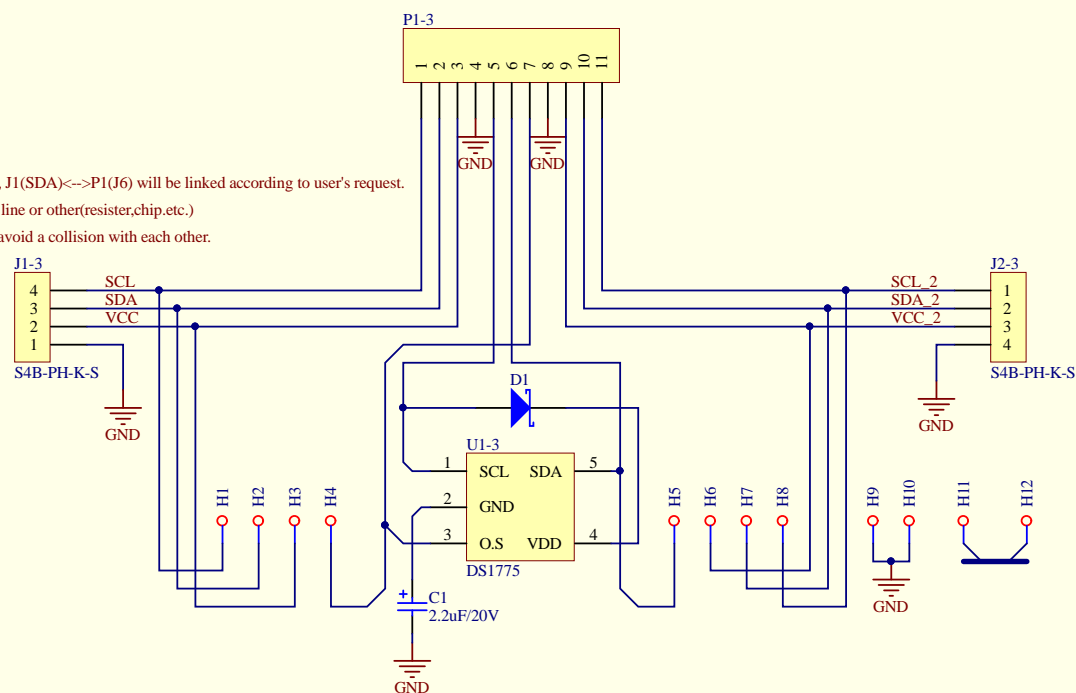



J1(SCL)<->P1(5), J1(SDA)<->P1(J6) will be linked according to user's request.
will be linked by line or other(resister,chip.etc.)
The reason is to avoid a collision with each other.



Title				<i>External Board 3 Mode</i>		
Size: A4	Number:	Revision: 1.0.0		Drawn by: Anne Moller		
Date: 3/29/2016	Time: 11:28 AM	Sheet 1 of 1				
File: D:\Vapeix_Shield_Work\External Board\PCB Project\External Board\External Board 3mode.SchDoc						

