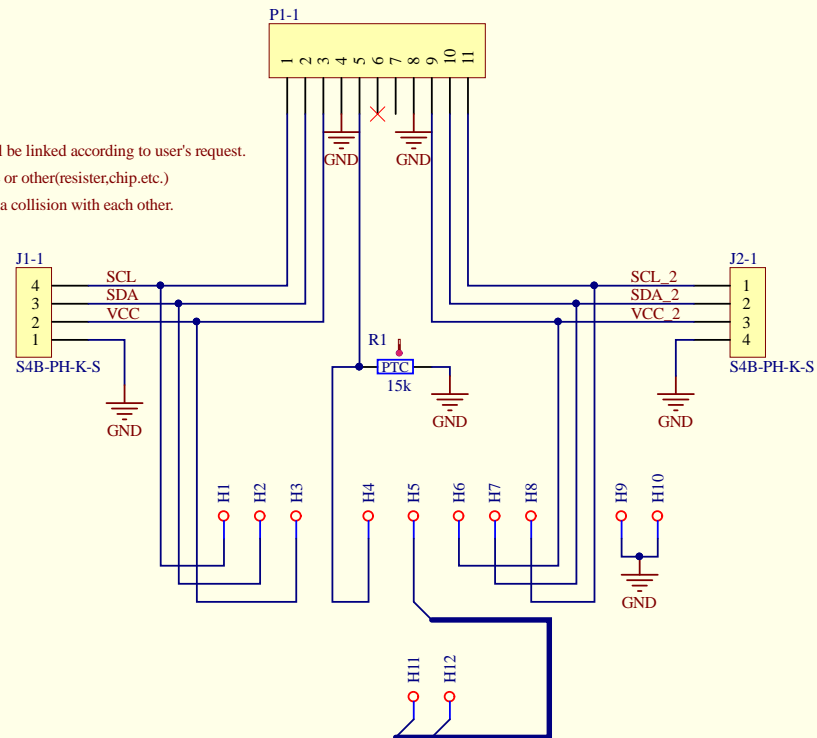



J1(SDA)<->P1(5) will be linked according to user's request.  
 will be linked by line or other(resister,chip,etc.)  
 The reason is to avoid a collision with each other.



Title				
External Board Mode 1 - Resister Sense				
Size: A4	Number:	Revision: 1.0.0	Drawn by: Anne Moller	
Date: 3/30/2016	Time: 2:45:17 AM	Sheet 1 of 1		
File: D:\GitWork\Vapeix-Shield-PCB\External Board\External Board_1mode.SchDoc				

