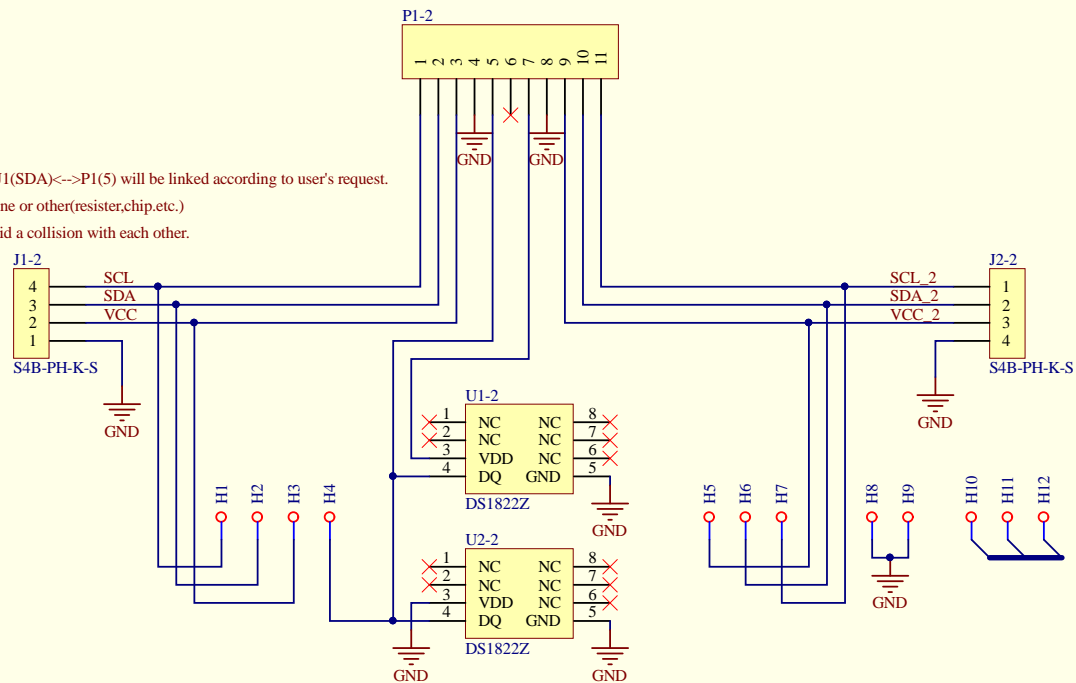



J1(SCL)<-->P1(7), J1(SDA)<-->P1(5) will be linked according to user's request.
 will be linked by line or other(resister,chip.etc.)
 The reason is to avoid a collision with each other.



Title				
External Board Mode 2 - 1 Wire				
Size: A4	Number:	Revision: 1.0.0	Drawn by: Anne Moller	
Date: 3/30/2016	Time: 2:45:29 AM	Sheet 1 of 1		
File: D:\GitWork\Vapeix-Shield-PCB\External Board\External Board_2mode.SchDoc				