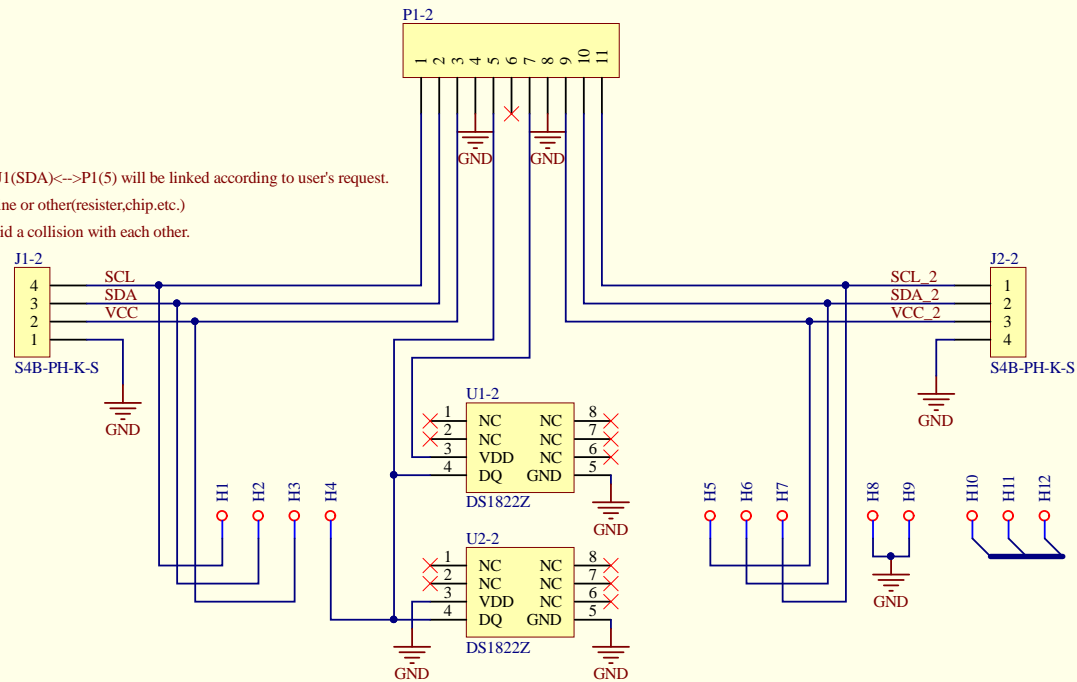



J1(SCL)<-->P1(7), J1(SDA)<-->P1(5) will be linked according to user's request.
 will be linked by line or other(resister,chip,etc.)
 The reason is to avoid a collision with each other.



Title				<i>External Board 2 Mode</i>		
Size: A4	Number:	Revision: 1.0.0		Drawn by: <i>Anne Moller</i>		
Date: 3/29/2016	Time: 9:57:05 PM	Sheet	1 of 1			
File: D:\GitWork\Vapeix-Shield-PCB\External Board\External Board_2mode.SchDoc						

