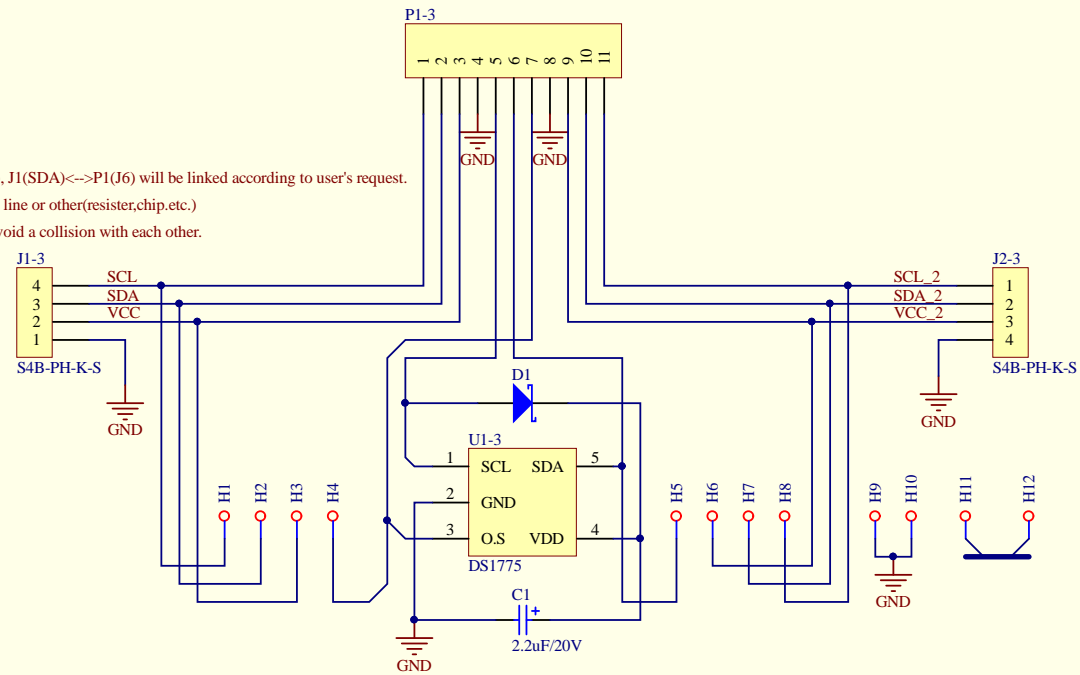



J1(SCL)<-->P1(5), J1(SDA)<-->P1(J6) will be linked according to user's request.
will be linked by line or other(resister,chip,etc.)
The reason is to avoid a collision with each other.



Title				<i>External Board Mode 3 - I2C</i>					
Size: A4		Number:		Revision: 1.0.0		Drawn by: Anne Moller			
Date: 3/30/2016		Time: 2:45:38 AM		Sheet 1 of 1					
File: D:\GitWork\Vapeix-Shield-PCB\External Board\External Board_3mode.SchDoc									