

# 1. Description

## 1.1. Project

Project Name	LCD_PASS_CHECK
Board Name	NUCLEO-WBA52CG
Generated with:	STM32CubeMX 6.12.0
Date	10/01/2024

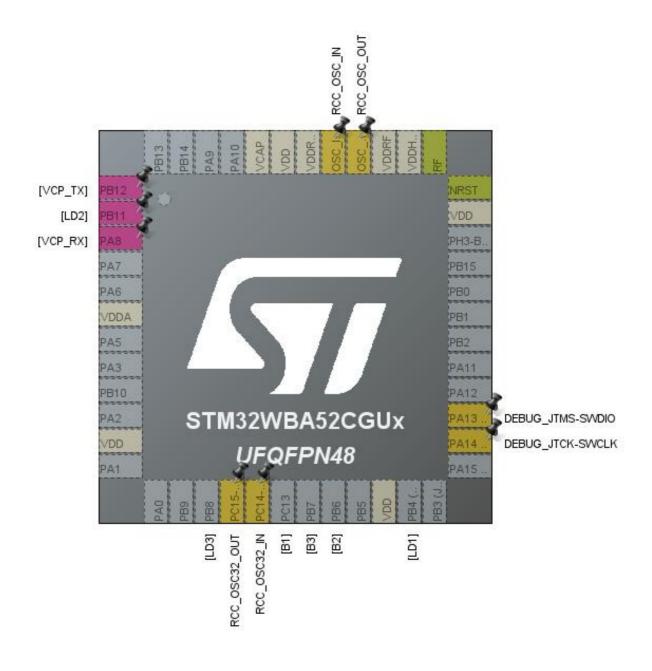
## 1.2. MCU

MCU Series	STM32WBA
MCU Line	STM32WBAx2
MCU name	STM32WBA52CGUx
MCU Package	UFQFPN48
MCU Pin number	49

## 1.3. Core(s) information

Core(s)	ARM Cortex-M33

## 2. Pinout Configuration

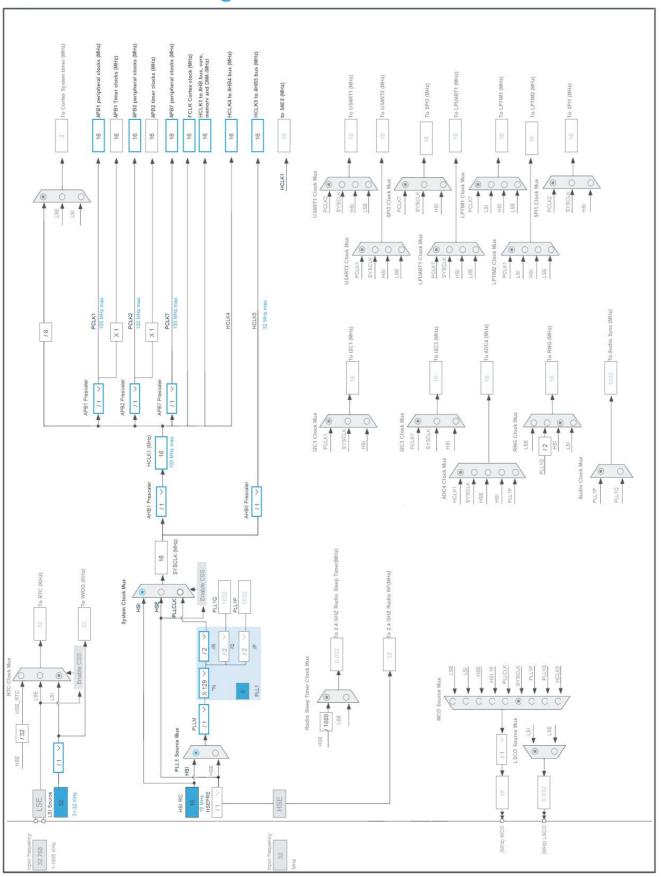


# 3. Pins Configuration

Pin Number UFQFPN48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PB12	I/O		
2	PB11	I/O		
3	PA8	I/O		
6	VDDA	Power		
11	VDD	Power		
16	PC15-OSC32_OUT *	I/O	RCC_OSC32_OUT	RCC_OSC32_OUT
17	PC14-OSC32_IN *	I/O	RCC_OSC32_IN	RCC_OSC32_IN
22	VDD	Power		
26	PA14 (JTCK/SWCLK) *	I/O	DEBUG_JTCK-SWCLK	DEBUG_JTCK-SWCLK
27	PA13 (JTMS/SWDIO) *	I/O	DEBUG_JTMS-SWDIO	DEBUG_JTMS-SWDIO
35	VDD	Power		
36	NRST	Reset		
37	RF	MonolO		
38	VDDHPA	Power		
39	VDDRF	Power		
40	OSC_OUT *	I/O	RCC_OSC_OUT	RCC_OSC_OUT
41	OSC_IN *	I/O	RCC_OSC_IN	RCC_OSC_IN
42	VDDRFPA	Power		
43	VDD	Power		
44	VCAP	Power		
49	VSS (exposed pad)	Power		

<sup>\*</sup> The pin is affected with a peripheral function but no peripheral mode is activated

# 4. Clock Tree Configuration



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# 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32WBA
Line	STM32WBAx2
мси	STM32WBA52CGUx
Datasheet	DS000000_Rev1

### 1.2. Parameter Selection

Temperature	25
Vdd	3.0

## 1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

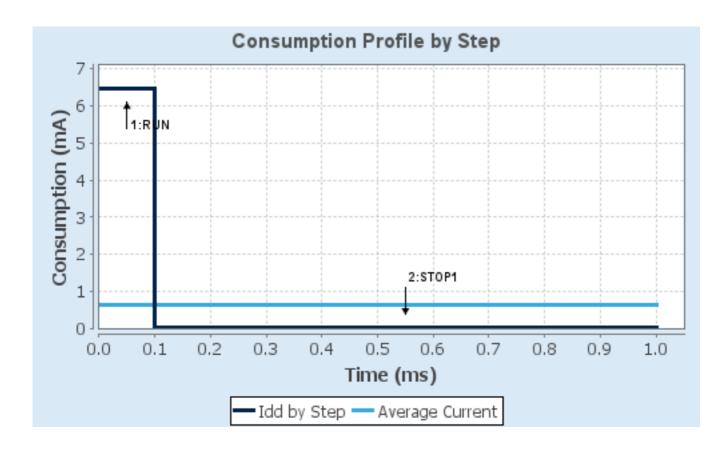
## 1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP1
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoScale/SMPS
Fetch Type	FLASH/Cache 2Ways	FLASH
CPU Frequency	100 MHz	0 Hz
Clock Configuration	HSE BYP PLL	ALL_CLOCKS_OFF PWR
	ALL RAM RETENTION	
Clock Source Frequency	32 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	6.46 mA	6.48 µA
Duration	0.1 ms	0.9 ms
DMIPS	125.0	0.0
Ta Max	104.48	105
Category	In DS Table	In DS Table

## 1.5. Results

Sequence Time	1 ms	Average Current	651.83 μA
Battery Life	1 month, 14 days,	Average DMIPS	12.5 DMIPS
	6 hours		

## 1.6. Chart



# 2. Software Project

## 2.1. Project Settings

Name	Value
Project Name	LCD_PASS_CHECK
Project Folder	D:\Micro_Controller\STM32\Projects\LCD_Projects\LCD_PASS_CHECK
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_WBA V1.4.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	SystemPower_Config	PWR
4	MX_ICACHE_Init	ICACHE

## 3. Peripherals and Middlewares Configuration

#### 3.1. ICACHE

Mode: 1-way (direct mapped cache)

3.2. MEMORYMAP mode: Activated

#### 3.3. NUCLEO-WBA52CG

mode: Human Machine Interface 3.3.1. Human Machine Interface:

#### Led:

USER LED BLUE (LD1) true \*
USER LED GREEN (LD2) true \*
USER LED RED (LD3) true \*

**Button:** 

USER B1 Mode EXTI \*
USER B2 Mode EXTI \*
USER B3 Mode EXTI \*

VCOM:

Virtual Com Port true \*

**Demonstration code:** 

Generate demonstration code Disabled

#### 3.4. NUCLEO-WBA52CG

mode: Human Machine Interface 3.4.1. Human Machine Interface:

#### Led:

USER LED BLUE (LD1) true \*
USER LED GREEN (LD2) true \*
USER LED RED (LD3) true \*

**Button:** 

USER B1 Mode EXTI \*

USER B2

Mode EXTI \*

USER B3 Mode EXTI \*

VCOM:

Virtual Com Port true \*

**Demonstration code:** 

Generate demonstration code Disabled

3.5. PWR

mode: Privilege attributes

3.5.1. PWR Privilege:

Privilege PWR:

PWR Privilege Disable

3.6. RCC

3.6.1. Parameter Settings:

**System Parameters:** 

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value (ms) 100
LSE Startup Timout Value (ms) 5000
HSE Trimming Value 0

LSE Trimming Value Current Source Resistance R

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1 \*

3.7. SYS

Timebase Source: SysTick

User modified value	

# 4. System Configuration

## 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
Single Mapped Signals	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	RCC_OSC32_OUT
	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	RCC_OSC32_IN
	PA14 (JTCK/SWC LK)	DEBUG_JTCK- SWCLK	n/a	n/a	n/a	DEBUG_JTCK-SWCLK
	PA13 (JTMS/SWDI O)	DEBUG_JTMS- SWDIO	n/a	n/a	n/a	DEBUG_JTMS-SWDIO
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	RCC_OSC_OUT
	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	RCC_OSC_IN

### 4.2. GPDMA1

### 4.3. LINKEDLIST

## 4.4. NVIC configuration

## 4.4.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	15	0
EXTI Line6 interrupt	true	0	0
EXTI Line7 interrupt	true	0	0
EXTI Line13 interrupt	true	0	0
Flash non-secure global interrupt	unused		
RCC non-secure global interrupt	unused		
FPU global interrupt	unused		
Instruction cache global interrupt	unused		
PWR global WKUP pin interrupt	unused		
RCC audio synchronization interrupt	unused		

## 4.4.2. NVIC Code generation

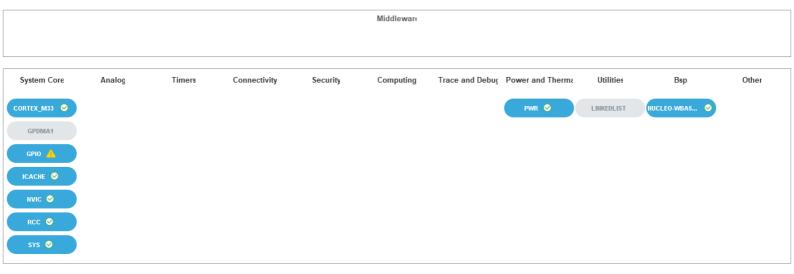
Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maakabla interrunt	false	true	false
Non maskable interrupt	laise	liue	laise
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI Line6 interrupt	false	true	true
EXTI Line7 interrupt	false	true	true
EXTI Line13 interrupt	false	true	true

* User modified value	

# 5. System Views

5.1. Category view

5.1.1. Current



## 6. Docs & Resources

Type Link

IBIS models https://www.st.com/resource/en/ibis\_model/stm32wba-ibis.zip

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

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