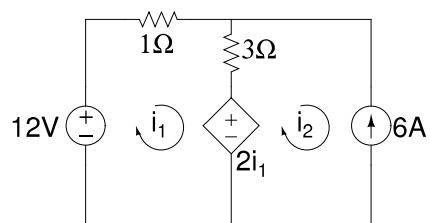


2. (25 pts) The two parts in this problem are unrelated.

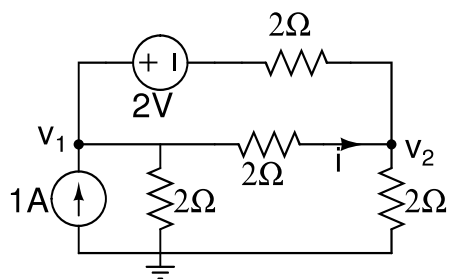
(a) Find the loop currents i_1 and i_2 in the following circuit.



$$i_1 = \underline{\hspace{2cm}}$$

$$i_2 = \underline{\hspace{2cm}}$$

(b) Find the node voltages v_1 , v_2 and the current i in the following circuit.

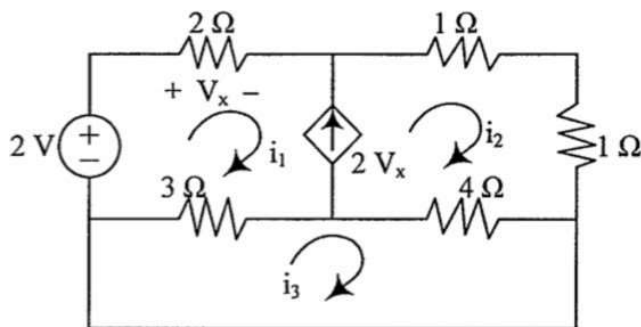


$$v_1 = \underline{\hspace{2cm}}$$

$$v_2 = \underline{\hspace{2cm}}$$

$$i = \underline{\hspace{2cm}}$$

4. (15 points) For the following circuit:



(a) (2 pts) What is V_x in terms of loop current i_1 ?

$$V_x = \underline{\hspace{2cm}}$$

(b) (3 pts) What is loop current i_2 in terms of loop current i_1 ?

$$i_2 = \underline{\hspace{2cm}}$$

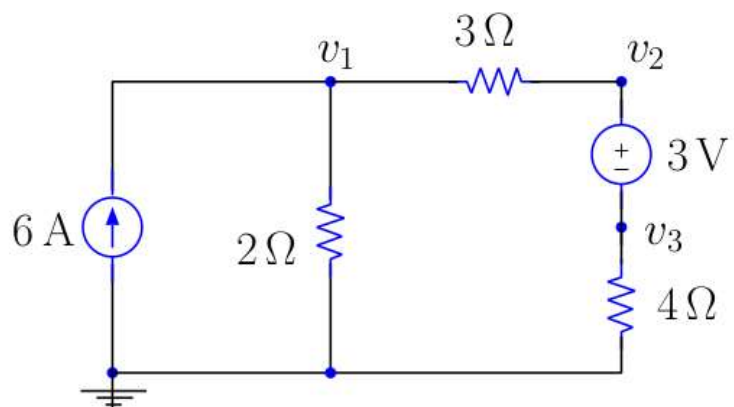
(c) (5 pts) Write the KVL equation for loop 3 in terms of i_1 , i_2 and i_3 .

equation: $\underline{\hspace{3cm}}$

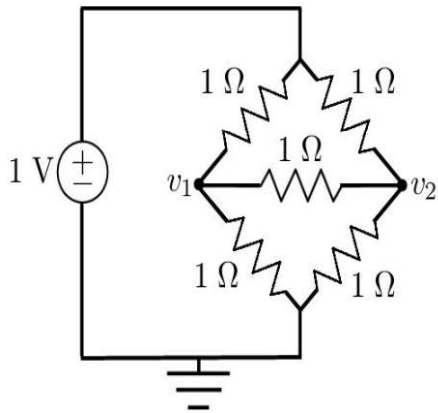
(d) (5 pts) Write one additional KVL equation for the circuit in terms of i_1 , i_2 and i_3 , which makes it possible to solve for these three variables. You are not required to solve the system.

equation: $\underline{\hspace{3cm}}$

Find v_1 , v_2 , v_3 .



6. (10 pts) Write, *but do not solve*, two node equations in order to solve for v_1 and v_2 . Fill in your final answer in the space provided, but be sure to show your work.

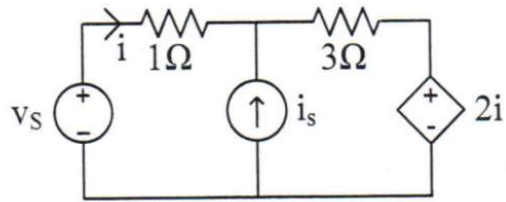


Answer:

$$(\quad)\mathbf{v}_1 + (\quad)\mathbf{v}_2 = (\quad)$$

$$(\quad)\mathbf{v}_1 + (\quad)\mathbf{v}_2 = (\quad)$$

The current i in the following circuit can be expressed as $i = K_1 v_s + K_2 i_s$. Find the values of K_1 and K_2 if $v_s = 12\text{V}$, $i_s = 6\text{A}$.



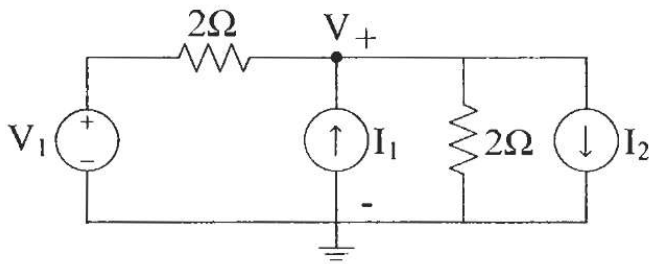
$$K_1 = \underline{\hspace{2cm}}$$

$$K_2 = \underline{\hspace{2cm}}$$

In the following circuit, using the principle of linearity and superposition, we can write the node voltage V as

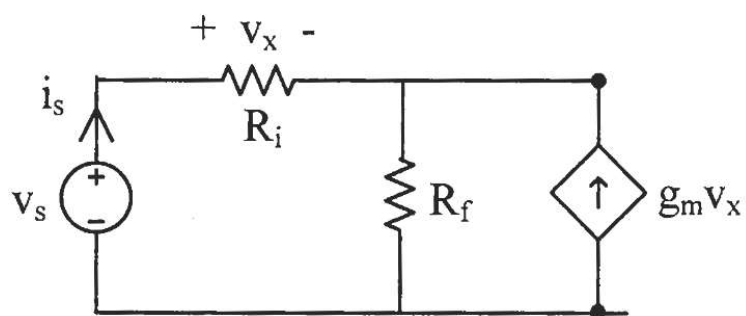
$$V = AV_1 + BI_1 + CI_2$$

Use source suppression to find the constants A , B , and C .



$A =$ _____
 $B =$ _____
 $C =$ _____

) Use the loop method to find the current i_s . Assume that v_s , R_i , R_f , and g_m are known.



Use the node method to find v_o as a function of the independent sources v_s and i_o . At terminals a-b what is the equivalent resistance and the open circuit voltage.

