

Analog Signal Processing  
Thursday, September 26, 8:45-10pm

Exam I

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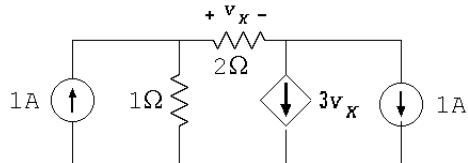
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Course: (circle one)	ECE210	ECE211		
Section to return exam: (circle one)	11AM	12PM	2PM	3PM

<p>Clearly PRINT your name in CAPITAL LETTERS.</p> <p>This is a closed book and closed notes exam.</p> <p>Calculators are not allowed.</p> <p>To get full credit, please SHOW all your work and simplify your answers.</p> <p>Write your final answers in the spaces provided.</p> <p>All answers should INCLUDE UNITS whenever appropriate.</p> <p>The exam is printed <b>double-sided</b>.</p>	<p><b>DO NOT write in these spaces.</b></p> <p>Problem 1 (30 points):_____</p> <p>Problem 2 (25 points):_____</p> <p>Problem 3 (25 points):_____</p> <p>Problem 4 (20 points):_____</p> <p>Total: (100 points):_____</p>
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1. (30 pts) The two parts of this problem are unrelated.

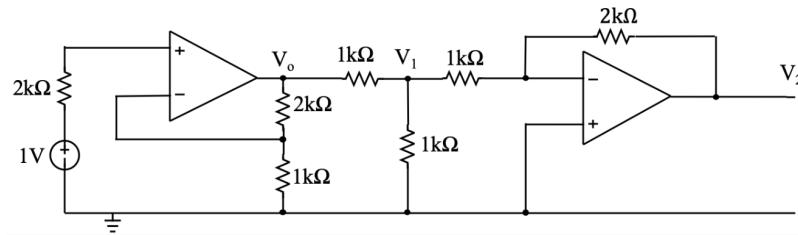
- (a) [15 pts] Consider the circuit below. Determine the voltage  $V_X$  and the absorbed power at the dependent source,  $P_{3V_X}$ .



$$V_X = \underline{\hspace{2cm}}$$

$$P_{3V_X} = \underline{\hspace{2cm}}$$

- (b) [15 pts] Determine the voltages  $V_0$ ,  $V_1$  and  $V_2$  in the following circuit, assuming linear operation and ideal op-amp conditions.

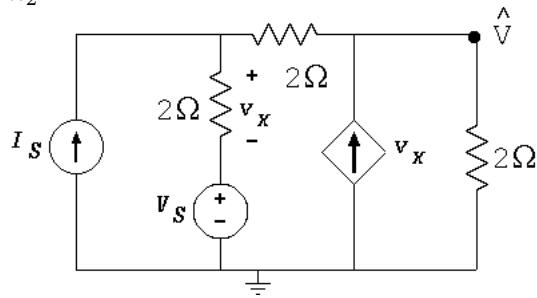


$$V_0 = \underline{\hspace{2cm}}$$

$$V_1 = \underline{\hspace{2cm}}$$

$$V_2 = \underline{\hspace{2cm}}$$

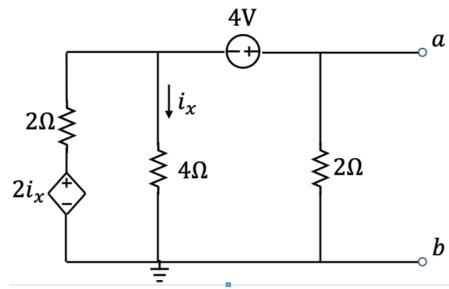
2. (25 pts) Consider the resistive circuit shown in the diagram below. Node voltage  $\hat{V}$  can be expressed as  $\hat{V} = k_1 V_S + k_2 I_S$ . Use superposition to obtain the value of  $k_1$  and the value of  $k_2$ .



$$k_1 = \underline{\hspace{2cm}}$$

$$k_2 = \underline{\hspace{2cm}}$$

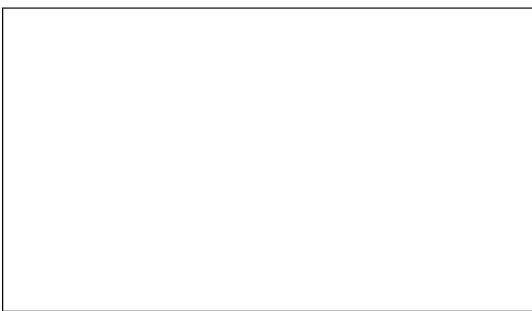
3. (25 pts) Consider the circuit below. Determine Thevenin's voltage,  $V_T$ , and Norton's current,  $I_N$ , between nodes  $a$  and  $b$ . Also draw the corresponding Thevenin equivalent circuit and Norton equivalent circuit.



$$V_T = \underline{\hspace{2cm}}$$

$$I_N = \underline{\hspace{2cm}}$$

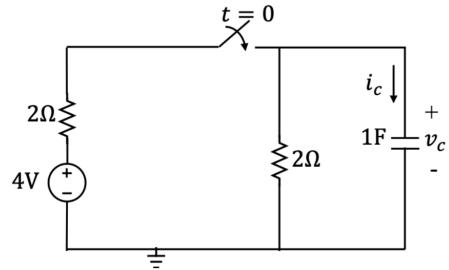
Thevenin equivalent circuit



Norton equivalent circuit



4. (20 pts) Consider the circuit below, assuming that  $v(0^-) = 1V$ .



- (a) [3 pts] Determine  $i_C(0^+)$  and  $v_C(0^+)$ . Explain.

$$i_C(0^+) = \underline{\hspace{2cm}}$$

$$v_C(0^+) = \underline{\hspace{2cm}}$$

- (b) [2 pts] For  $t > 0$ , determine time constant . Show your work.

$$\tau = \underline{\hspace{2cm}}$$

- (c) [3 pts] Determine the steady state values of  $i_C$  and  $v_C$ . Explain.

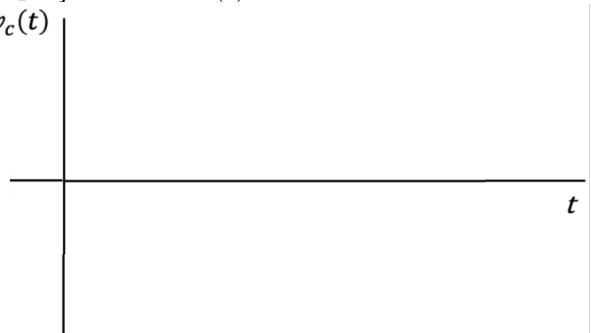
$$i_C(\infty) = \underline{\hspace{2cm}}$$

$$v_C(\infty) = \underline{\hspace{2cm}}$$

(d) [8 pts] Express  $v_C(t)$  for  $t > 0$ .

$$v_C(t) = \underline{\hspace{2cm}}$$

(e) [4 pts] Sketch  $v_C(t)$  for  $t > 0$ . Label numerical values on the axes.



You may use this sheet for additional calculations but **do not** separate this sheet from the rest of the exam.

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