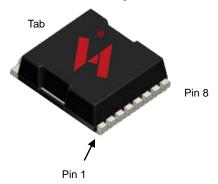


N-Channel Enhancement Mode MOSFET

Feature

- 40V/600A
 - $R_{DS(ON)}=0.45 \text{ m}\Omega(\text{typ.})\text{@Vgs} = 10\text{V}$
 - $R_{DS(ON)}=0.61 \text{ m}\Omega(typ.)@V_{GS}=4.5V$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen-Free Devices Available (RoHS Compliant)

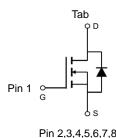
Pin Description



TOLL

Applications

- Switching application
- Power management for inverter systems
- Battery management



N-Channel MOSFET

Ordering and Marking Information



Package Code

TA:TOLL

Date Code

XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit			
Common Rat	Common Ratings (Tc=25°C Unless Otherwise Noted)					
VDSS	Drain-Source Voltage		40	V		
Vgss	Gate-Source Voltage		±20	V		
TJ	Junction Temperature Range		-55 to 175	°C		
Тѕтс	Storage Temperature Range		-55 to 175	°C		
ls	Source Current-Continuous(Body Diode)	600	А			
Mounted on I	Mounted on Large Heat Sink					
Ідм	Pulsed Drain Current *	Tc=25°C	2400	А		
1-	Tc=25°C		600	А		
lσ	Continuous Drain Current	Tc=100°C	424	А		
Б	Maximum Power Dissipation Tc=25°C Tc=100°C		428	W		
P _D			214	W		
R₀JC	Thermal Resistance, Junction-to-Case		0.35	°C/W		
R _{eJA}	Thermal Resistance, Junction-to-Ambient **		45	°C/W		
Eas	SinglePulsed-Avalanche Energy *** L=1mH		2300	mJ		

Note:

- * Repetitive rating; pulse width limited by max.junction temperature.
- ** Surface mounted on 1in2 FR-4 board.
- *** Limited by TJmax , starting TJ=25°C, L = 1mH, Rg= 25Ω , Vgs =10V.

Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Cymphol	Devementes	Took Conditions	HYG006N04LS1TA			l lesia	
Symbol	pol Parameter Test Conditions		Min	Тур.	Max	Unit	
Static Cha	Static Characteristics						
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} = 250μA	40	-	-	V	
I Built to Our and had a const		V _{DS} =40V,V _{GS} =0V	-	-	1	μΑ	
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μΑ	
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} = 250µA	1.3	1.8	2.4	V	
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA	
Dro/out	Drain-Source On-State Resistance	V _{GS} = 10V,I _{DS} =80A	-	0.45	0.60	mO	
Rds(on)	Dialii-Source Oii-State Resistance	V _{GS} = 4.5V,I _{DS} =80A	-	0.61	0.75	mΩ	
Diode Characteristics							
VsD	Diode Forward Voltage	Isp=80A,Vgs=0V	-	0.78	1.2	V	
trr	Reverse Recovery Time	les 404 dles/dt 4004/up	-	80	-	ns	
Qrr	Reverse Recovery Charge	- Isb=40A,dIsb/dt=100A/μs	-	148	-	nC	

HYG006N04LS1TA



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Cumbal	Donomotor	Test Conditions	HYG006N04LS1TA			11:4
Symbol	Parameter		Min	Тур.	Max	Unit
Dynamic (Dynamic Characteristics					
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	3.2	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	15400	-	
Coss	Output Capacitance	V _{DS} = 25V,	-	3200	-	pF
Crss	Reverse Transfer Capacitance	Frequency=300kHz	-	115	-	
td(ON)	Turn-on Delay Time		-	28	-	
Tr	Turn-on Rise Time	$V_{DD}=20V,R_{G}=2.5\Omega,$	-	104	-	
td(OFF)	Turn-off Delay Time	Ips= 80A,Vgs= 10V	-	125	-	ns
Tf	Turn-off Fall Time		-	114	-	
Gate Charge Characteristics						
Qg	Total Gate Charge (V _{GS} =10V)		-	230	-	
Qg	Total Gate Charge (V _{GS} =4.5V)	\/ -22\/ \/ -10\/ -90 \	-	110	-	~ C
Qgs	Gate-Source Charge	$ V_{DS} = 32V$, $V_{GS} = 10V$, $I_{DS} = 80A$	-	50	-	nC
Qgd	Gate-Drain Charge		-	38	-	

Note: *Pulse test, pulse width ≤ 300 us, duty cycle $\leq 2\%$



Typical Operating Characteristics

Figure 1: Power Dissipation

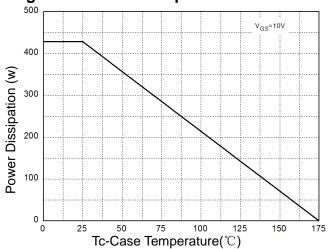


Figure 2: Drain Current

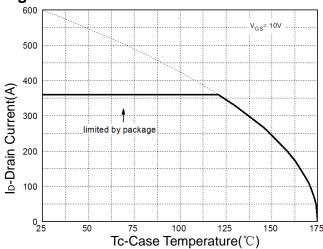


Figure 3: Safe Operation Area

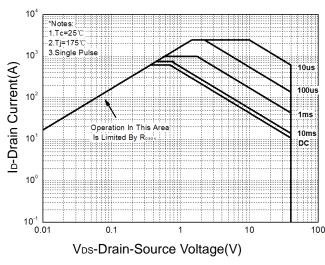


Figure 4: Thermal Transient Impedance

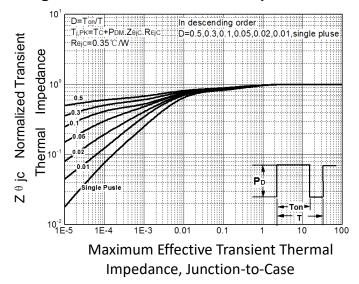


Figure 5: Output Characteristics

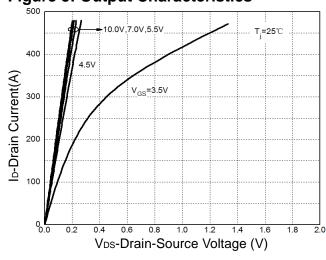
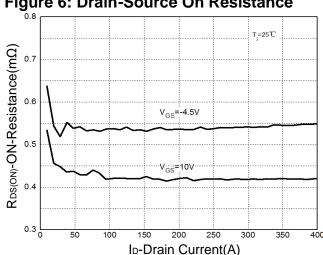


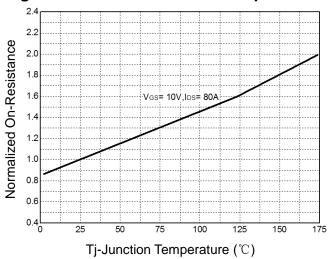
Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature



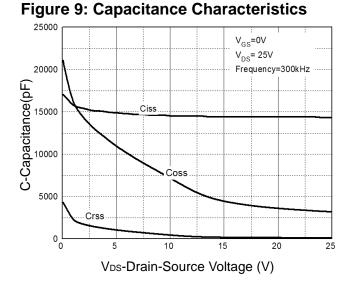
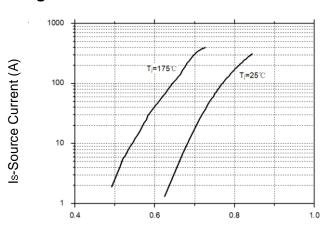
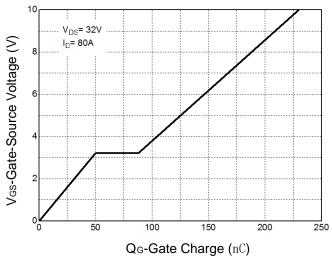


Figure 8: Source-Drain Diode Forward



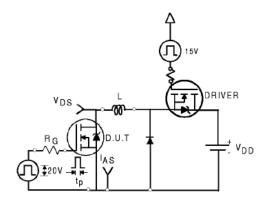
Vsp-Source-Drain Voltage(V)

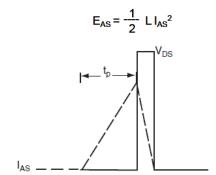
Figure 10: Gate Charge Characteristics



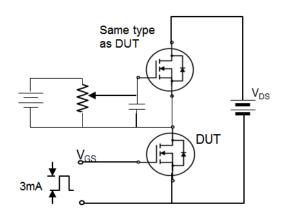


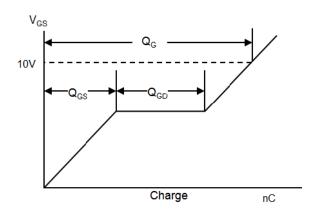
Avalanche Test Circuit



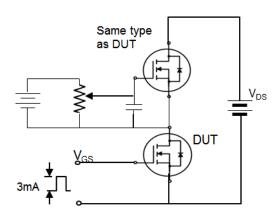


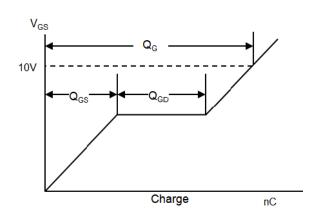
Switching Time Test Circuit





Gate Charge Test Circuit





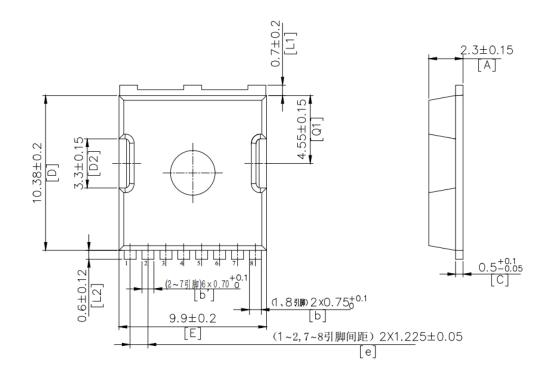


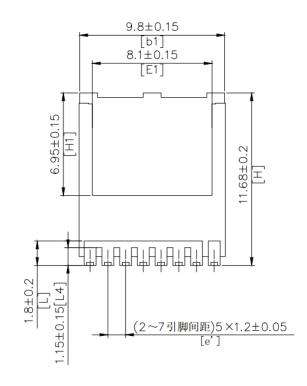
Device Per Unit

Package Type	Unit	Quantity
TOLL	Reel	1200

Package Information

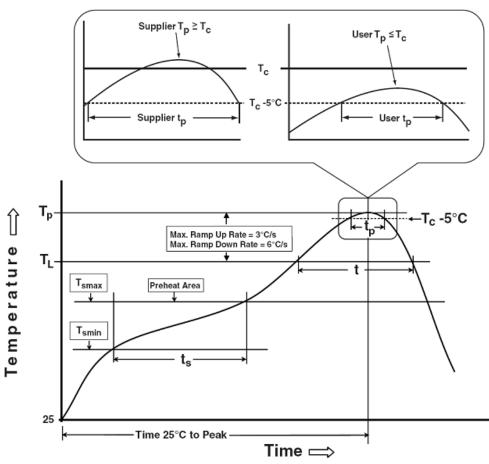
TOLL







Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak	100 °C	150 °C	
Temperature min (T _{smin})	150 °C	200 °C	
Temperature max (T _{smax})	60-120 seconds	60-120 seconds	
Time (Tsmin to Tsmax) (t _s)	00-120 3econds	00-120 Seconds	
Average ramp-up rate	3 °C/second max.	3°C/second max.	
(T _{smax} to T _P)	3 °C/second max.		
Liquidous temperature (T _L)	183 °C	217 °C	
Time at liquidous (t _L)	60-150 seconds	60-150 seconds	
Peak package body Temperature	See Classification Temp in table 1	SacClassification Tomain table 2	
(T _p)*	See Classification Temp in table 1	SeeClassification Tempin table 2	
Time (t _P)** within 5°C of the specified	20** seconds	30** seconds	
classification temperature (T _c)	20 Seconds	30 Seconds	
Average ramp-down rate (Tpto Tsmax)	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	
*Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.			

resolution for poark preme remperature (19) to defined as a cappiler minimum and a deer maximum

^{**} Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

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Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
PRECON	JESD-22, A113	85°C/85%RH,168Hrs
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168 /500/1000Hrs, Vgs100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	1000 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com Technical Support:Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: www.hymexa.com