## **Hardware Eng Internship JD (GROUPED)**

Join us on our Internship Program and grow within one of the most historical UK-forged technology companies!

Ready to dive into real-world projects, expand your industry connections, and build skills that will carry you onto the next stage of your career?

#### **Areas offering Hardware Intern Roles:**

We have 6 months internship opportunities across our hardware teams.

We work in small to medium-sized teams with most following modern Agile principles. Engineers share ideas and add to the ideas of others, document and present their work for discussion, review and support the efforts of others, whilst sharing their findings impartially and authoritatively.

## What you could be doing as a Hardware Intern:

- Working with design teams to develop IP that delivers high performance, power efficient products.
- Analyzing existing tools looking for enhancements/automation, alongside trialing new tools.
- Verifying IP using a wide range of methodologies constrained random simulation using testbenches written in SystemVerilog, running real applications on emulation or FPGA platforms, and using formal methods.
- Implementing Arm IP in silicon process nodes using design automation tools.
- Developing system solutions using ARM IP for different market domains ranging from mobile, IoT, data centres, etc.
- Writing specifications for Arm's IP products and systems analysing trade-offs between different options using software or hardware models.
- Providing Verification and Implementation support for systems through the lifecycle of the design right up to the delivery to the customer.

## We're looking for individuals who are:

Pursuing bachelors/masters in ECE, CSE, EEE.

# Qualities that will help your application stand out:

- Experience in at least one programming language.
- A real passion for computing and/or the semi-conductor industry that goes beyond your studies.
- Good attention to detail with the ability to problem solve and express ideas optimally.
- Team-spirit with an appreciation for the "We, not I" core belief.
- If you have an interest in computer architecture fundamentals, digital design concepts, CPU
  architecture and microarchitecture features (such as caches, MMU, SMP, coherency, CPU
  pipelines) this is a plus, but not essential.

• Any familiarity with a hardware description language like VHDL or Verilog/SystemVerilog is also helpful.

#### In Return:

Working on interesting new projects is exciting, but we also know how meaningful it is to receive support. That's why throughout your internship, you can expect regular feedback and development opportunities, social activities to connect with your peers, an end of summer celebration, plus the opportunity to receive future Graduate positions (subject to performance). #getreadytogrow

Our program is crafted to give you the best start possible and support your personal growth as well as professional development. Gain a competitive salary, supportive rewards package, alongside unparalleled learning and networking opportunities from the best in industry.