



BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT

Department Of Electronics And Telecommunication

III SEMESTER

DIGITAL SYSTEM AND DESIGN (I8EC34)

12 HOUR DIGITAL CLOCK SIMULATION USING MULTISIM

COURSE COORDINATOR

Dr. Seema Singh

PRESENTED BY

SVarsha - IBY20ET048

COMPONENTS USED

- 4510BD_10V IC (CMOS PRESETTABLE BCD UP/DOWN COUNTER)
- 4518BD_18V IC (DUAL BCD UP/DOWN COUNTER)
- AND, OR, NAND GATES
- 4 ITEM DIP SWITCH PACK
- PULSE VOLTAGE SOURCE
- CMOS SUPPLY
- DIGITAL HEX DISPLAY - 7 SEGMENT

HOURS

1

2

:

MINUTES

5

9

:

SECONDS

5

9



Counts
0 to 1



Counts
0 to 9



Counts
0 to 5



Counts
0 to 9



Counts
0 to 5



Counts
0 to 9

