

electrical [V(t)-Rilt) - Ldi-te 6=0

i(t) =](s) i(t) => I(s) 6(t) => I(s) 6(t) => I(s)

3.1) J2(S) = V(S) TLS2+(bL+JE) S+(bR+keke) FOR FAMPIAPUT VCS) = A lim W(t) = A, K, to 1 3.4) Gan = Kt = 1 output = SIDRE (WHAT)

bright = SIDRE (WHAT)